

FIG. 1A

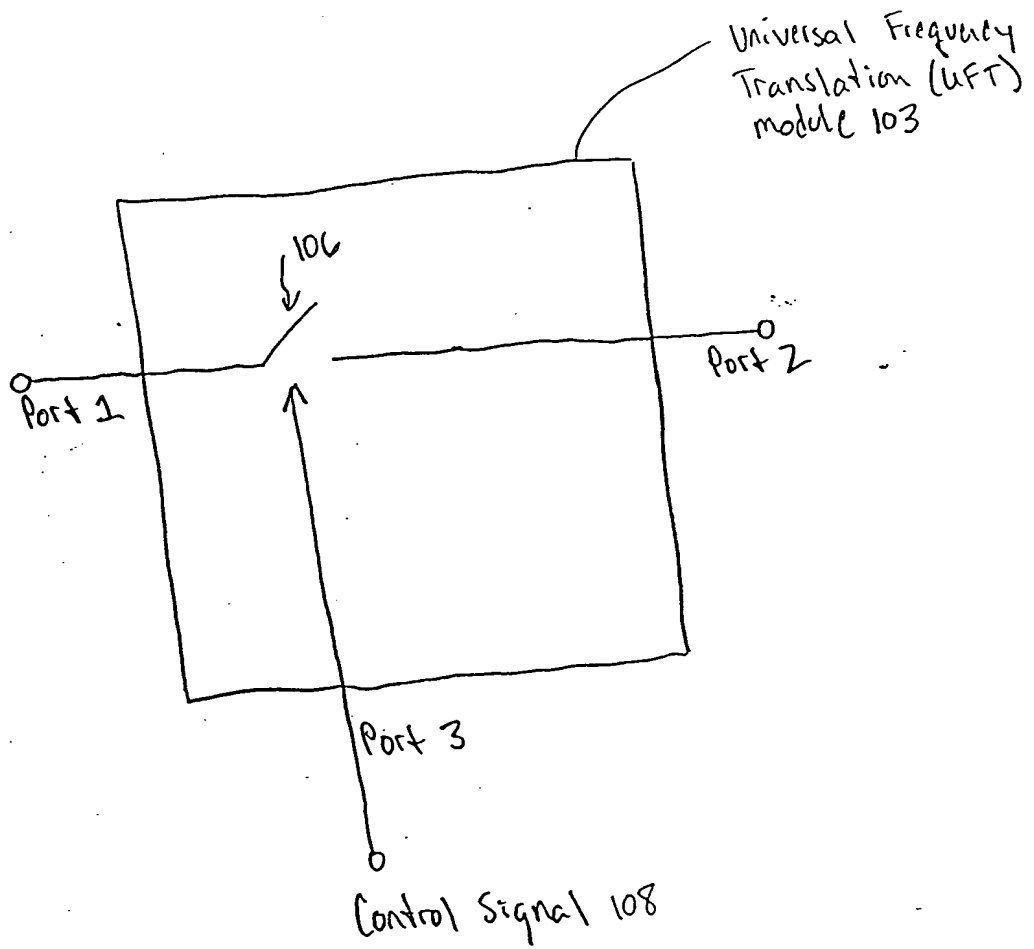


FIG. 1B

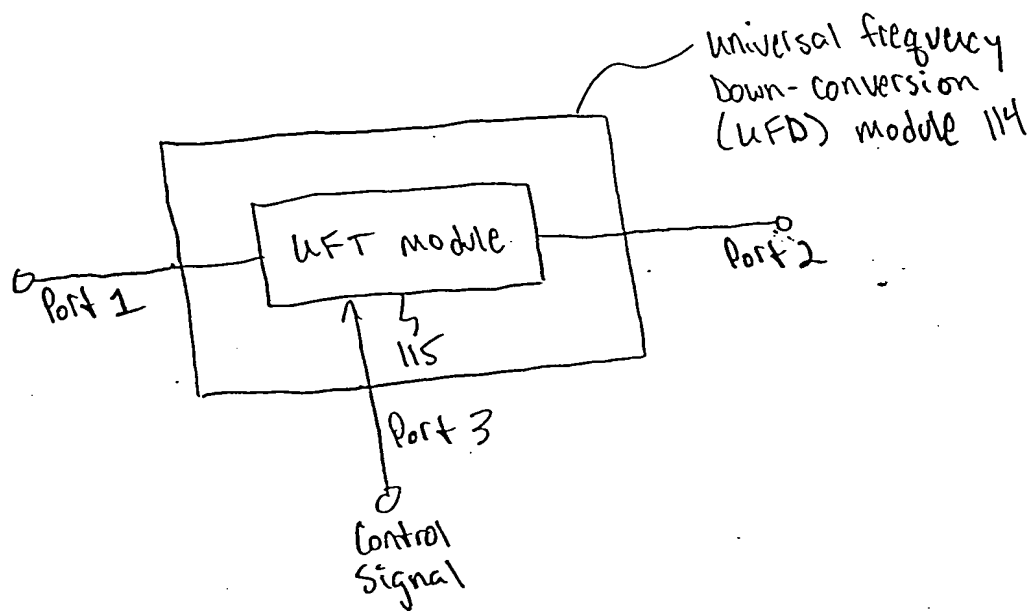


FIG. 1C

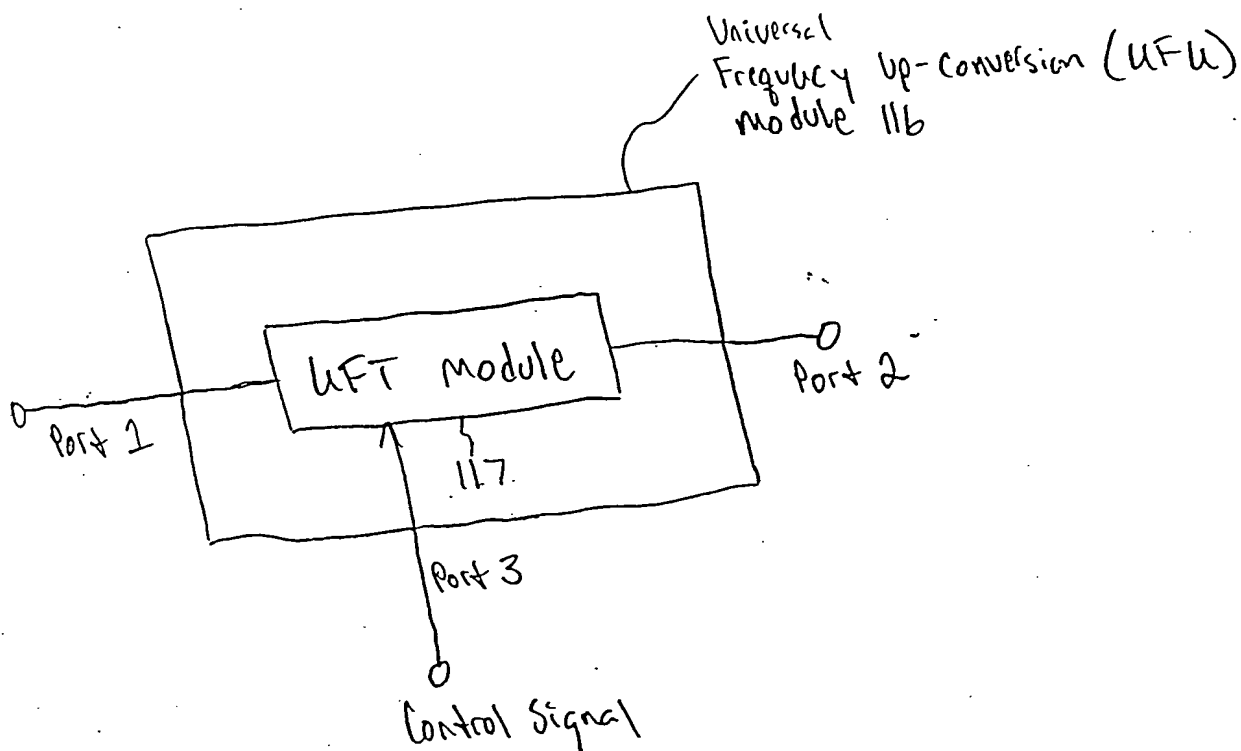


FIG. 1D

www.gettyimages.com

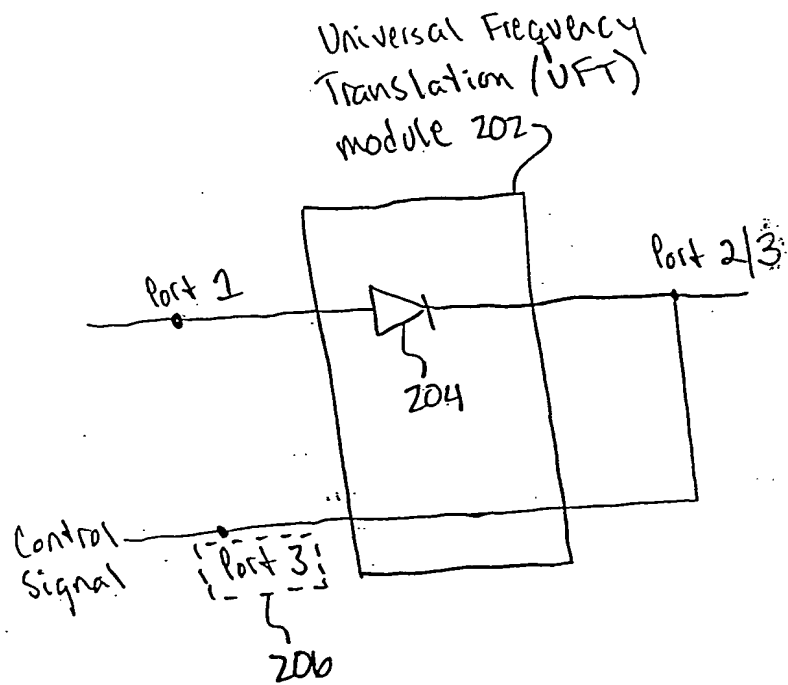


FIG. 2A

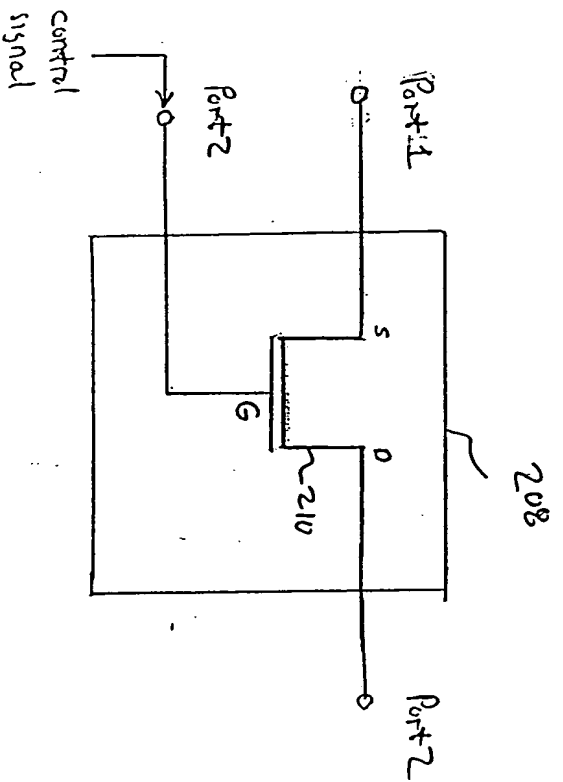


FIG. 2B

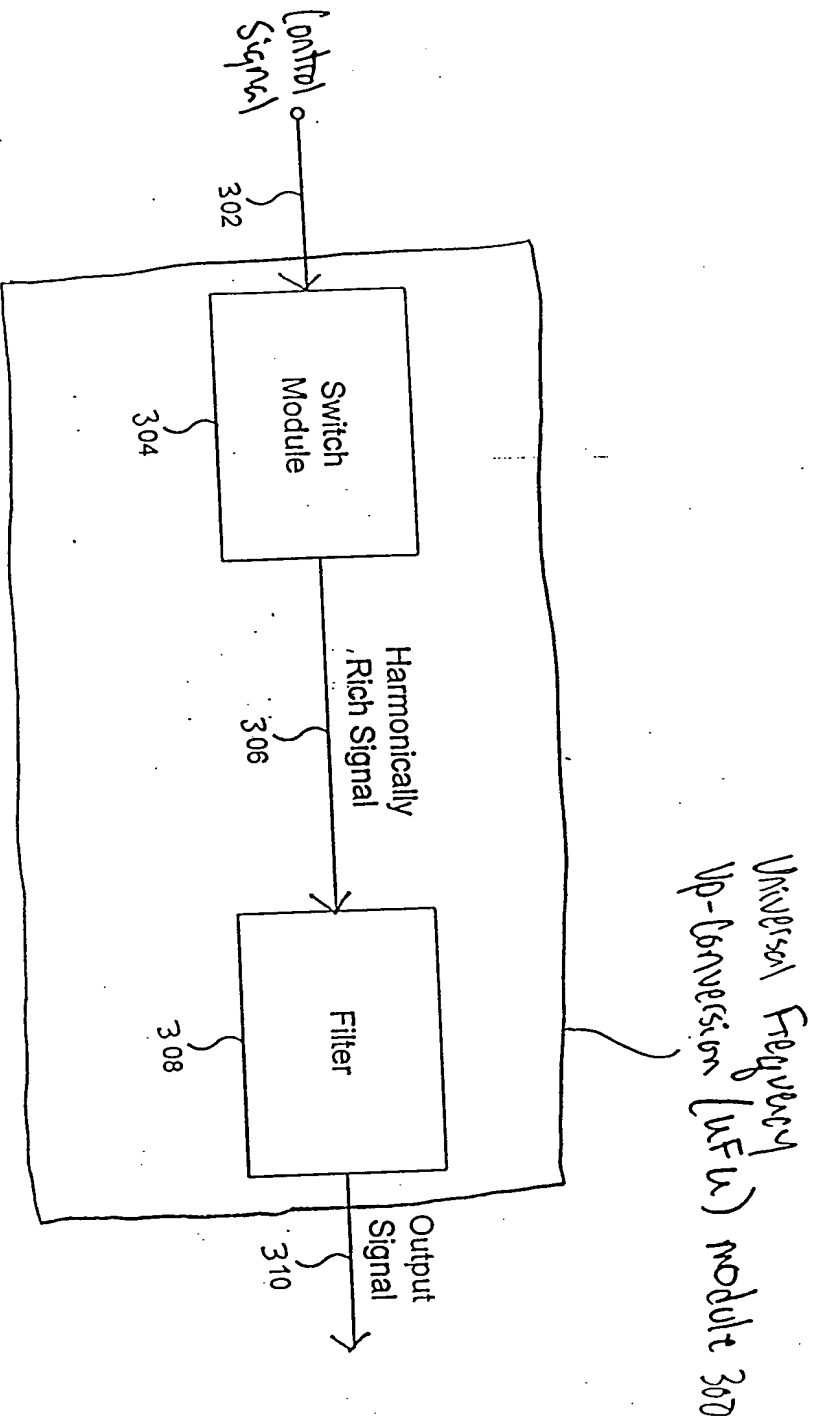
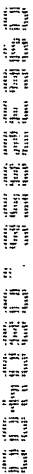


FIG. 3

W



9809-02.vsd/48

INFORMATION
SIGNAL
102

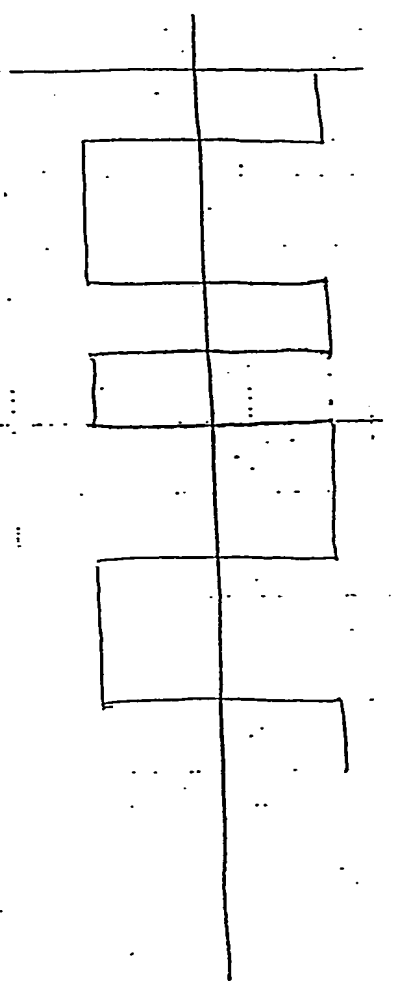


FIG. 1A

OSCILLATING
SIGNAL
104

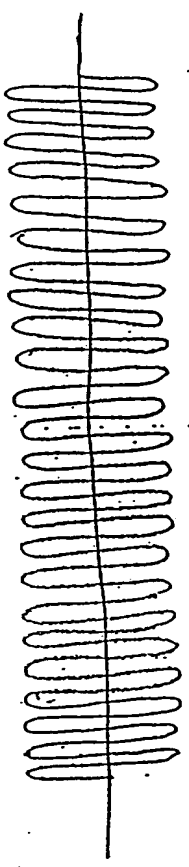


FIG. 1B

FREQUENCY MODULATED
INPUT SIGNAL
106

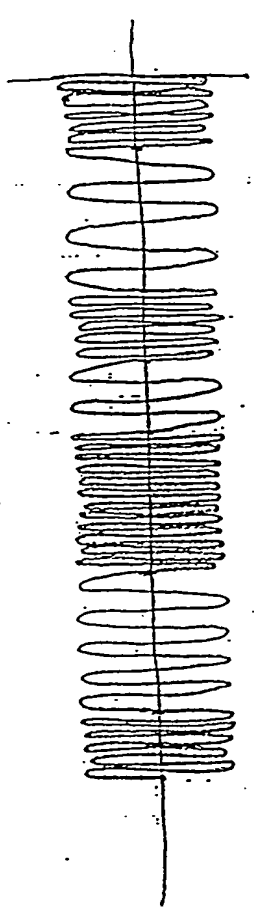


FIG. 1C

HARMONICALLY
RICH SIGNAL
(SHOWN AS SQUARE WAVE)
108

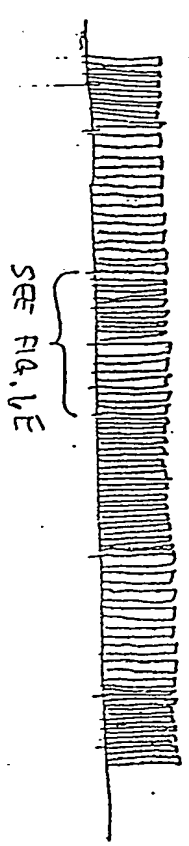


FIG. 1D

FIG. 1E

3

EXPANDED VIEW OF
HARMONICALLY RICH
SIGNAL 608

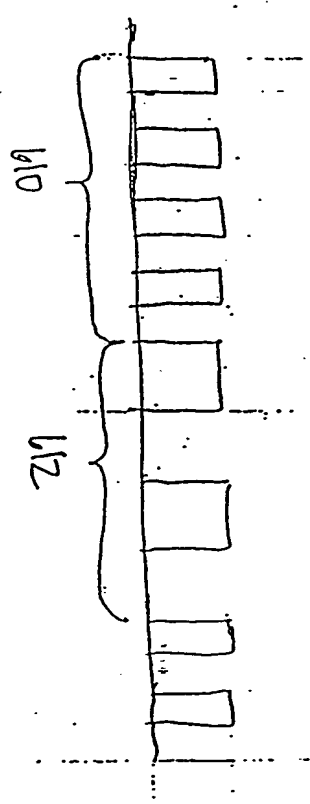


FIG. 6E

HARMONICS OF
SIGNAL 610
(SHOWN SEPARATELY)

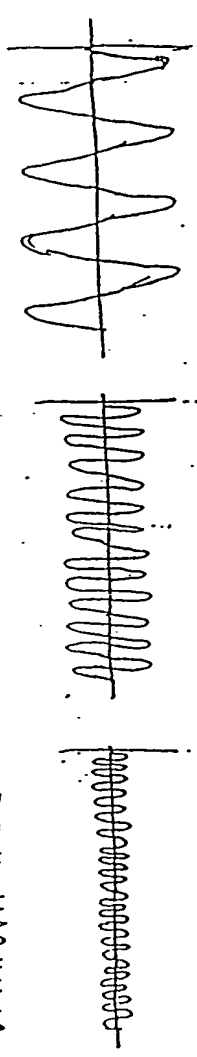


FIG. 6F

HARMONICS OF
SIGNAL 612
(SHOWN SEPARATELY)

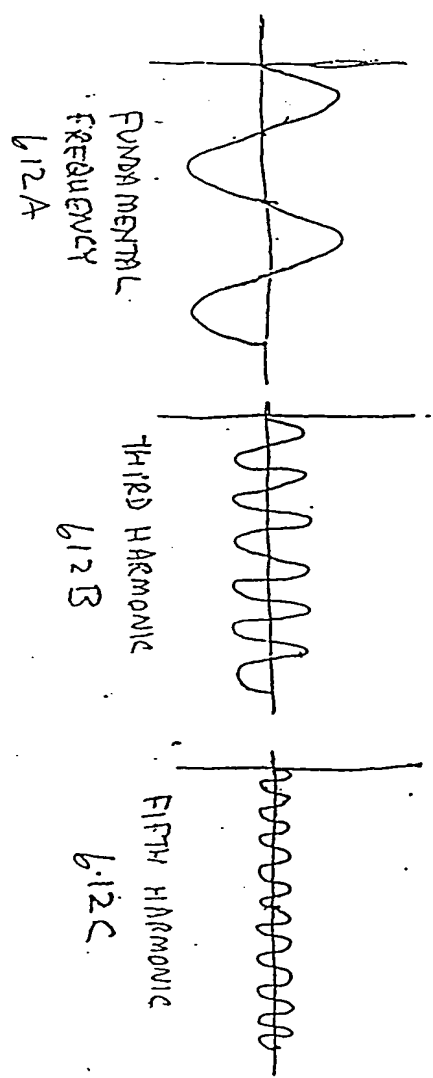


FIG. 6G

FIG. 6 (cont.)

3

HARMONICS OF
SIGNALS A10 AND
A12. (SHOWN
MULTIPLY BUT
NOT SUMMED)

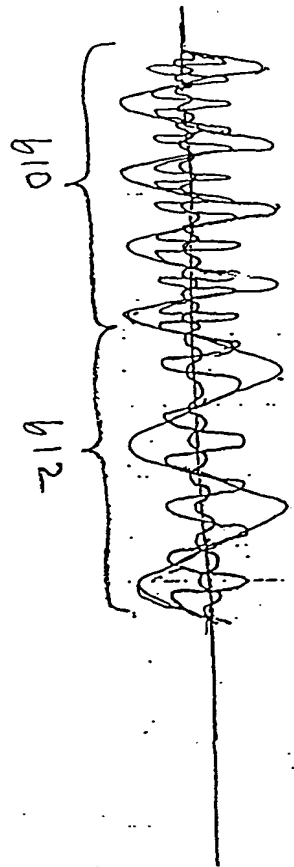


FIG. 6H

FILTERED
OUTPUT
SIGNAL
A14

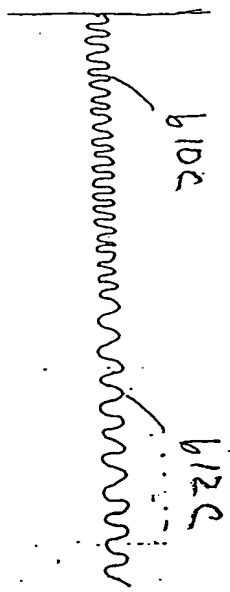


FIG. 6I

FIG. 6 (cont)

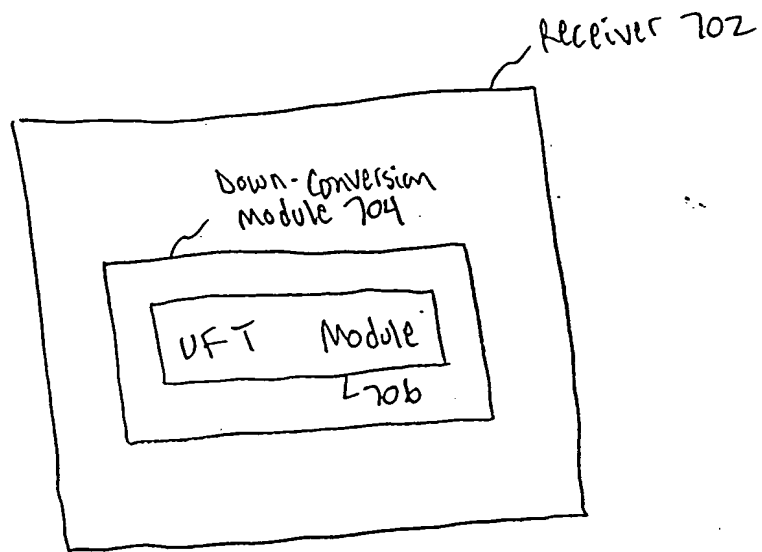


FIG. 7

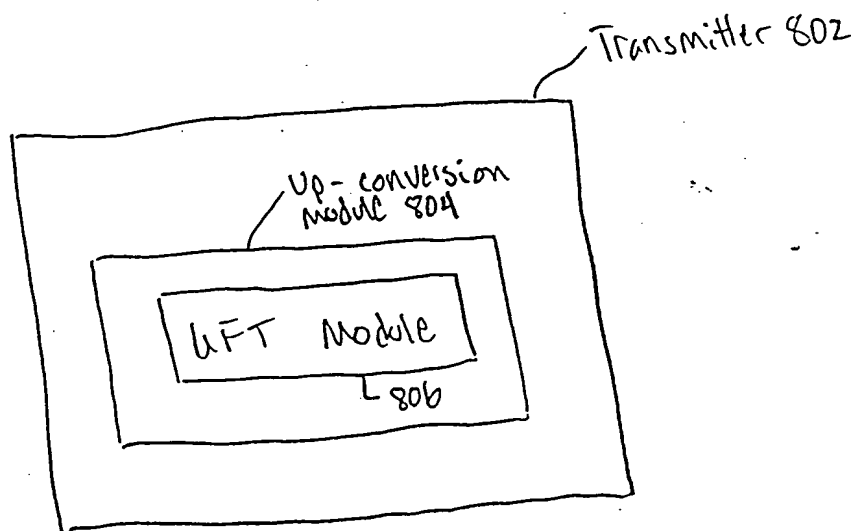


FIG. 8

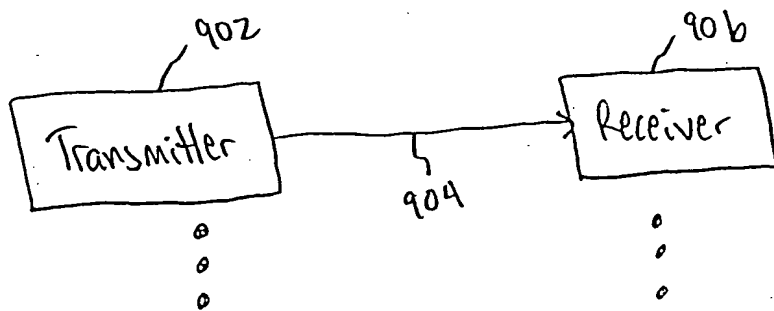


FIG. 9

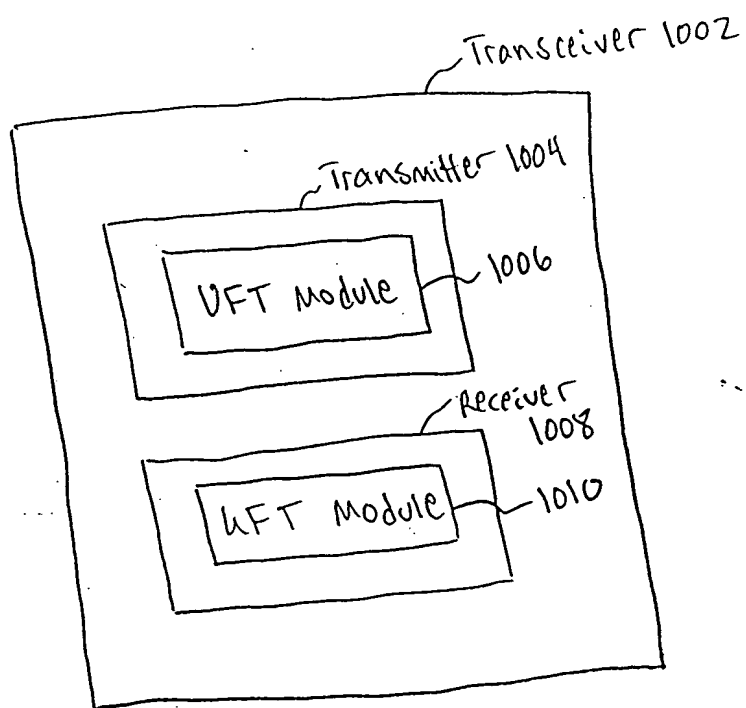


FIG. 10

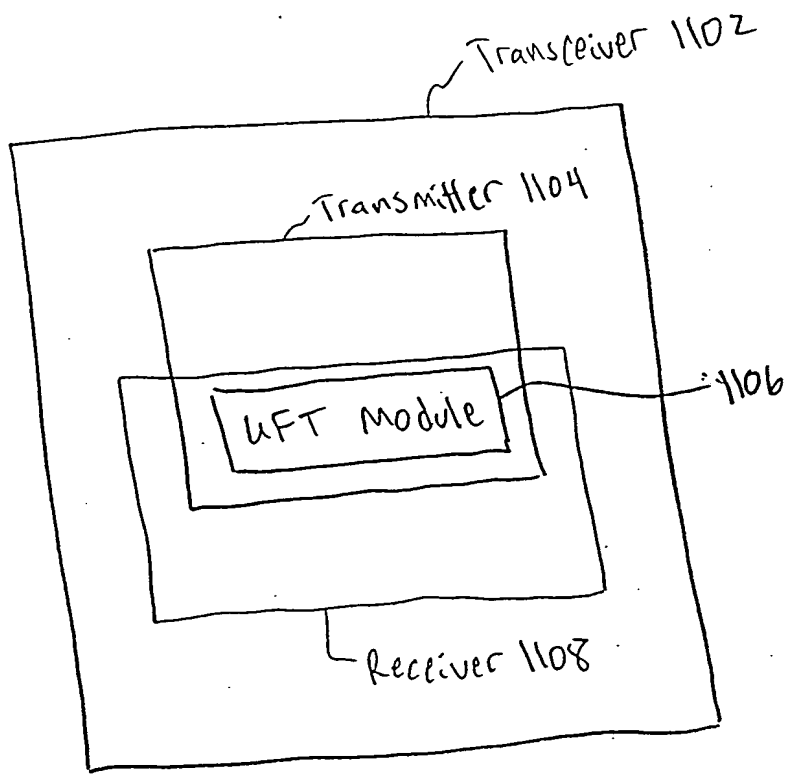


FIG. 11

Downloaded from www.technicaldrawing.com

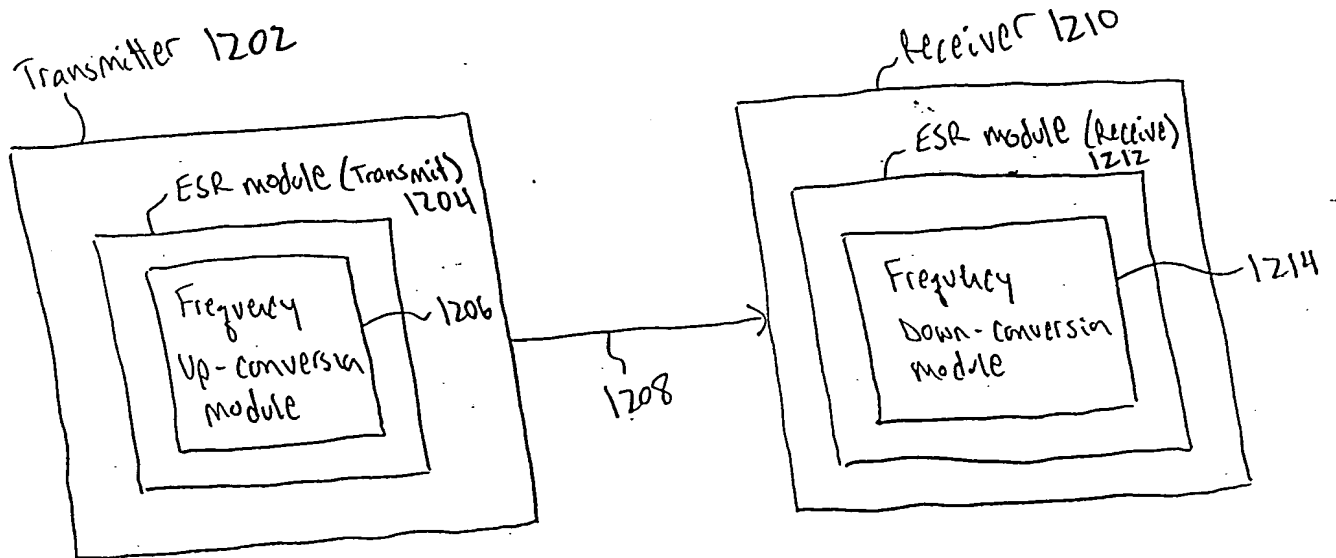


FIG. 12

Unified Down-converting
and Filtering (UDF) module 1302

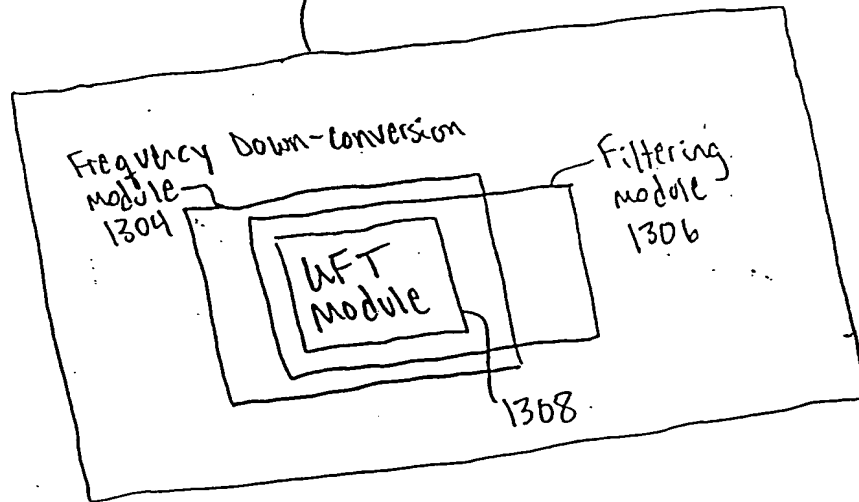


FIG. 13

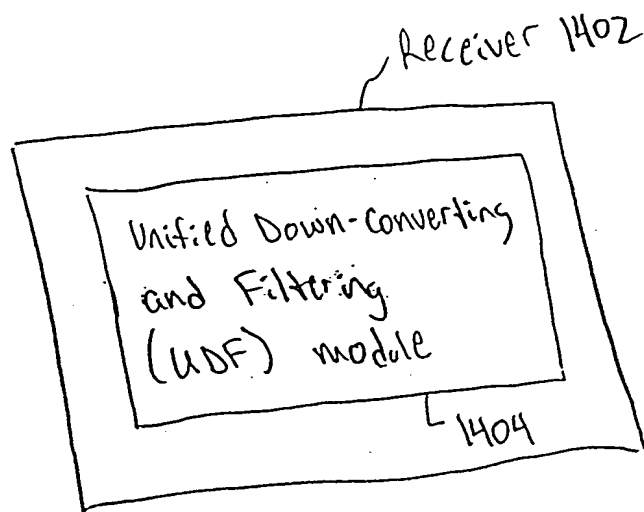
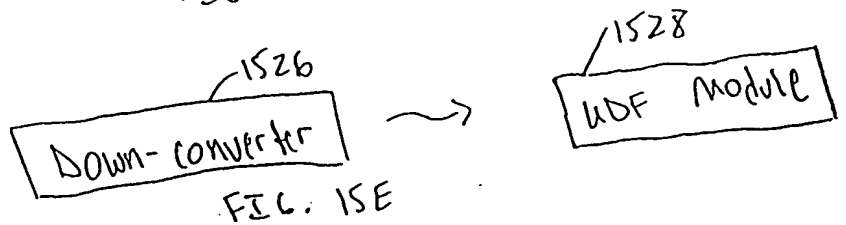
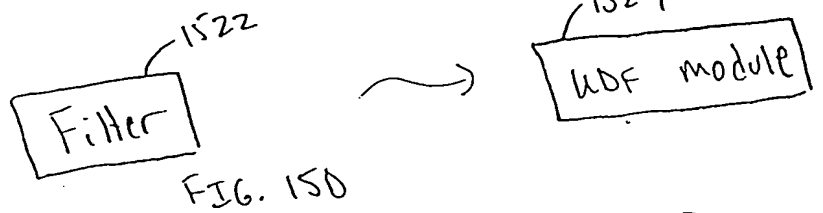
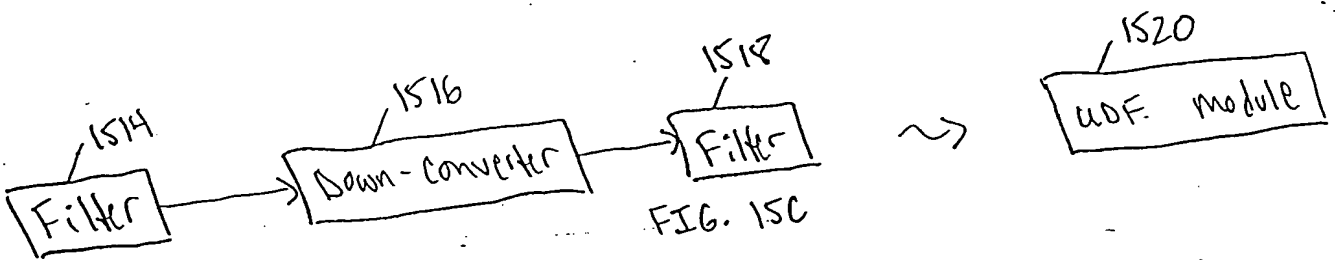
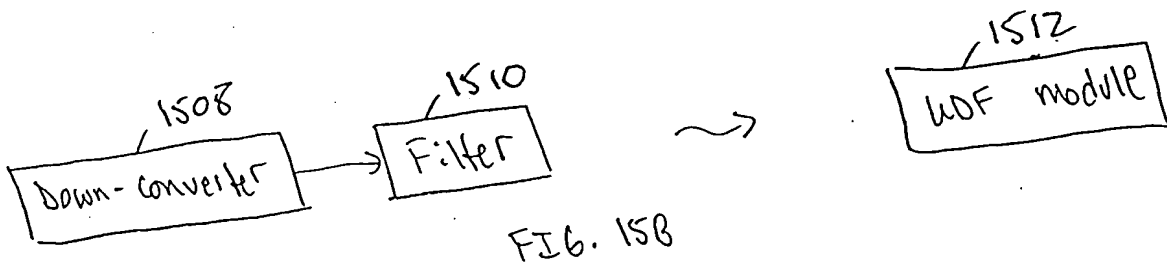
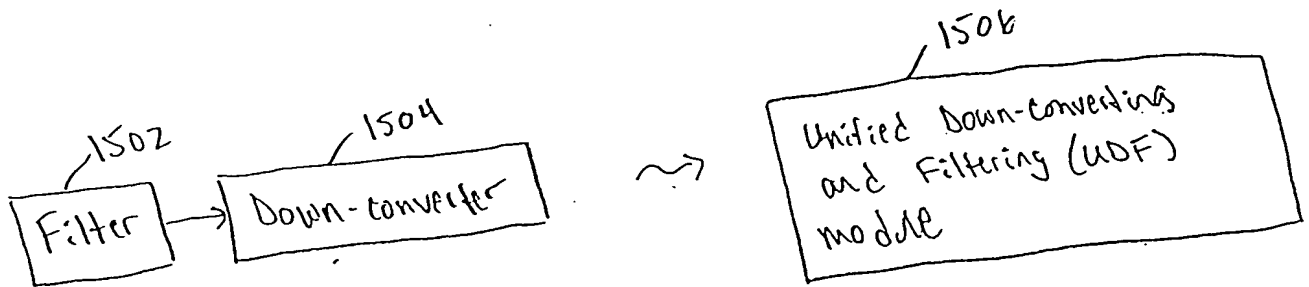


FIG. 14



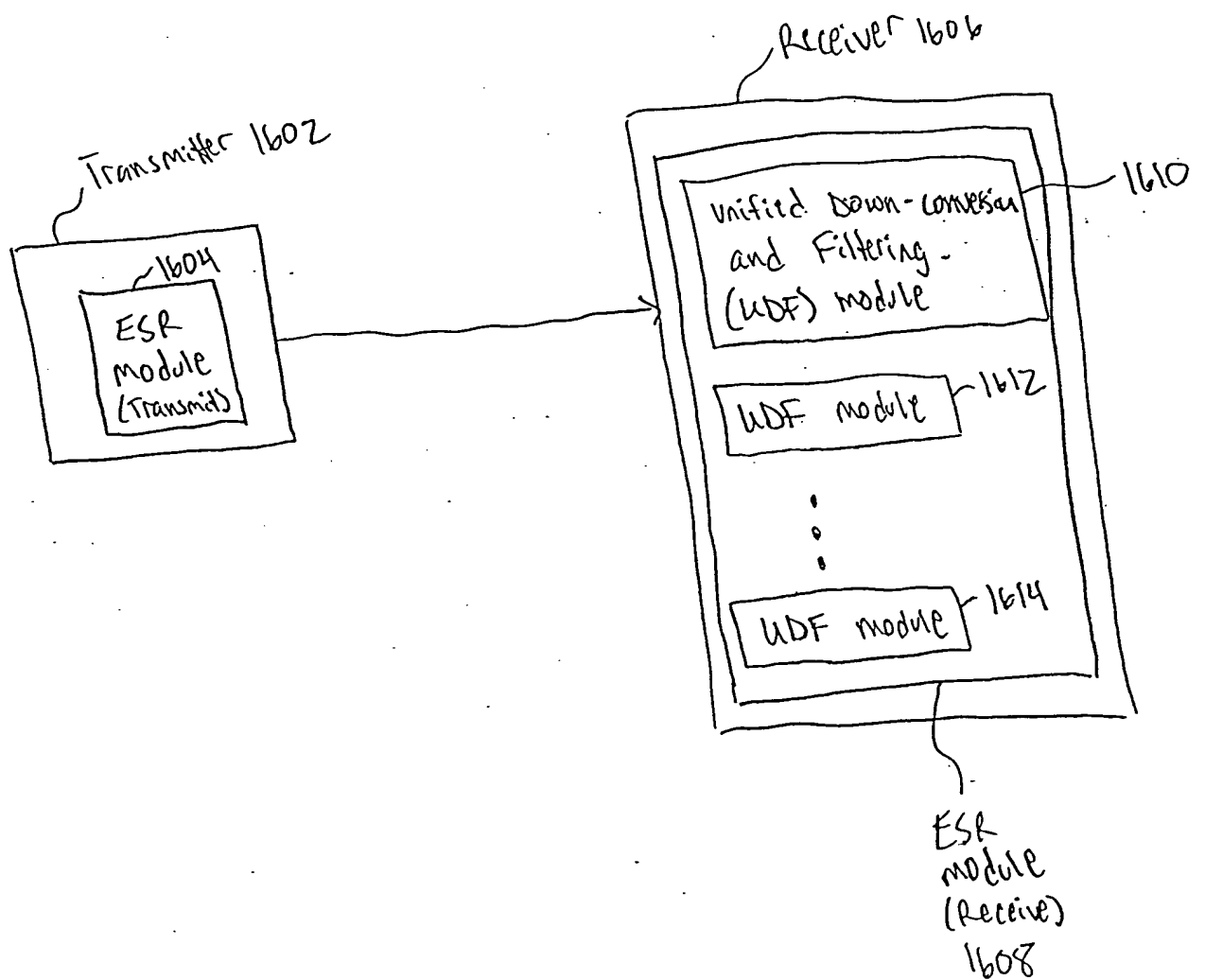


FIG. 16

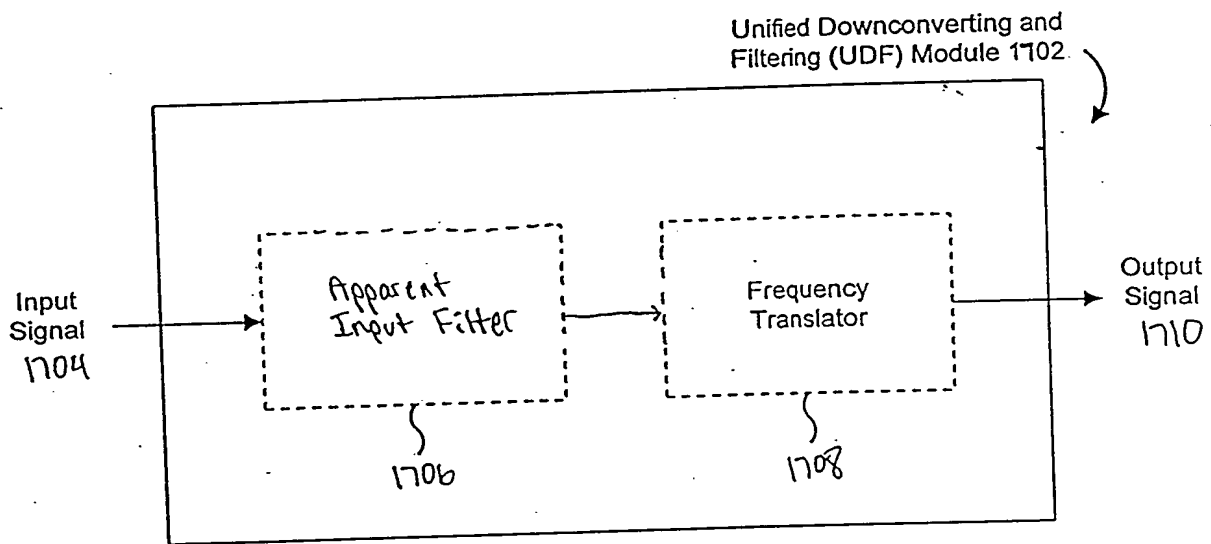


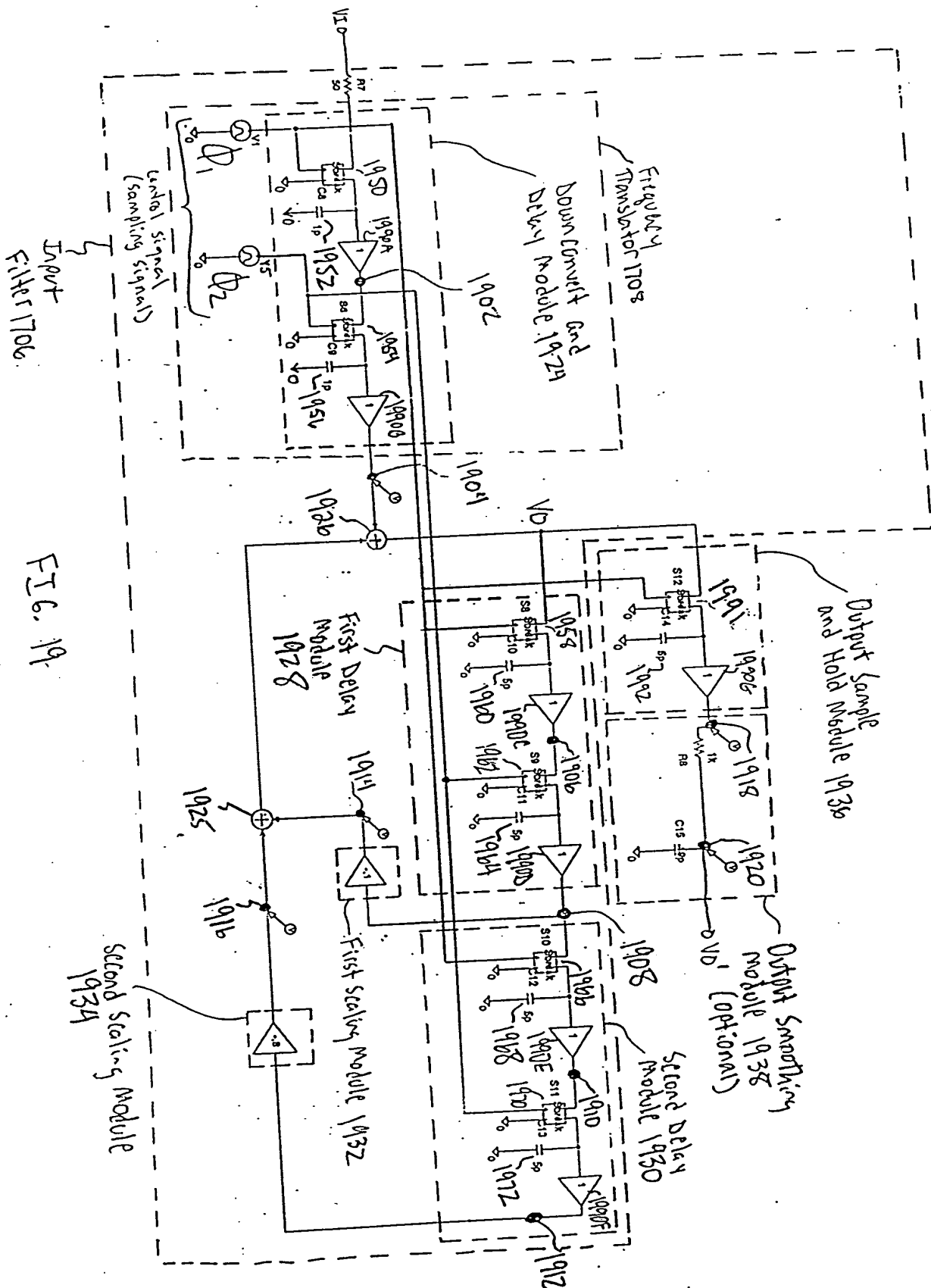
FIG. 17

1802

Time Node	t-1 (rising edge of ϕ_1)	t-1 (rising edge of ϕ_2)	t (rising edge of ϕ_1)	t (rising edge of ϕ_2)	t+1 (rising edge of ϕ_1)
1902	$V_{I,t-1}$ <u>1804</u>	$V_{I,t-1}$ <u>1808</u>	$V_{I,t}$ <u>1816</u>	$V_{I,t}$ <u>1826</u>	$V_{I,t+1}$ <u>1838</u>
1904	—	$V_{I,t-1}$ <u>1810</u>	$V_{I,t-1}$ <u>1818</u>	$V_{I,t}$ <u>1828</u>	$V_{I,t}$ <u>1840</u>
1906	VO_{t-1} <u>1806</u>	VO_{t-1} <u>1812</u>	VO_t <u>1820</u>	VO_t <u>1830</u>	VO_{t+1} <u>1842</u>
1908	—	VO_{t-1} <u>1814</u>	VO_{t-1} <u>1822</u>	VO_t <u>1832</u>	VO_t <u>1844</u>
1910	— <u>1807</u>	—	VO_{t-1} <u>1824</u>	VO_{t-1} <u>1834</u>	VO_t <u>1846</u>
1912	—	— <u>1815</u>	—	VO_{t-1} <u>1836</u>	VO_{t-1} <u>1848</u>
1918	—	—	—	—	$V_{I,t} -$ <u>1850</u> $0.1 * VO_t$ $0.8 * VO_{t-1}$

FIG. 18

use module 1972
(band pass)



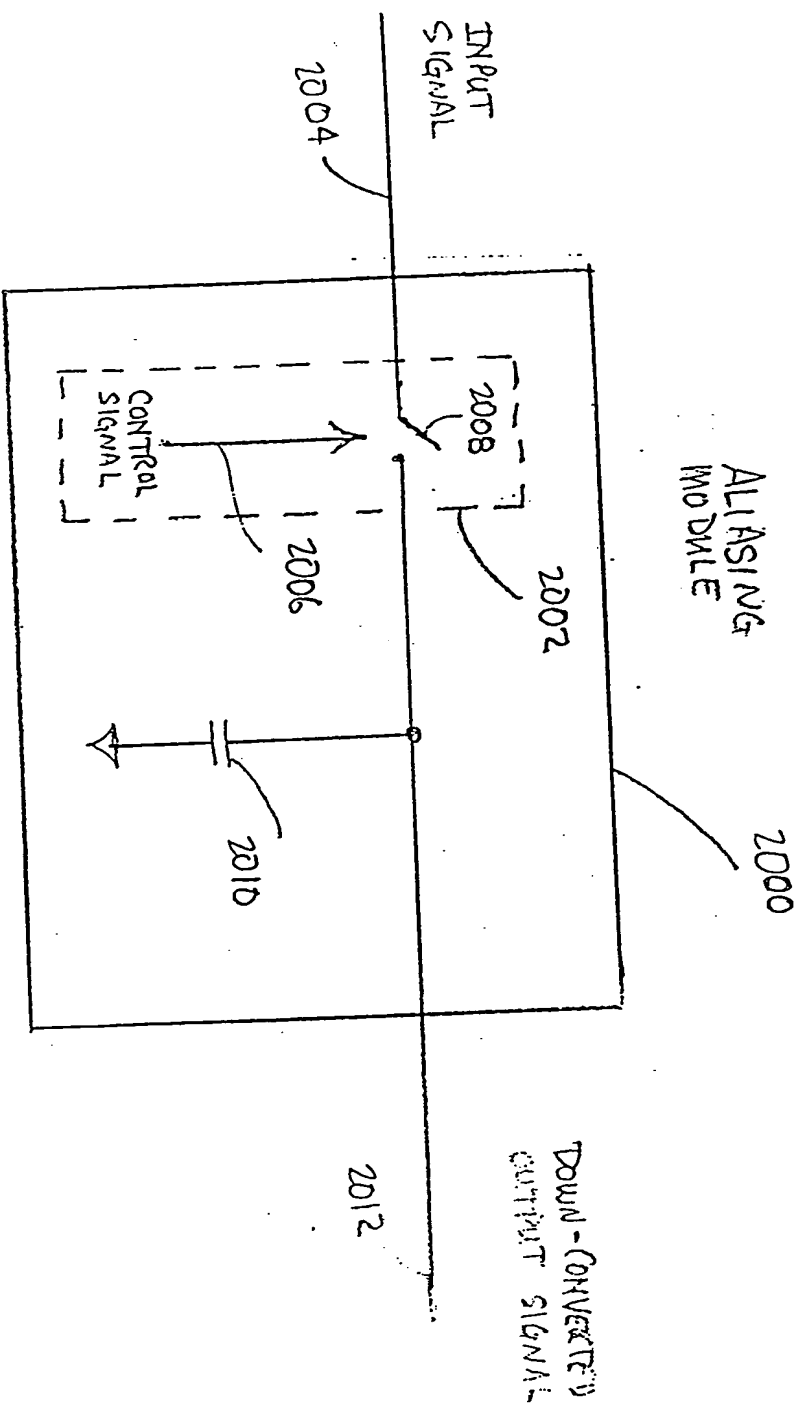


FIG. 20A

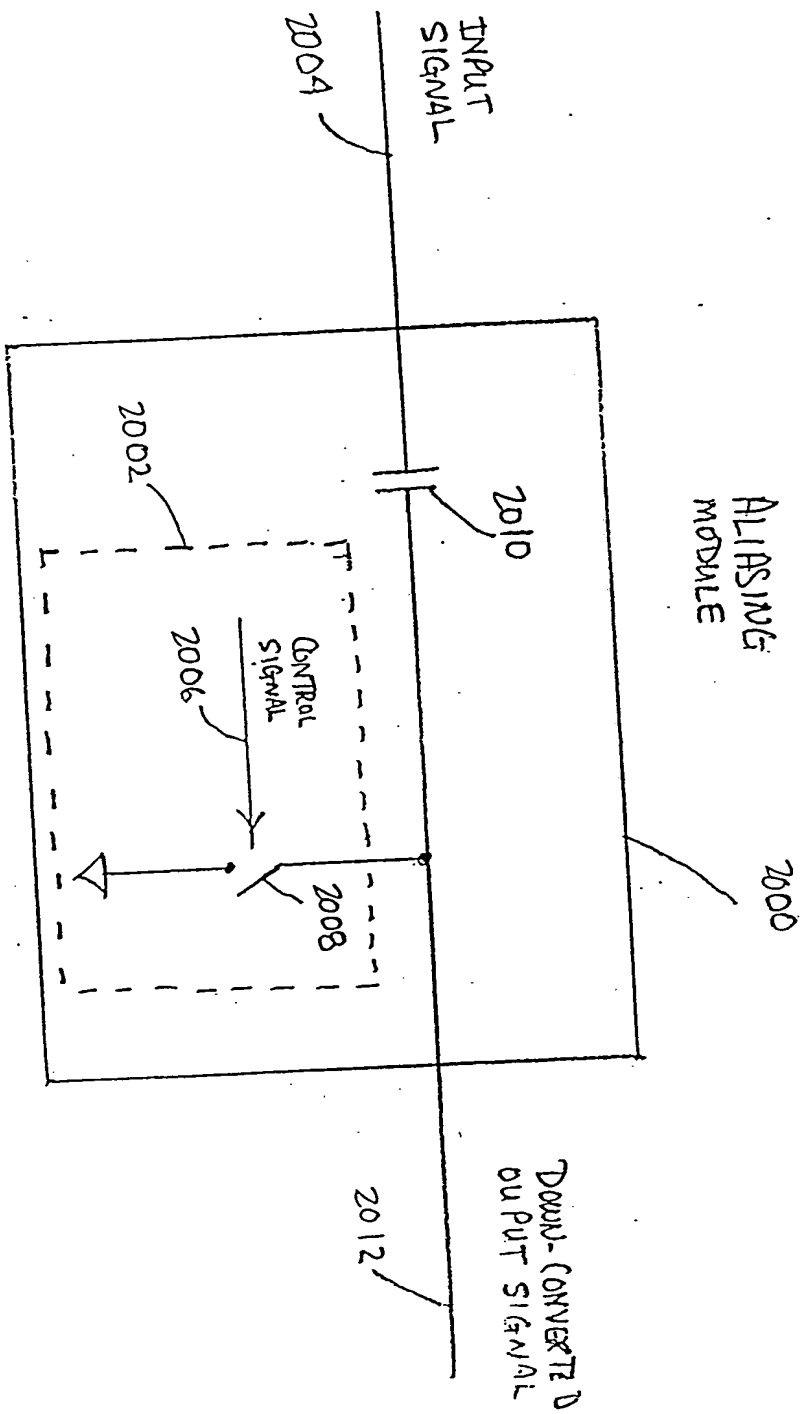


FIG. 20A-1

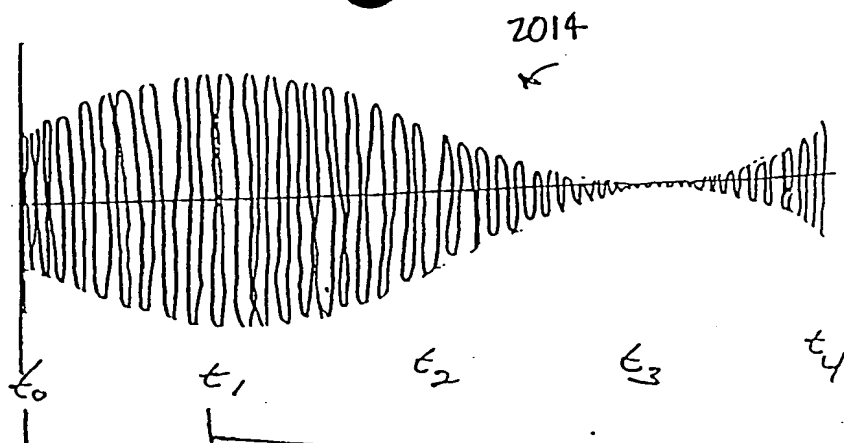


FIG. 20B

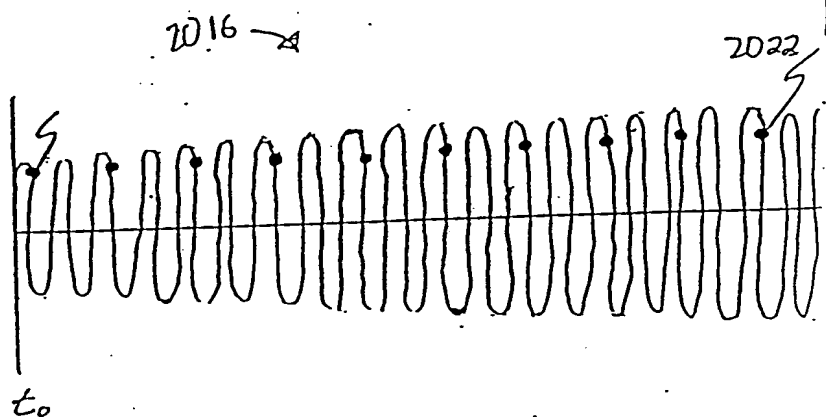


FIG. 20C

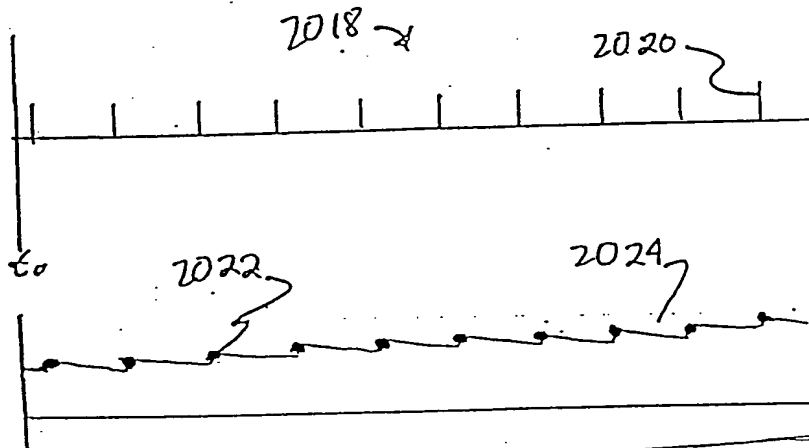


FIG. 20D

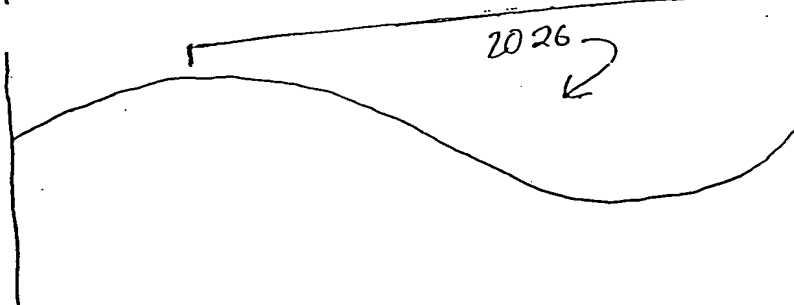


FIG. 20F

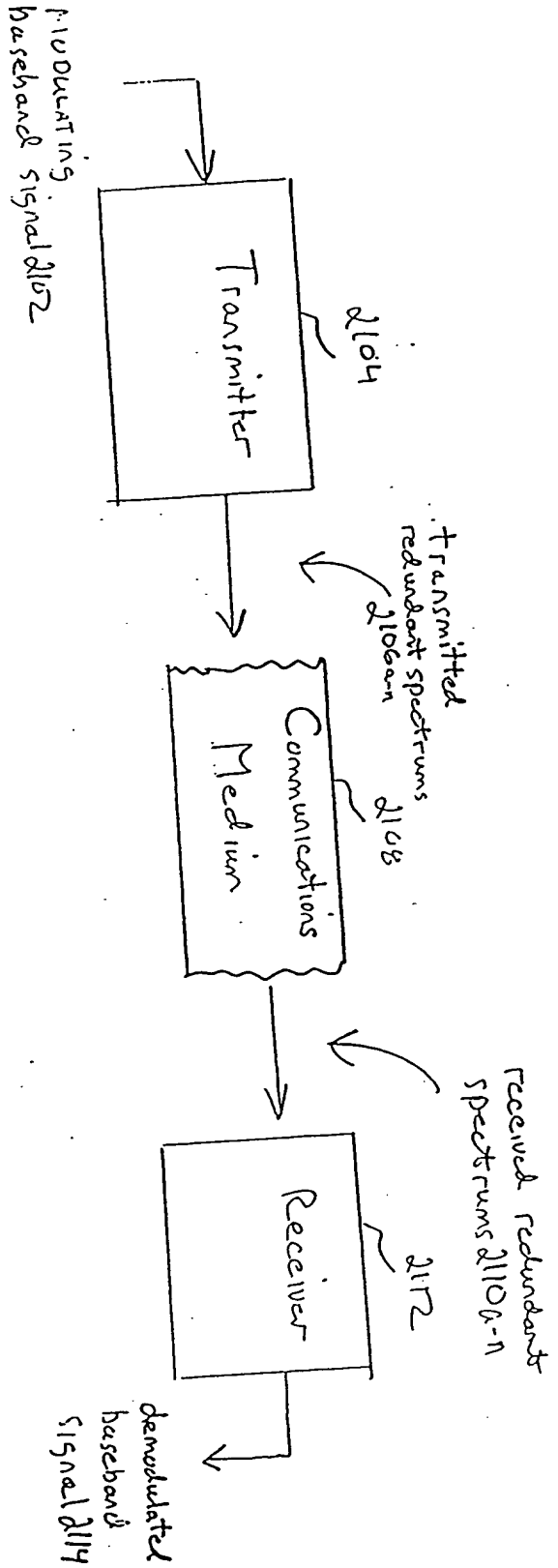
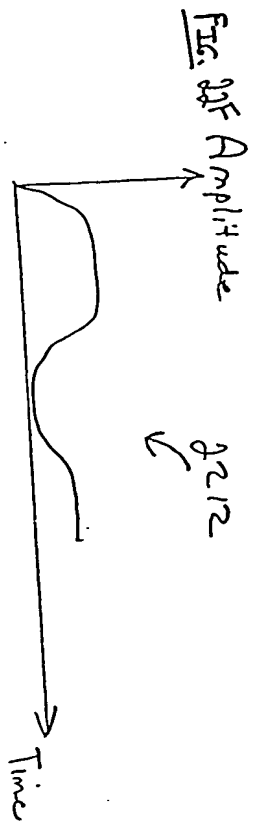
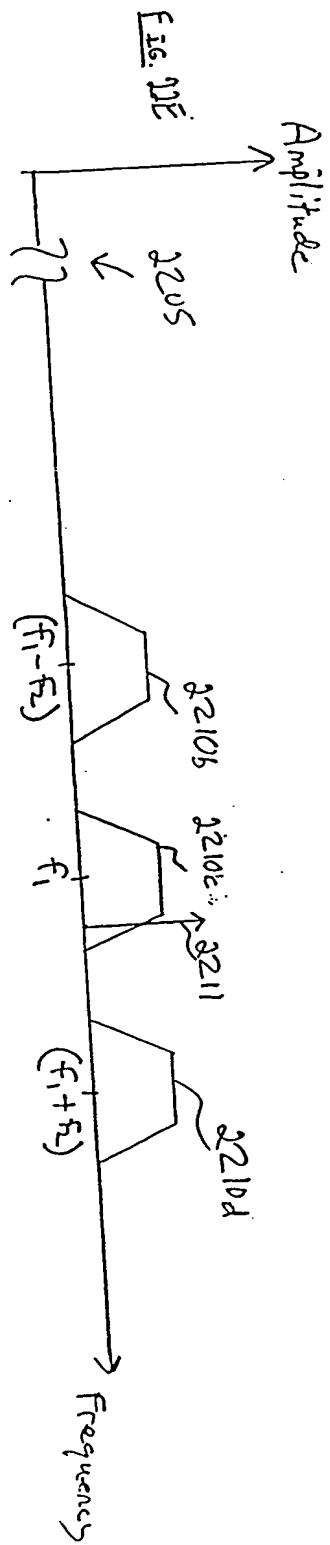


Fig. 21



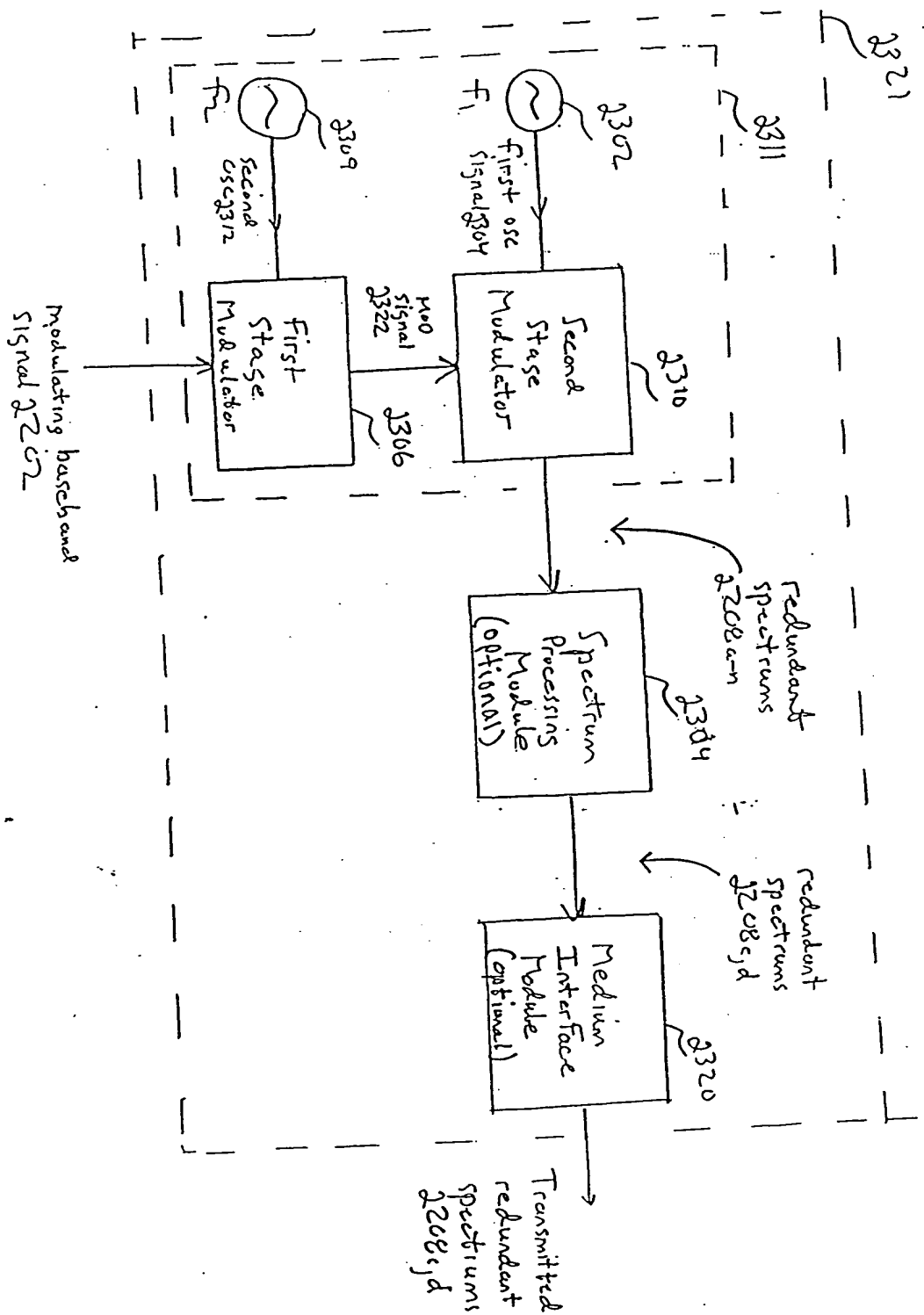


FIG. 23D

FIG. 23E

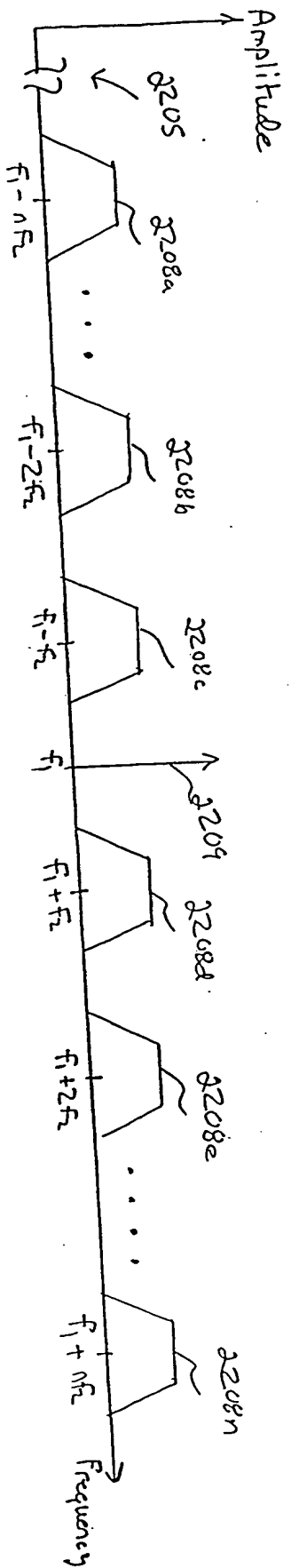


FIG. 23F

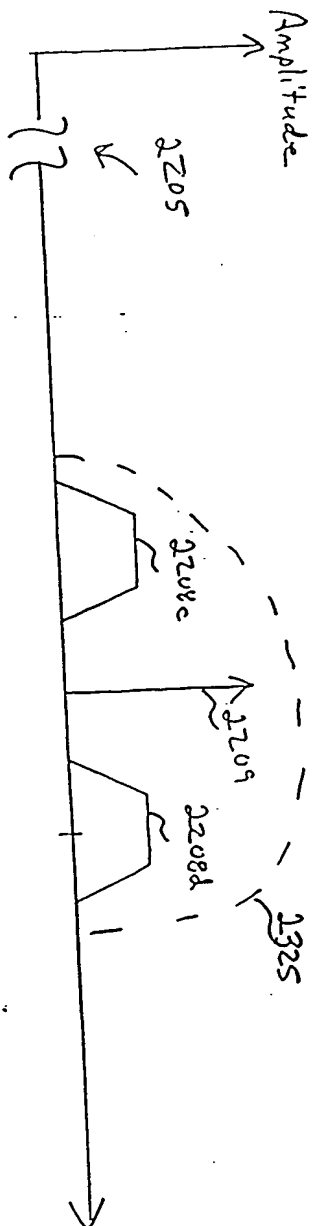
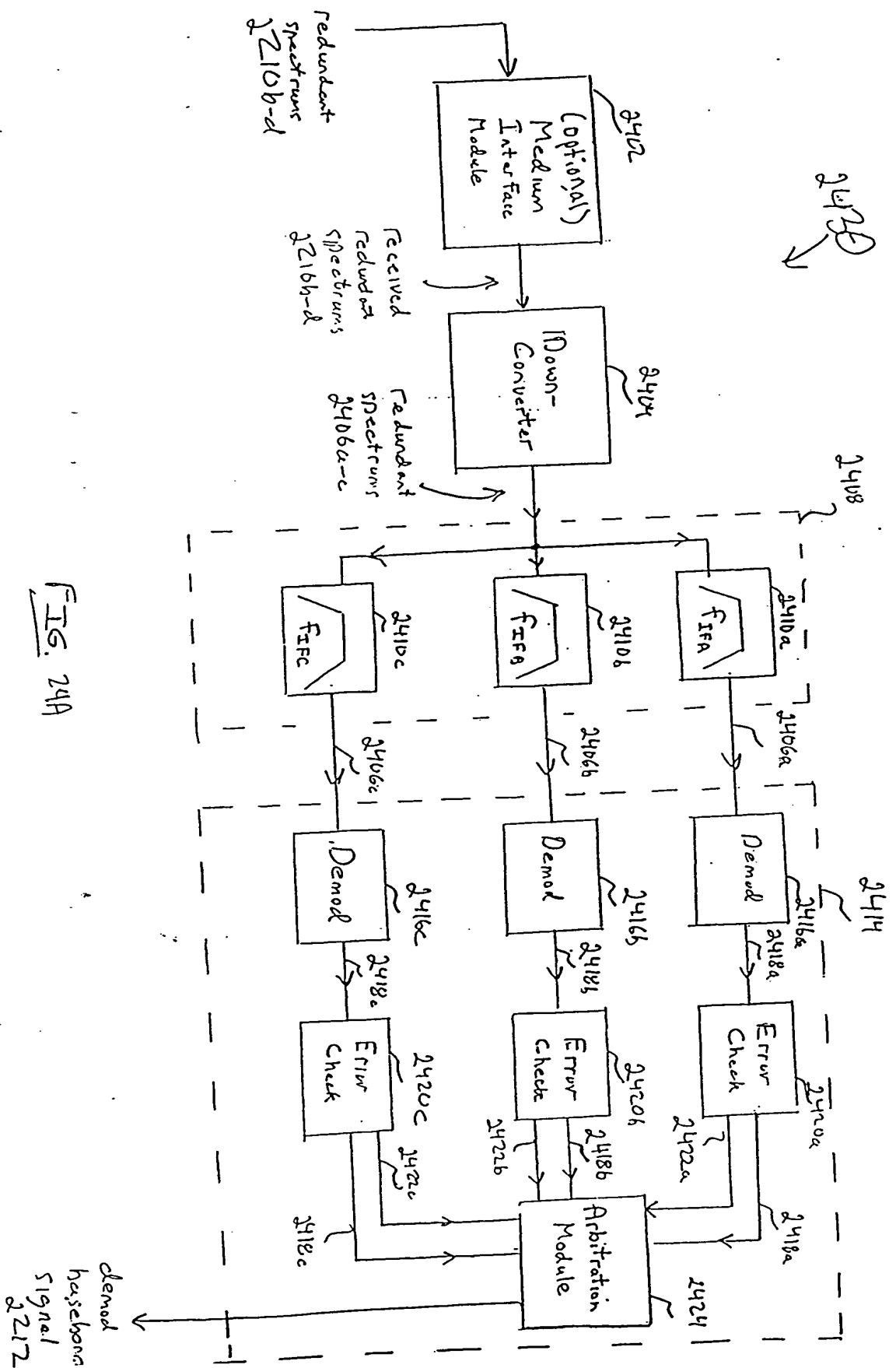
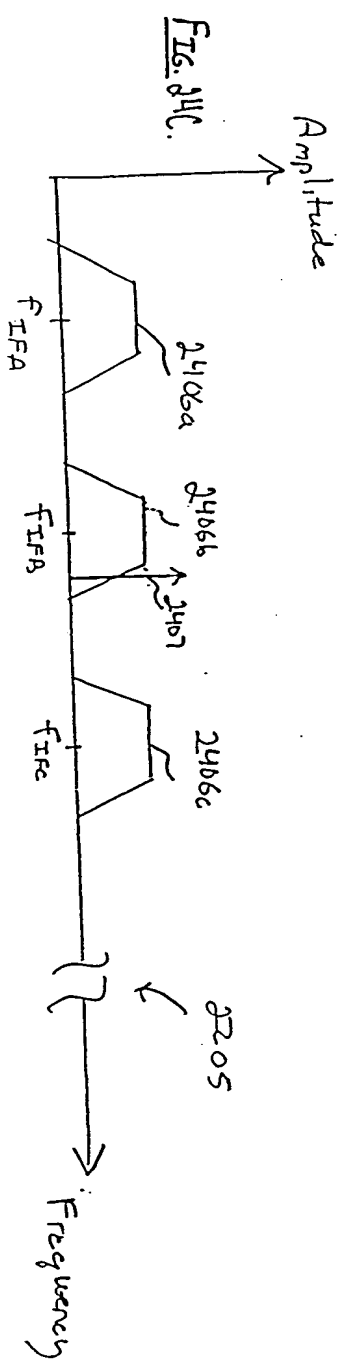
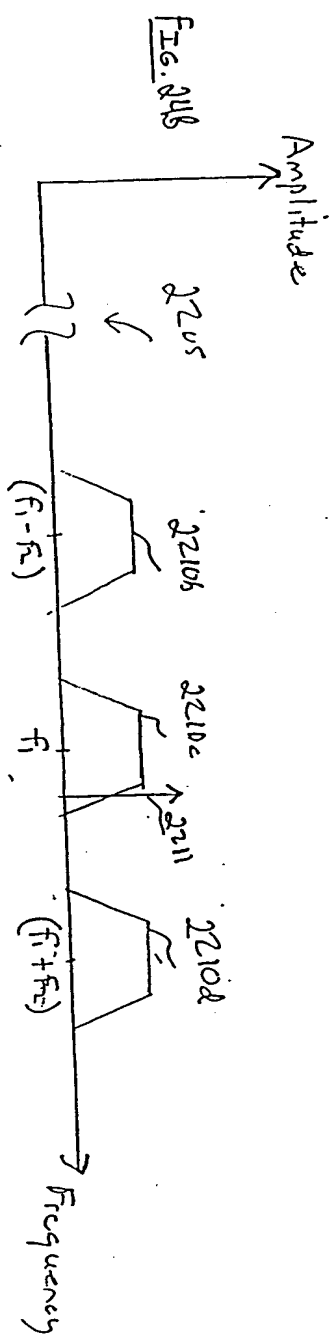


FIG. 23E and FIG. 23F are schematic diagrams of a system for generating a signal having a desired frequency spectrum.





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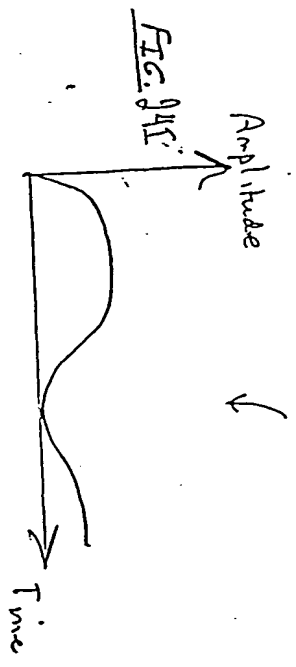
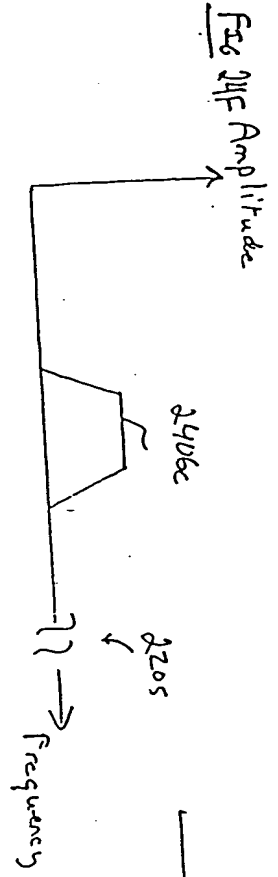
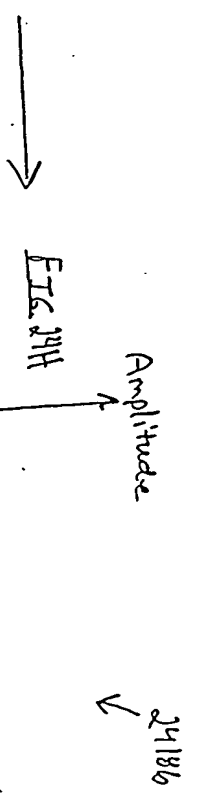
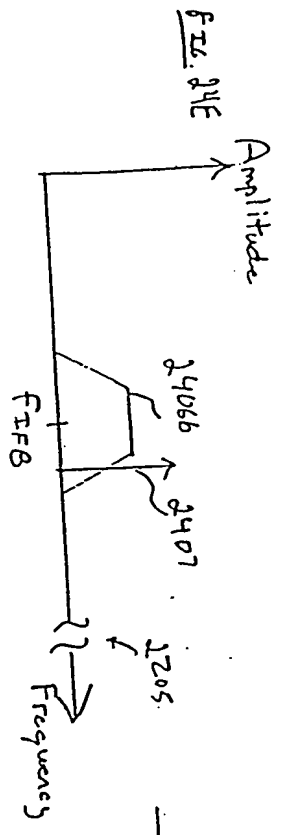
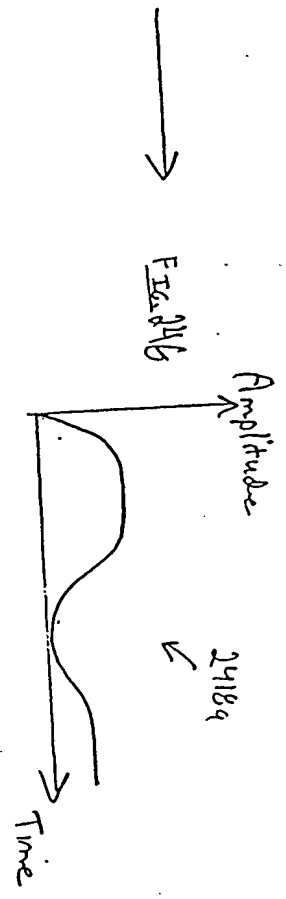
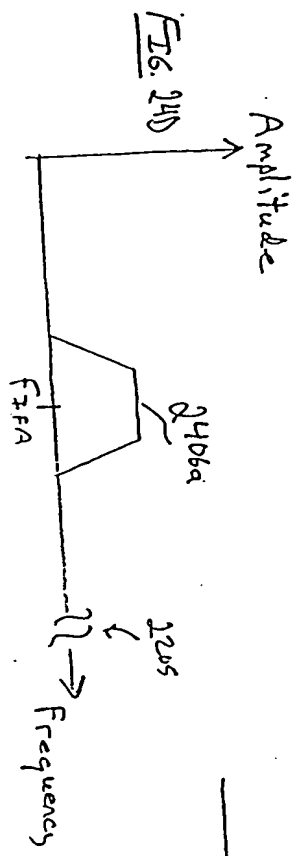
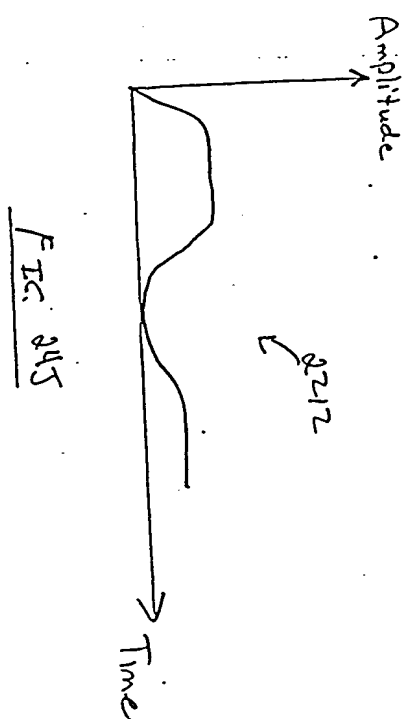


FIG. 24D

W



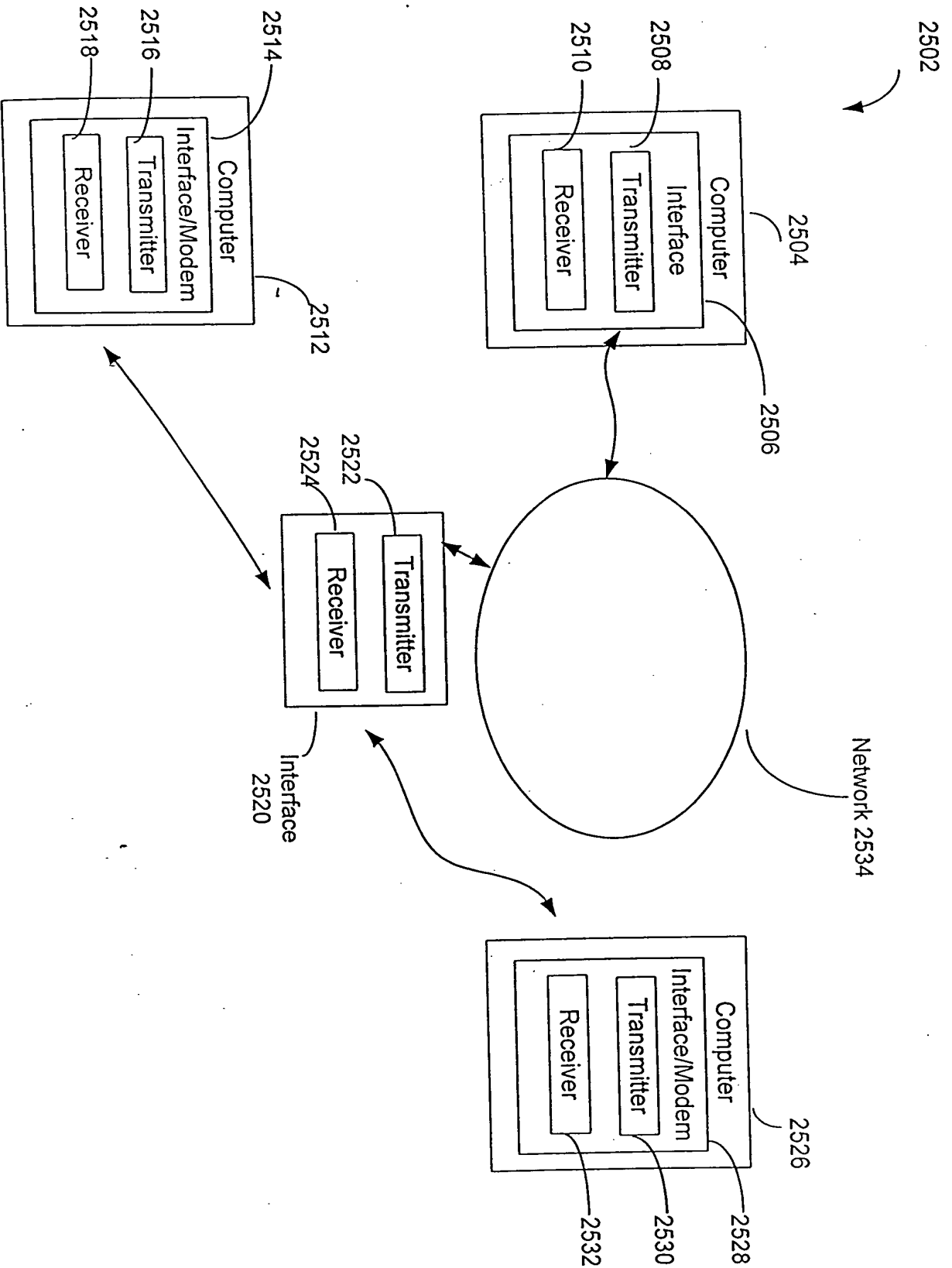


FIG. 25

2606

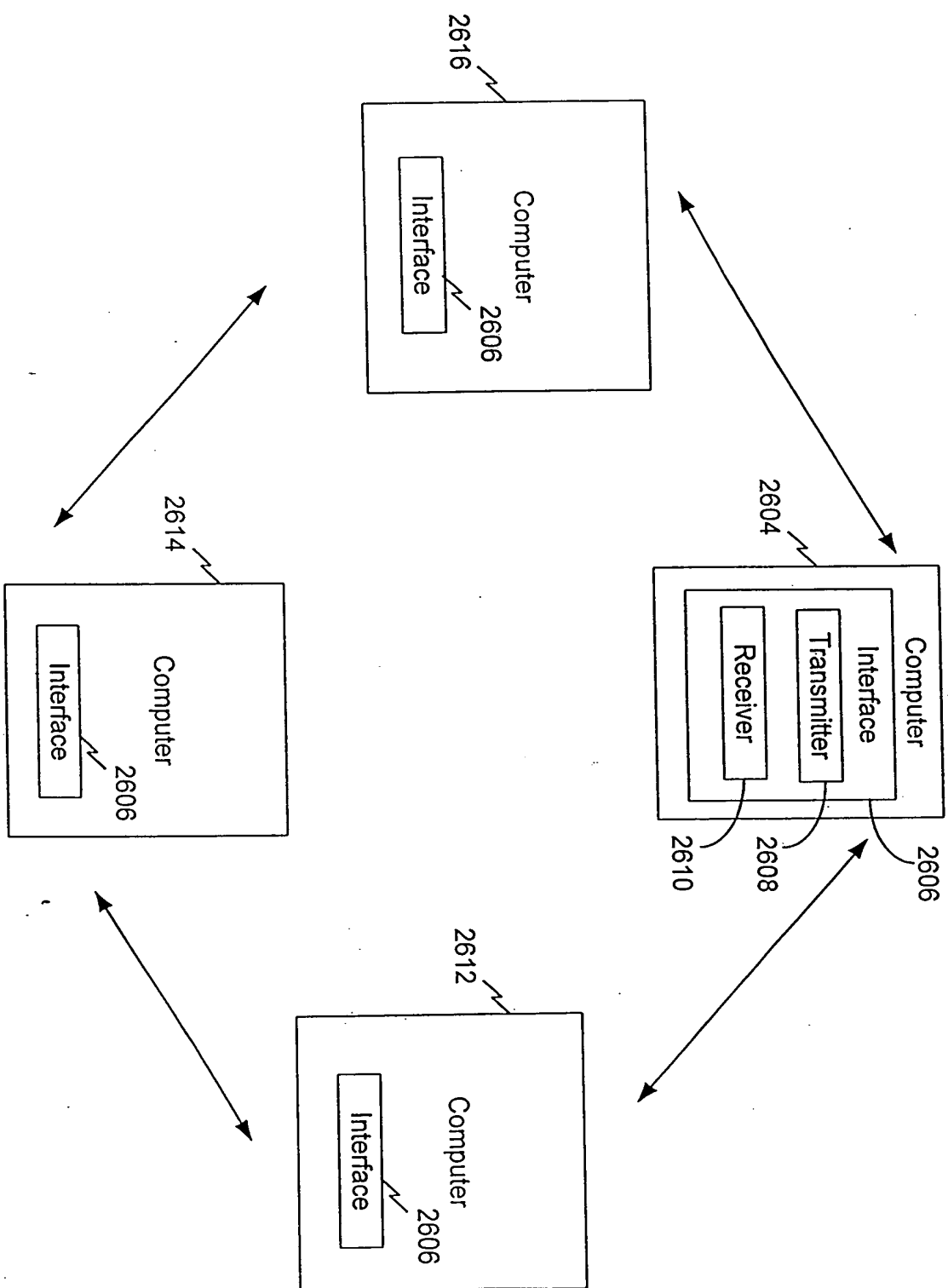


FIG. 26

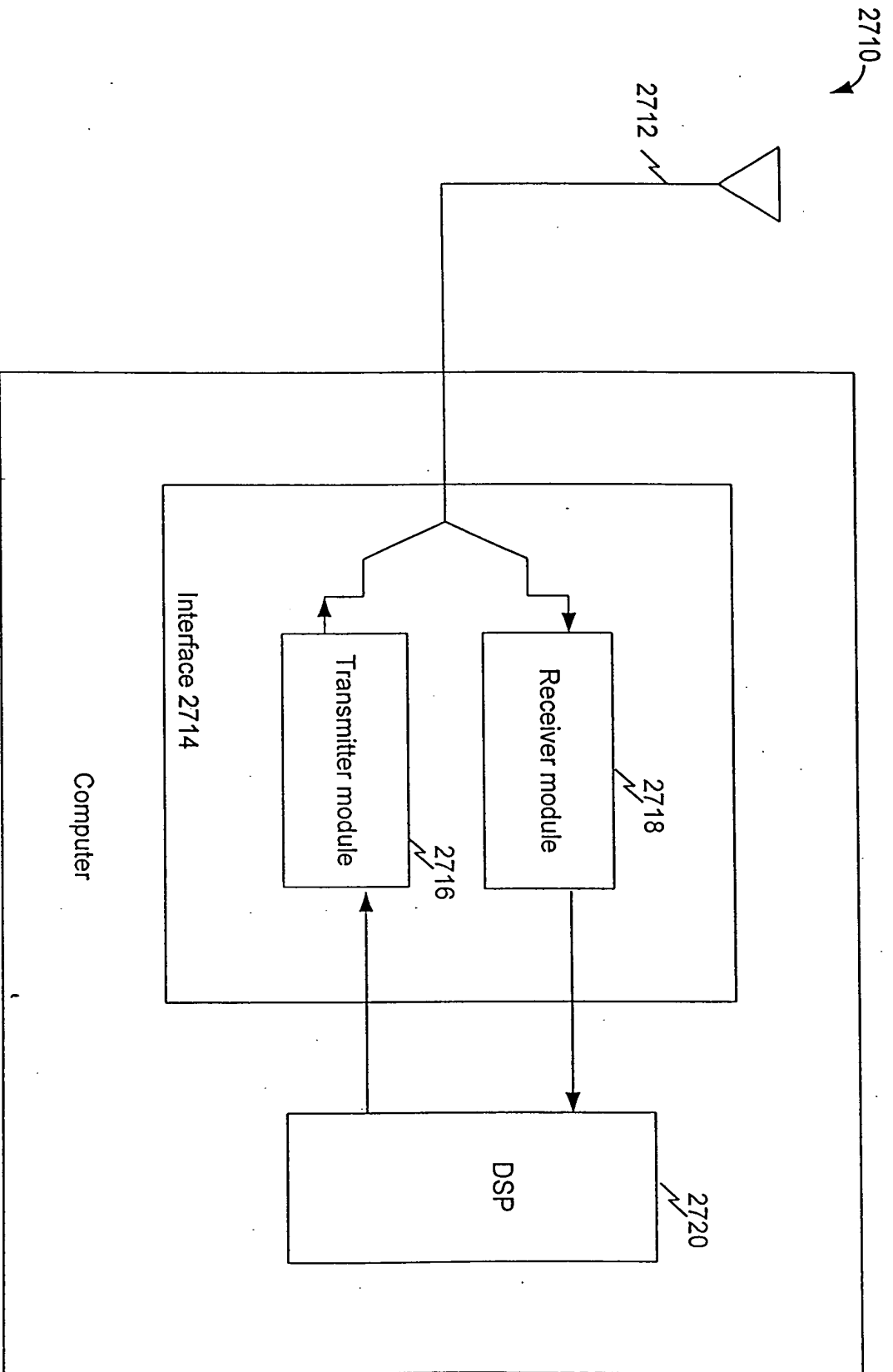


FIG. 27

[illegible]

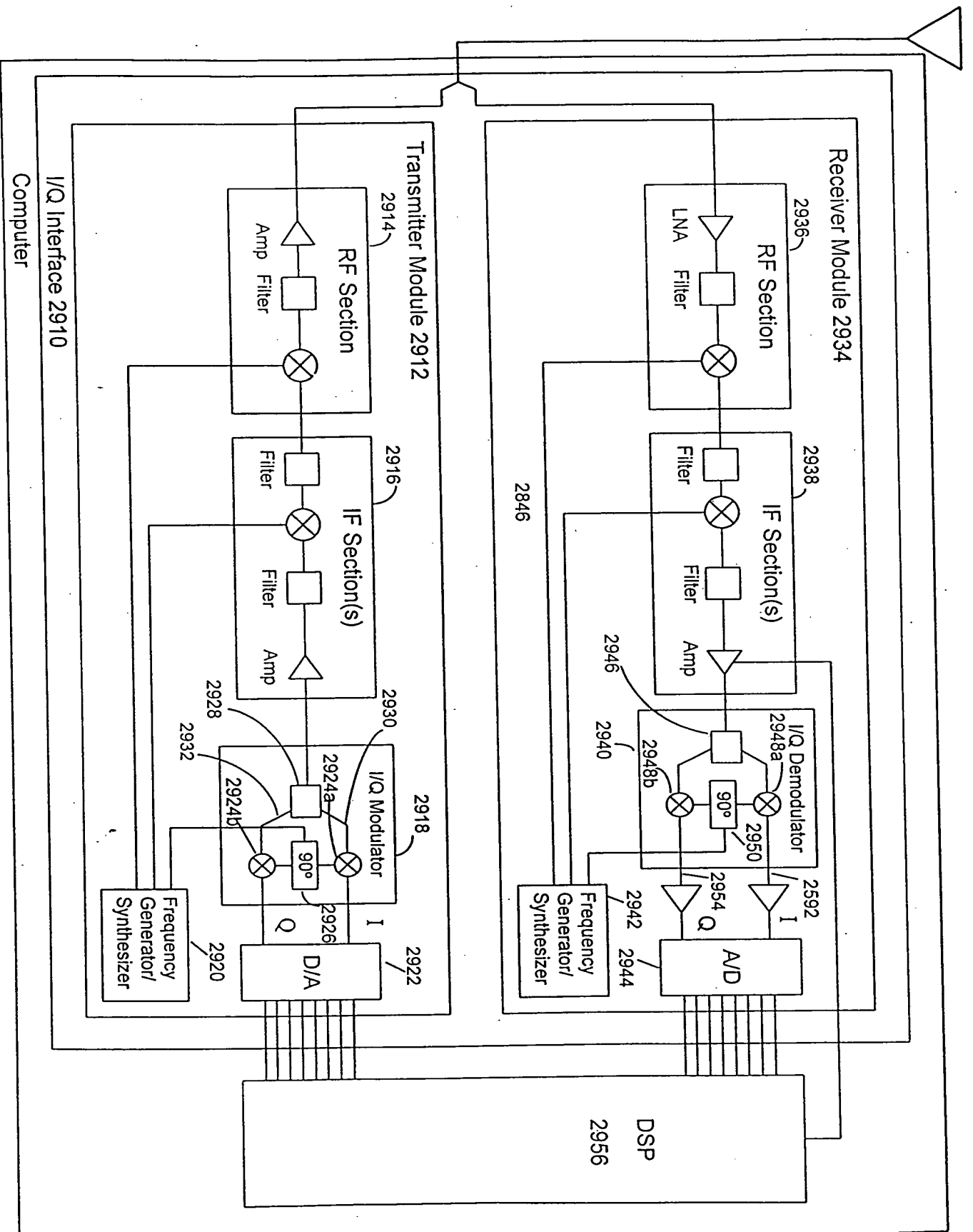
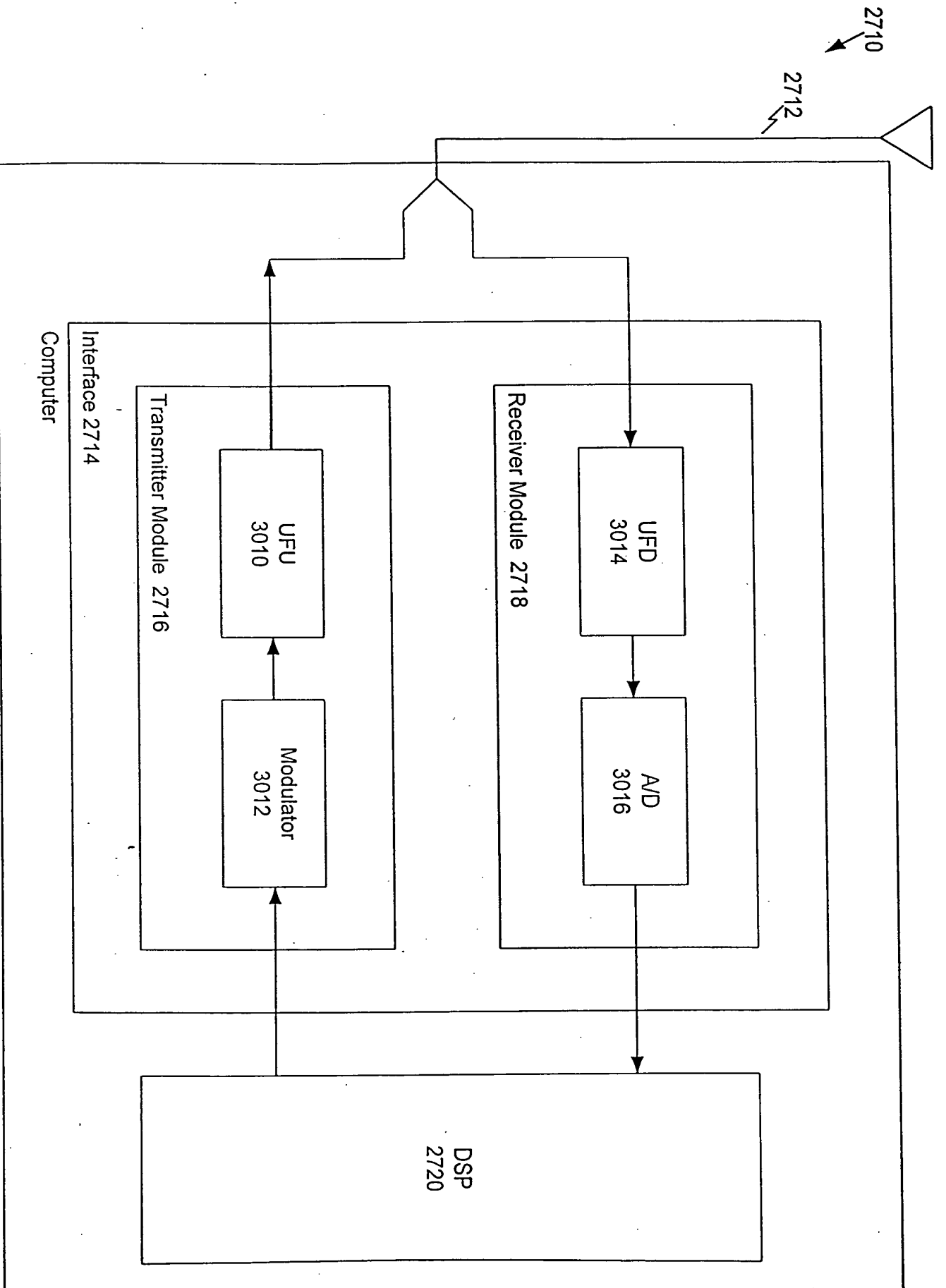


FIG. 29



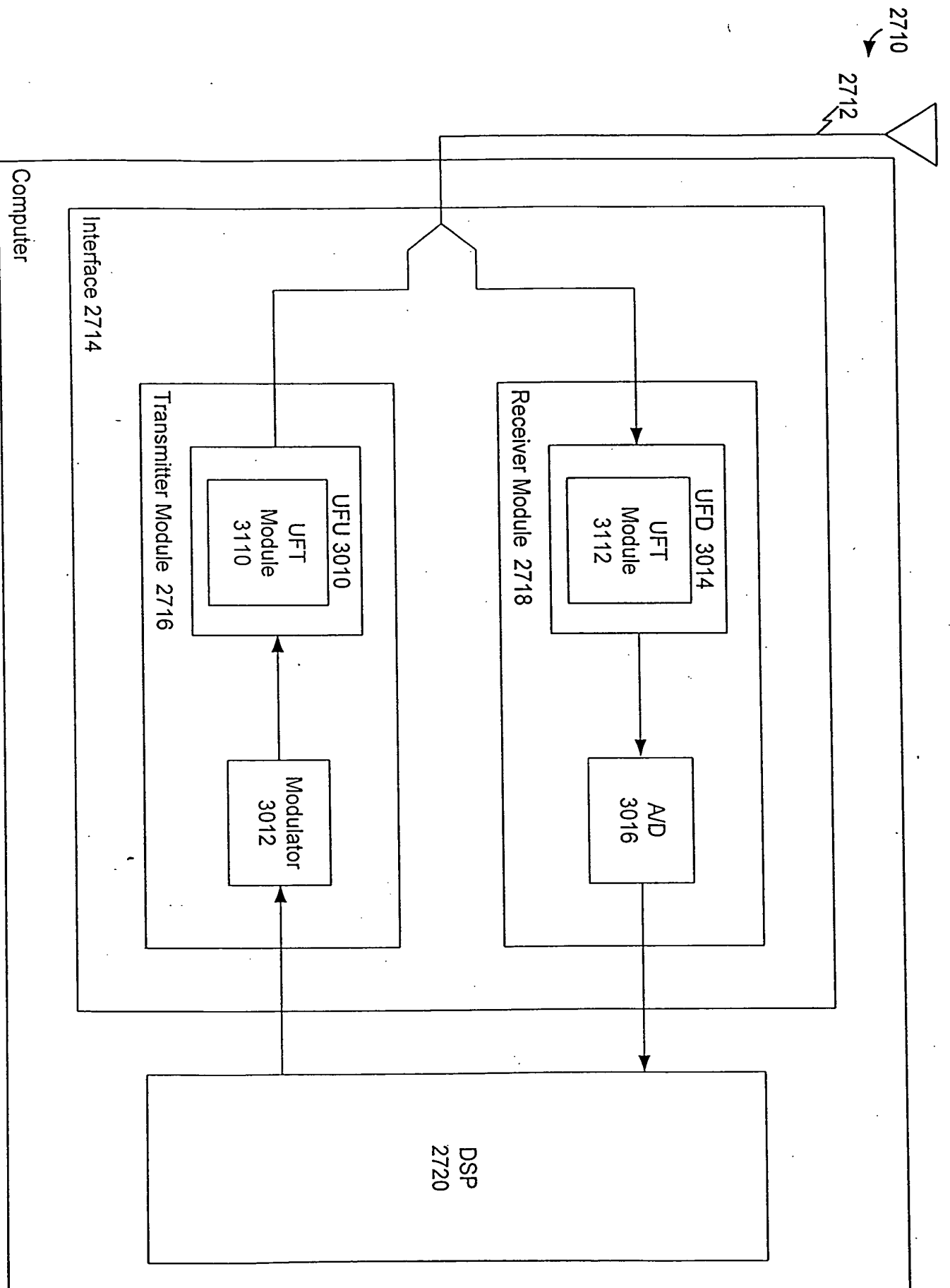


FIG. 31

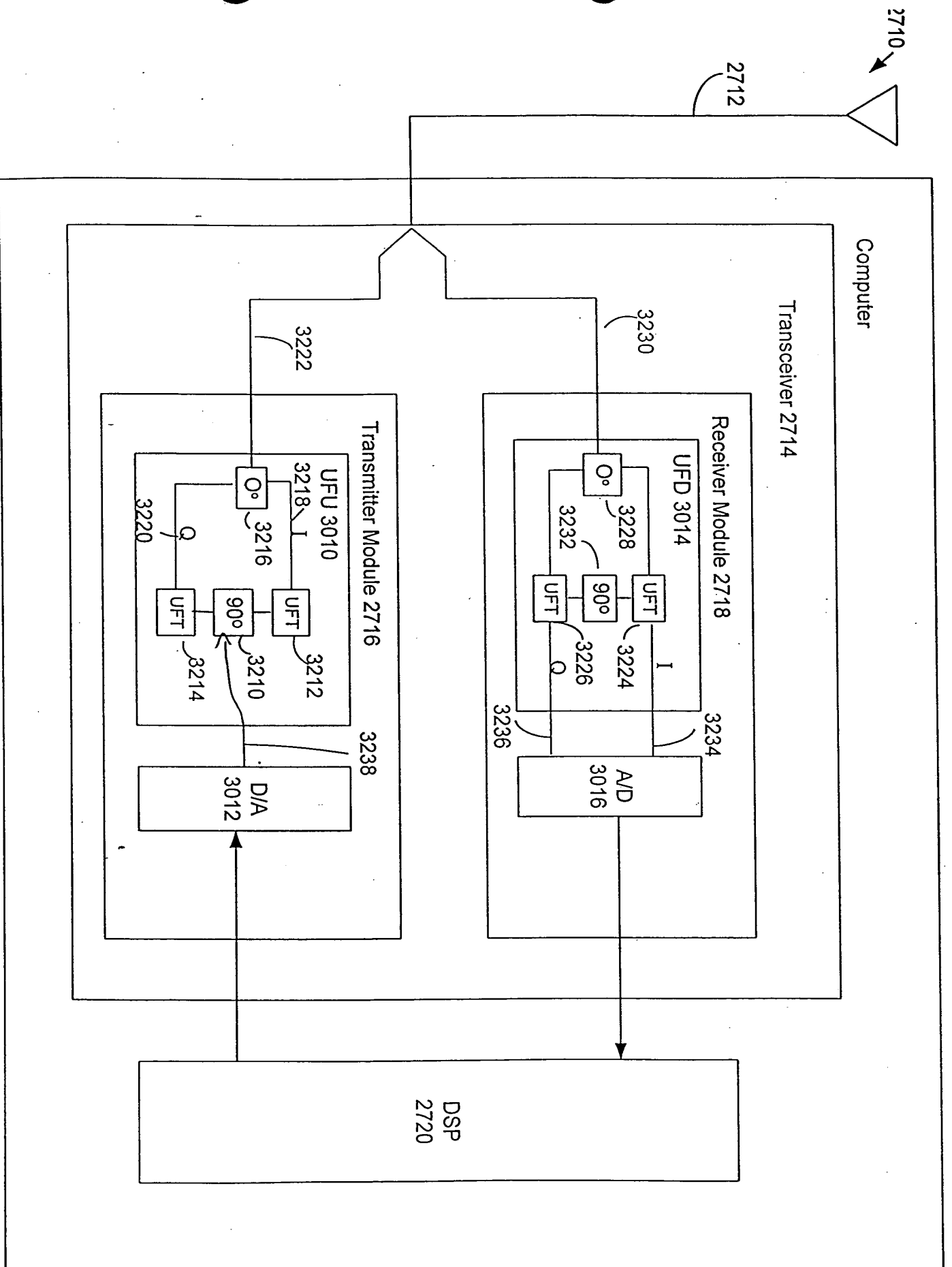


FIG. 32

3302

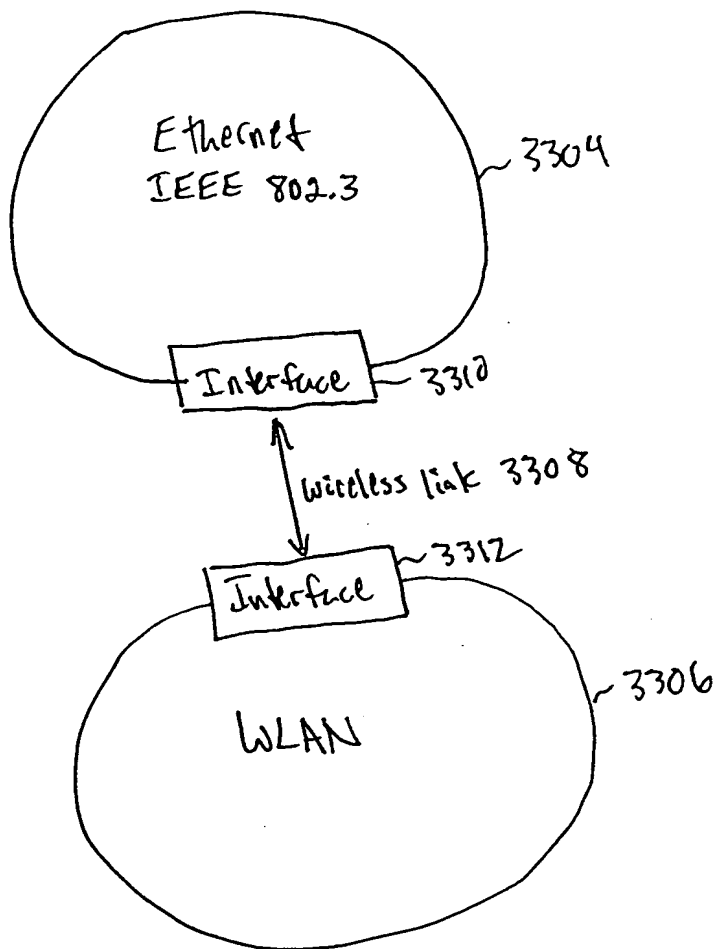


FIG. 33

3402
↓

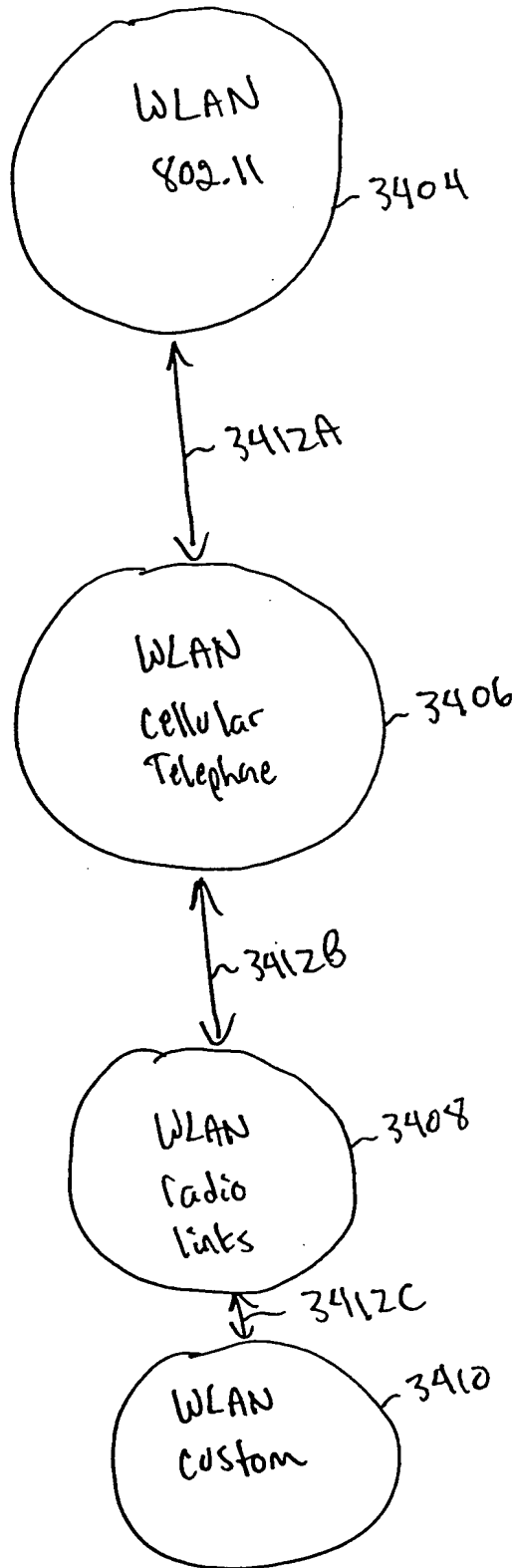
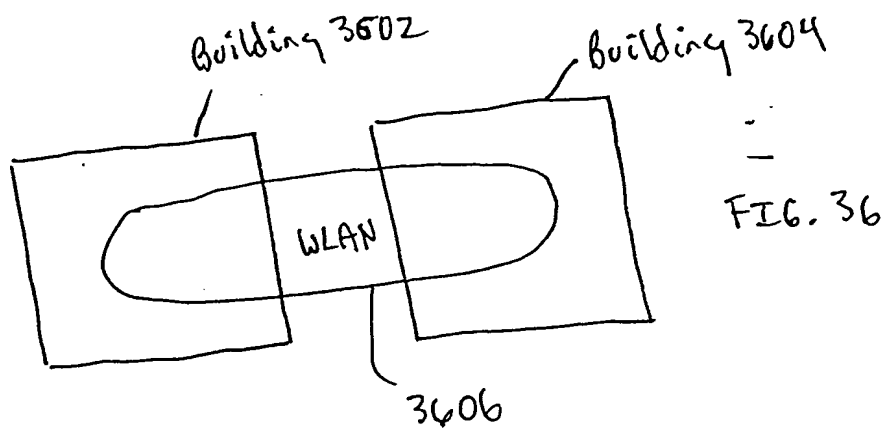
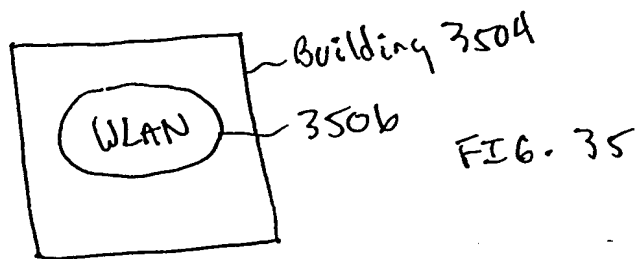


FIG. 34

3502



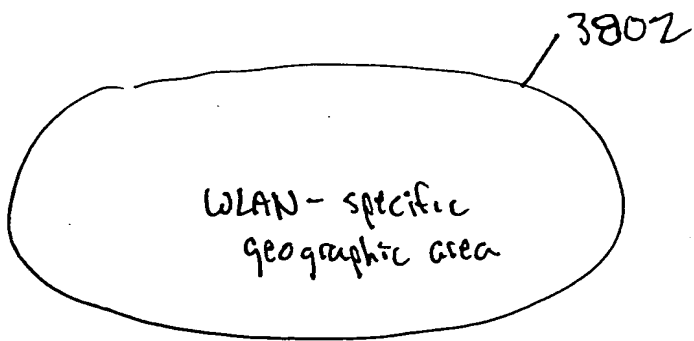
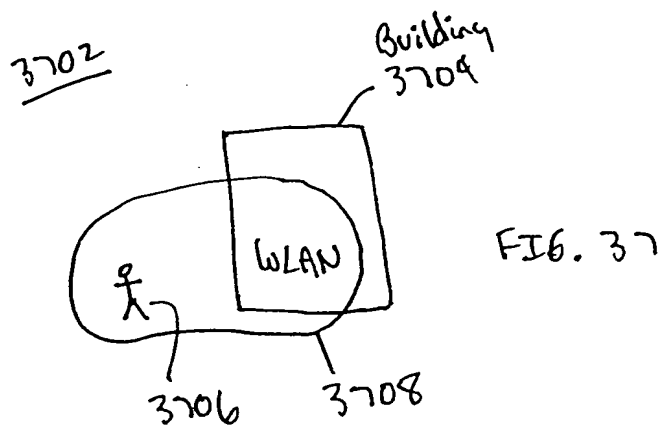


FIG. 38

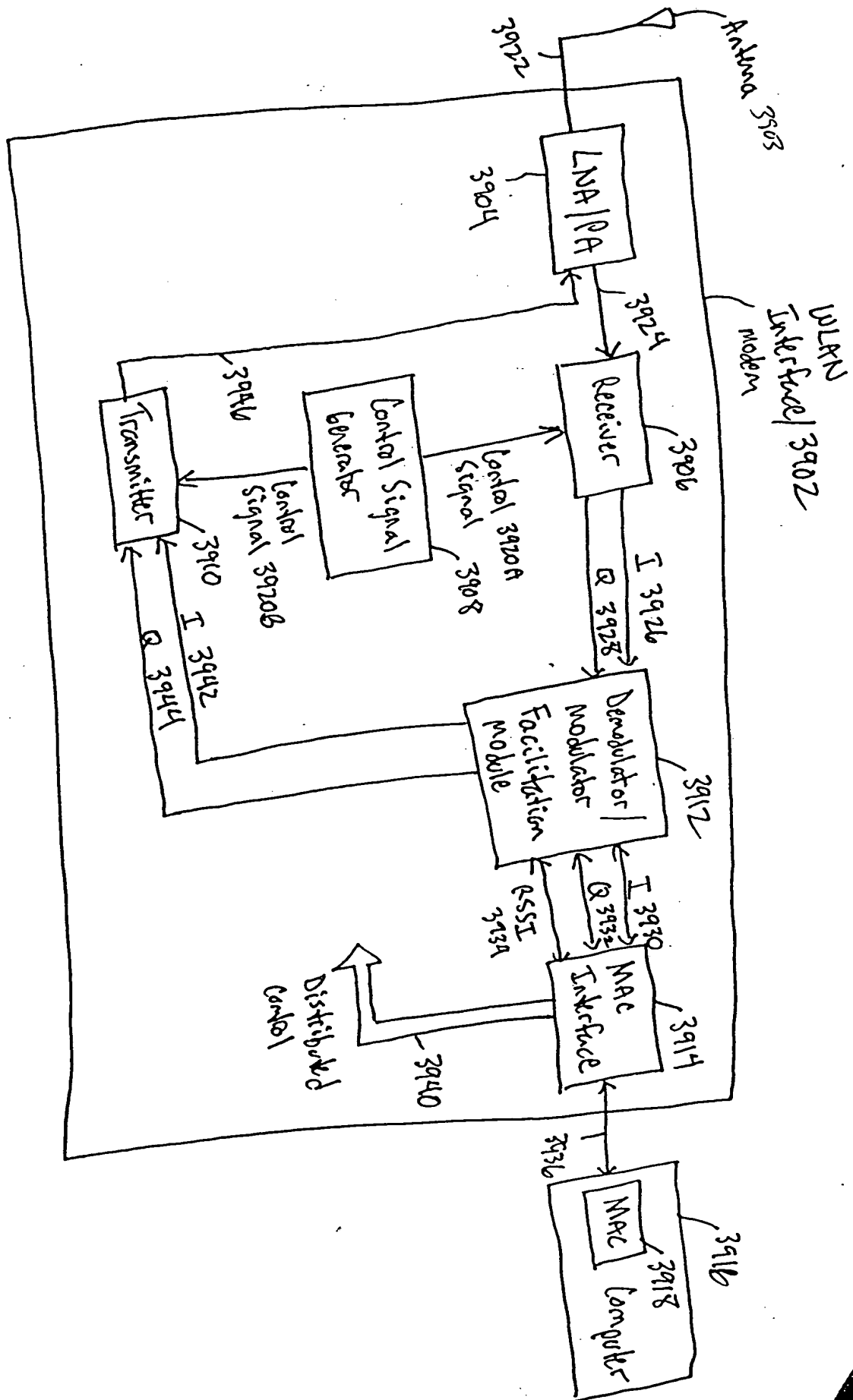


FIG. 39

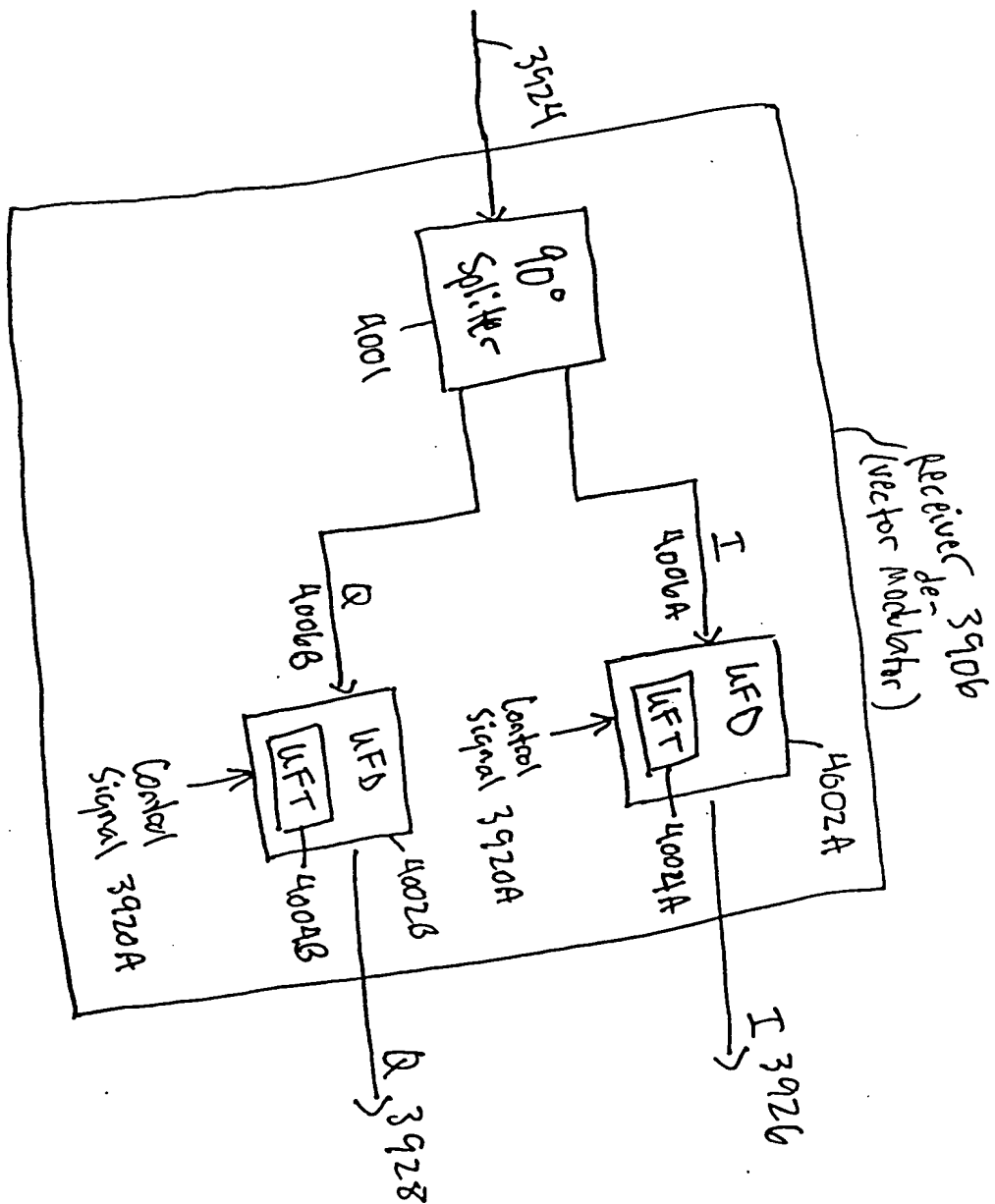


FIG. 40

FIG. 40 is a block diagram of a receiver system.

FIG. 40

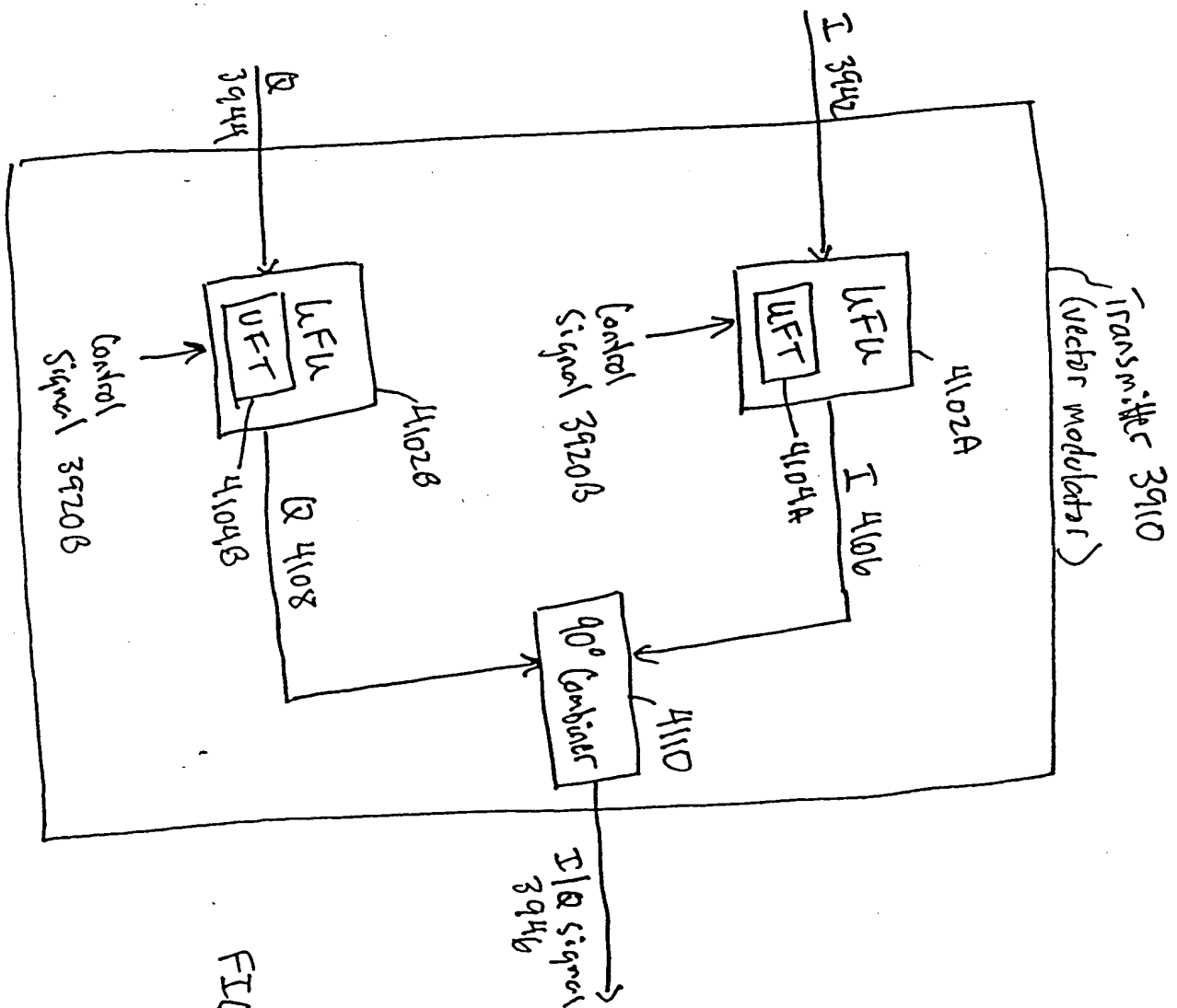
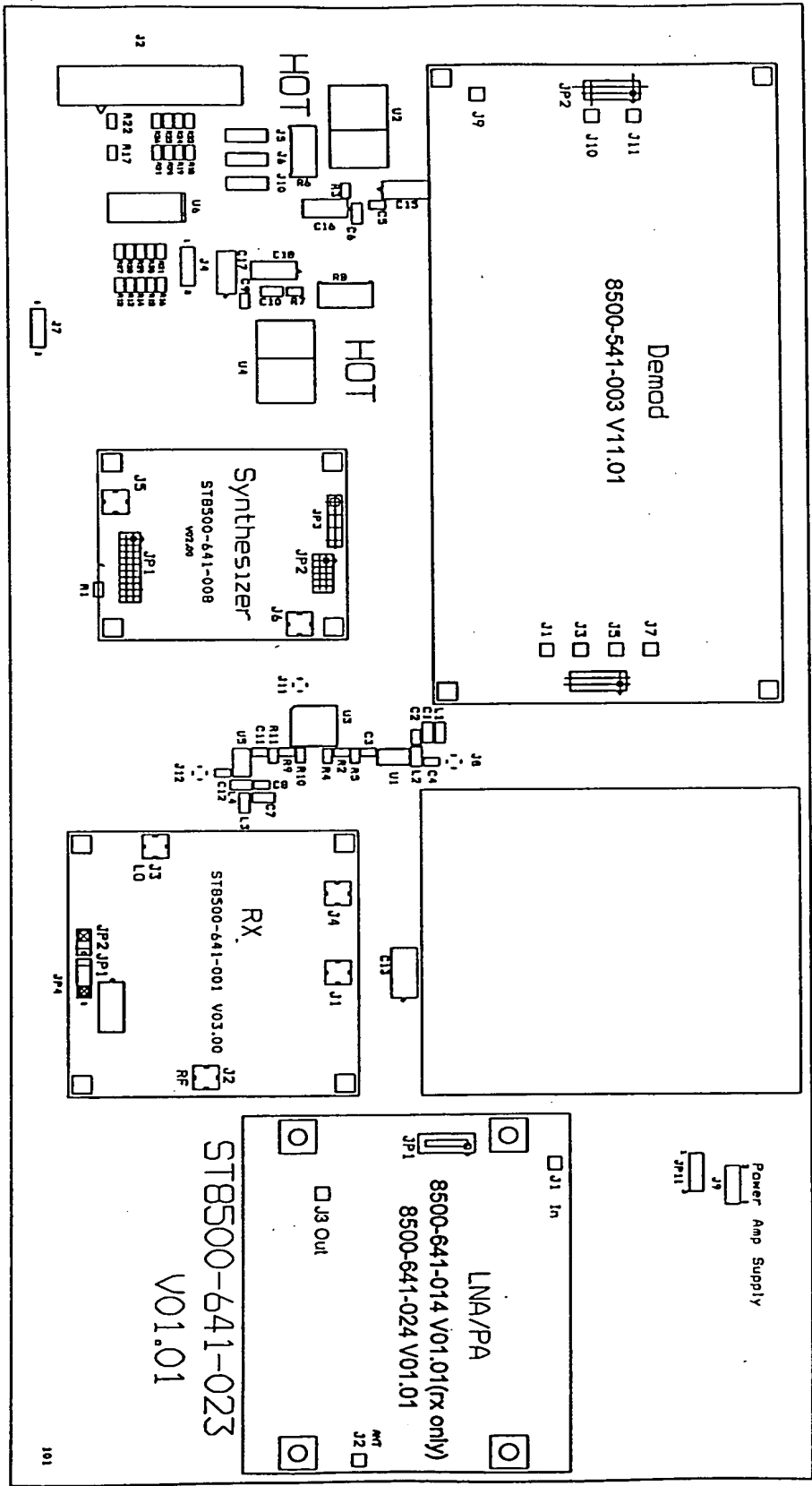


FIG. 41

4302

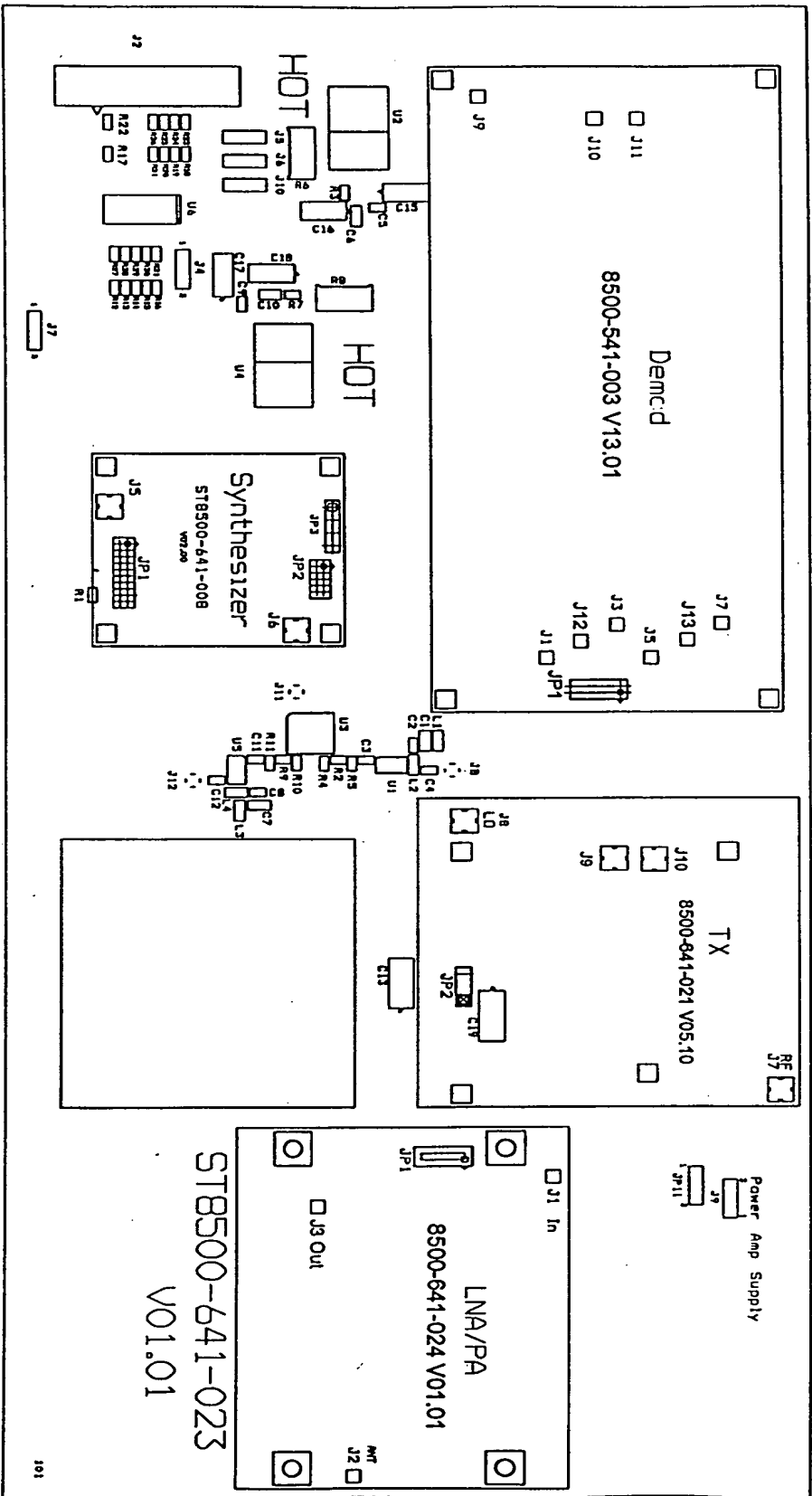


Receive Only

FIG. 43

FIG. 43 is a block diagram of the receiver system of the present invention. The receiver system includes a power amp supply, a demodulator, a synthesizer, a receiver, and a low noise amplifier/preamplifier. The power amp supply is connected to the demodulator, the synthesizer, the receiver, and the low noise amplifier/preamplifier. The demodulator is connected to the synthesizer, the receiver, and the low noise amplifier/preamplifier. The synthesizer is connected to the receiver and the low noise amplifier/preamplifier. The receiver is connected to the low noise amplifier/preamplifier.

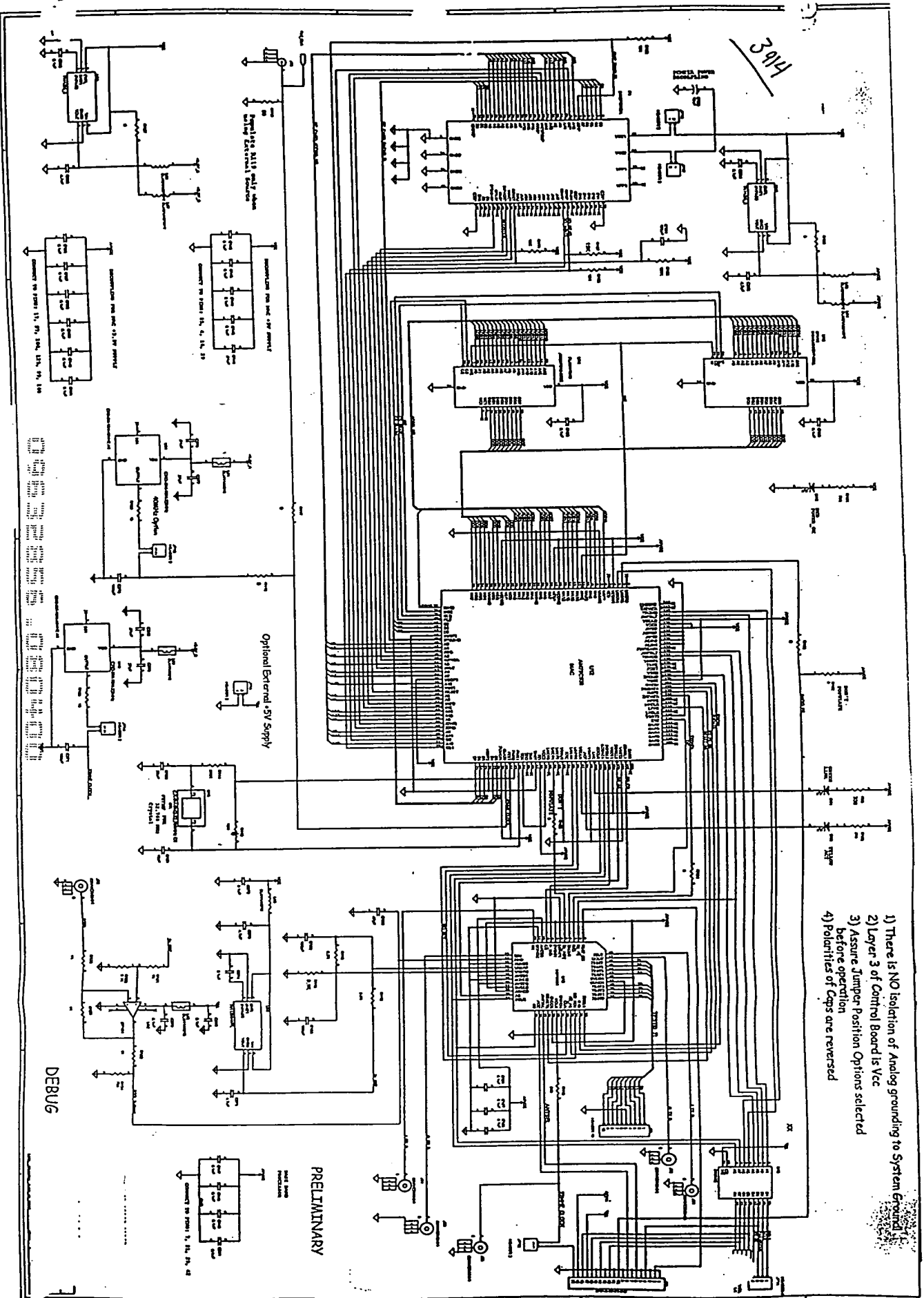
4402



Transmit Only

FIG. 44

Fig. 45



PARK VISION PCMCIA CONTROLLER BOM

Item	Quantity	Reference	Part Description	Part Number	Manufacturer
1	1	C123	Part Description 10uF CAP 6032, Tantalum,20%	TAJT106K010R	Kemet
2	3	C263, C273, C275, C282	4.7uF CAP 6032,Tantalum,20%	T491A475M006AS	Kemet
3	25	C120, C125, C126, C127, C128, C136, C137, C138, C139, C140, C141, C142, C143, C144, C145, C147, C148, C149, C264, C272, C274, C279, C280, C281, C283	0.1uF CAP 0603,X7R,10% GRM39X7R104K050AD		Murata
4	3	C146, C269, C276	.01uF CAP 0603,X7R,10% GRM39X7R103K050AD		Murata
5	5	C124, C132, C133, C271, C278	100pF CAP 0603,X7R,10% GRM39COG101K050AD		Murata
6	1	C129	47pF CAP 0603,X7R,10% GRM39COG470J100AD		Murata
7	2	C270, C277	27pF CAP 0603,X7R,10% GRM39COG270K050AD		Murata
8	1	C130	22pF CAP 0603,X7R,10% GRM39COG220K050AD		Murata
9	1	C131	10pF CAP 0603,X7R,10% GRM39COG100D050AD		Murata
10	1	DS1	LED, Green 597-3311-420		Dialight
11	1	DS2	LED Yellow 597-3401-420		Dialight
12	1	DS3	LED Red 597-3111-420		Dialight
13	6	JP12, JP13, JP14, JP15, JP16, JP17	Connector HEADER 2Pin 2MS-19-33-01		Specialty Electronics
14	1	JP11	Connector HEADER 4Pin 100VH/TM1SQW.100/4		BLKCON
15	7	J16, J20, J21, J22, J23, J24, J25	Connector 82MMCX 82MMCX-50-0-1		Huber/Shuner
16	1	J18	Connector Header10 TMS-110-01-G-S		samtec
17	1	J19	Connector with Ejector EHT-1-10-01-S-D		samtec
18	1	P1	Connector 34X2PCMCIA DICMJ-68S-SPC-M08		ITT Canon
19	7	L59, L60, L61, L63, L64, L65, L66	Fertile Bead BLM11A121S		Murata
20	1	R112	10M, Resistor, 0603, 5%	ERJ-3GSYJ394V'	Panasonic
21	1	R114	390K, Resistor, 0603, 5%	ERJ-3GSYJ104V	Panasonic
22	1	R105	100K, Resistor, 0603, 5%	ERJ-3GSYJ153V	Panasonic
23	1	R106, R107, R108, R111	15K, Resistor, 0603, 5%	ERJ-3GSYJ912V	Panasonic
24	4	R116	9.1K, Resistor, 0603, 5%	ERJ-3GSYJ822V	Panasonic
25	1	R115	8.2K, Resistor, 0603, 5%	ERJ-3GSYJ392V	Panasonic
26	1	R113	3.9K, Resistor, 0603, 5%	ERJ-3GSYJ751V	Panasonic
27	1	R101	750, Resistor, 0603, 5%	ERJ-3GSYJ561V	Panasonic
28	1	R110	560, Resistor, 0603, 5%	ERJ-3GSYJ331V	Panasonic
29	2	R99, R100	100K, Resistor, 0603, 5%	ERJ-3GSYJ331V	Panasonic
30	2		100K, Resistor, 0603, 5%	ERJ-3GSYJ331V	Panasonic

FI 6.46A

31	1	R119	50, Resistor, 0603, f	ERJ-3GSYJ500V	Panasonic
32	2	R128, R129	10, Resistor, 0603, 5%	ERJ-3GSYJ100V	Panasonic
33	8	R102, R103, R104, R109, R117, R118, R120, R127	0, Resistor, 0603, 5%	RM732Z1J000ZT	ERJ-KOA
34	6	R121, R122, R123, R124, R125, R126	TBD, Resistor, 0603, 5%	3GSYJ000V	Panasonic
35	1	U10	SRAM	R	Panasonic
36	1	U12	MAC	KM62256DLTG-5L	Samsung
37	1	U13	Baseband Processor	M5M5256CVP-55LL	Mitsubishi
38	1	U14	FLASH RAM	AM79C930	AMD
39	1	U15	32 KHz Crystal	HFA3842 A1	Harris
40	2	U45	Bus Buffer	AM29F010-55EC	AMD
41	1	U48	Regulator 3.5 V	CX-6V-SM2-32.768KHz C/I	Statek
42	1	U49	22MHz Oscillator	DS3862	National
43	1	U50	2 Volt Reference	TK11235BMC	TOKO
44	1	U51	40MHz Oscillator	FOX F3346-22MHz	FOX
				TK11220BMC	TOKO
				CXO-M-10N-40MHz A/I	Statek

FIG. 46B

3412

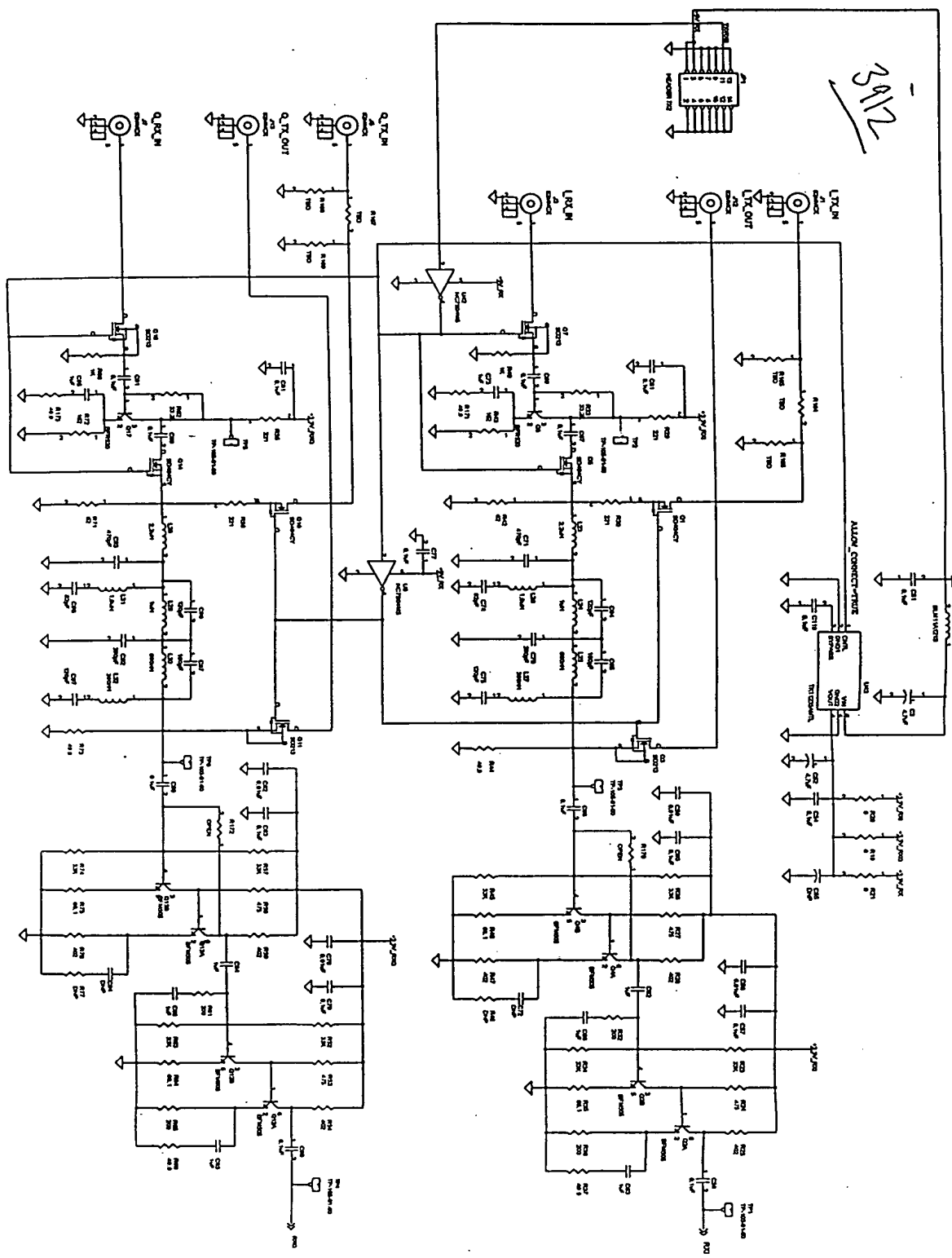


FIG. 47

FIG. 47 is a schematic diagram of a multi-channel system. The system includes a central multi-channel amplifier with four input channels labeled LTX IN, LTX OUT, QTX IN, and QTX OUT. Each channel includes a pre-amplifier stage with a 100k resistor and a 10pF capacitor, followed by a 10k resistor and a 10pF capacitor. The central amplifier has a 10k resistor and a 10pF capacitor. The output of the central amplifier is connected to a 10k resistor and a 10pF capacitor. The circuit also includes a 10k resistor and a 10pF capacitor. The circuit is powered by a 10k resistor and a 10pF capacitor. The circuit is labeled with various components such as resistors, capacitors, and transistors. The circuit is a multi-channel system with four input channels and a central amplifier. The circuit is labeled with various components such as resistors, capacitors, and transistors.

Item	Quantity	Reference	Part	Part Number	Manufacturer
1	4	C3,C52,C108,C110	4.7uF	T491A475K006AS	KEMET
2	26	C51,C54,C57,C58,C60,C61, C67,C68,C69,C77,C79,C80, C81,C83,C89,C90,C91,C111, C112,C113,C114,C115,C116, C117,C118,C119	0.1uF	GRM39Y5V104Z016	Murata
3	1	C55	DNP	T491A475K006AS	KEMET
4	8	C56,C59,C78,C82,C99,C101, C103,C104	0.01uF	GRM39X7R103K050	Murata
5	8	C62,C63,C66,C73,C84,C85, C88,C95	1uF	GRM40Y5V105Z016	Murata
6	4	C64,C75,C86,C97	120pF	GRM39COG121J050	Murata
7	2	C65,C87	180pF	GRM39COG181J050	Murata
8	2	C70,C92	390pF	GRM39COG391J050	Murata
9	2	C71,C93	470pF	GRM39COG471J050	Murata
10	2	C72,C94	DNP	GRM40Y5V105Z016	Murata
11	2	C74,C96	82pF	GRM39COG820J050	Murata
12	2	C100,C106	DNP	DNP	Murata
13	2	C105,C102	1000pF	GRM39COG102K050	Murata
14	2	D3,D1	BAW56WT1	BAW56WT1	Motorola
15	2	D4,D2	BAV70LT1	BAV70LT1	Motorola
16	1	JP1	HEADER 7X2	FTSH-107-02-L-D	Samtec
17	9	J1,J3,J5,J7,J9,J10,J11, J12,J13	82MMCX	82MMCX-50-0-1	Suhner
18	1	L1	BLM11A121S	BLM11A121S	Murata
19	2	L23,L28	2.2uH	LQG21N2R2K10	Murata
20	2	L29,L24	1uH	LQG21N1R0K10	Murata
21	2	L30,L25	680nH	LQG21NR68K10	Murata
22	2	L26,L31	1.8uH	LQG21N1R8K10	Murata
23	2	L32,L27	390nH	LQG21NR39K10	Murata
24	4	Q1,Q5,Q10,Q14	SD404CY	SD404CY	Calogic
25	4	Q2,Q4,Q12,Q13	BFM505	BFM505	Philips
26	4	Q3,Q7,Q11,Q16	SD213	SD213	Calogic
27	2	Q17,Q8	BFR520	BFR520	Philips
28	4	R19,R20,R21,R83	0	ERJ3GSY0R00	Panasonic
29	8	R23,R26,R34,R45,R52,R57, R63,R74	33K	ERJ3GSYJ333	Panasonic
30	4	R24,R27,R53,R58	475	ERJ3EKF4750	Panasonic
31	6	R25,R28,R47,R54,R59,R76	402	ERJ3EKF4020	Panasonic
32	4	R29,R30,R55,R56	221	ERJ3EKF2210	Panasonic
33	2	R32,R61	200	ERJ3GSYJ201	Panasonic
34	2	R33,R62	33.2K	ERJ3GSYJ333	Panasonic
	4	R35,R46,R64,R75	68.1	ERJ3EKF68R1	Panasonic

FIG. 49A

36	2	R36,R65	200	ERJ3EKF2000	Panasonic
7	6	R37,R44,R66,R73,R171, R173	49.9	ERJ3EKF49R9	Panasonic
38	6	R40,R68,R78,R79,R80,R89	1K	ERJ3EKF1001	Panasonic
39	2	R42,R71	62	ERJ3GSYJ620	Panasonic
40	2	R43,R72	162	ERJ3EKF1620	Panasonic
41	2	R77,R48	DNP	ERJ3GSYJ330	Panasonic
42	4	R81,R82,R85,R87	2K	ERJ3EKF2001	Panasonic
43	1	R84	909	ERJ3EKF9090	Panasonic
44	1	R88	15K	ERJ3EKF1502	Panasonic
45	1	R90	10K	ERJ3EKF1002	Panasonic
46	2	R91,R92	100	ERJ3EKF1000	Panasonic
47	6	R164,R165,R166,R167,R168, R169	TBD		Panasonic
48	2	R170,R172	OPEN		Panasonic
49	6	TP1,TP2,TP3,TP4,TP5,TP6	TP-105-01-00		
50	2	U42,U6	NC7S04M5	NC7S04M5	National Semiconductor
51	1	U7	AD8052AR	AD8052AR	Analog Devices
52	1	U8	AD1582	AD1582	Analog Devices
53	1	U9	AD605AR	AD605AR	Analog Devices
54	1	U43	TK11235AMTL	TK11235BM	Toko

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FIG. 49B

Bill Of Materials

Item	Quantity	Reference	Part	Part Number	Manufacturer
1	3	C3,C52,C55	4.7uF	T491A475K006AS	KEMET
2	26	C51,C54,C57,C58,C60,C61, C67,C68,C69,C77,C79,C80, C81,C83,C89,C90,C91,C111, C112,C113,C114,C115,C116, C117,C118,C119	0.1uF	GRM39Y5V104Z016	Murata
3	8	C56,C59,C78,C82,C99,C101, C103,C104	0.01uF	GRM39X7R103K050	Murata
4	10	C62,C63,C66,C72,C73,C84, C85,C88,C94,C95	1uF	GRM40Y5V105Z016	Murata
5	4	C64,C75,C86,C97	120pF	GRM39COG121J050	Murata
6	2	C87,C65	180pF	GRM39COG181J050	Murata
7	2	C70,C92	390pF	GRM39COG391J050	Murata
8	2	C71,C93	470pF	GRM39COG471J050	Murata
9	2	C96,C74	82pF	GRM39COG820J050	Murata
10	5	C100,C102,C105,C106,C107	100pF	GRM39COG101K050	Murata
11	1	C108	1uF		
12	1	C110	4.7uF		
13	2	D3,D1	BAW56WT1	BAW56WT1	Motorola
14	2	D4,D2	BAV70LT1	BAV70LT1	Motorola
15	2	JP2,JP1	HEADER 7X2		
16	6	J1,J3,J5,J7,J10,J11	82MMCX	142-0701-231	Johnson
17	1	J9	82MMCX	82MMCX-50-0-1	Suhner
18	1	L1	BLM11A121S	BLM11A121S	Murata
19	2	L28,L23	2.2uH	LQG21N2R2K10	Murata
20	2	L24,L29	1uH	LQG21N1R0K10	Murata
21	2	L30,L25	680nH	LQG21NR68K10	Murata
22	2	L26,L31	1.8uH	LQG21N1R8K10	Murata
23	2	L27,L32	390nH	LQG21NR39K10	Murata
24	4	Q1,Q5,Q10,Q14	SD404CY	SD404CY	Calogic
25	4	Q2,Q4,Q12,Q13	BFM505	BFM505	Philips
26	4	Q3,Q7,Q11,Q16	SD213	SD213	Calogic
27	2	Q17,Q8	BFR520	BFR505	Philips
28	5	R19,R20,R21,R171,R173	0		
29	8	R23,R26,R34,R45,R52,R57, R63,R74	33K	ERJ3GSYJ333	Panasonic
30	4	R24,R27,R53,R58	475	ERJ3EKF4750	Panasonic
31	6	R25,R28,R47,R54,R59,R76	402	ERJ3EKF4020	Panasonic
32	4	R29,R30,R55,R56	221	ERJ3EKF2210	Panasonic
33	2	R32,R61	200	ERJ3GSYJ201	Panasonic
34	2	R33,R62	33.2K	ERJ3GSYJ333	Panasonic
	4	R35,R46,R64,R75	68.1	ERJ3EKF68R1	Panasonic
	2	R36,R65	200	ERJ3EKF2000	Panasonic

FIG. 52A

37	2	R66,R37	49.9	ERJ3EKF49R9	Panasonic
8	6	R40,R68,R78,R79,R80,R89	1K	ERJ3EKF1001	Panasonic
39	2	R42,R71	62	ERJ3GSYJ620	Panasonic
40	2	R43,R72	162	ERJ3EKF6810	Panasonic
41	2	R44,R73	49.9	ERJ3EKF1001	Panasonic
42	2	R77,R48	33	ERJ3GSYJ330	Panasonic
43	4	R81,R82,R85,R87	2K	ERJ3EKF2001	Panasonic
44	1	R83	0	ERJGSY0R00	Panasonic
45	1	R84	1.1K	ERJ3EKF2001	Panasonic
46	1	R88	15K	ERJ3EKF1502	Panasonic
47	1	R90	10K	ERJ3EKF1002	Panasonic
48	2	R91,R92	100	ERJ3EKF1000	Panasonic
49	6	R164,R165,R166,R167,R168,	TBD		
		R169			
50	2	R170,R172	OPEN		
51	6	TP1,TP2,TP3,TP4,TP5,TP6	TP-105-01-00		
52	2	U42,U6	NC7S04M5		National Semiconductor
53	1	U7	AD8032AR	AD8032AR	Analog Devices
54	1	U8	AD1582	AD1582	Analog Devices
55	1	U9	AD605AR	AD605AR	Analog Devices
56	1	U43	TK11235AMTL	TK11235AMTL	Toko

FIG. 52B

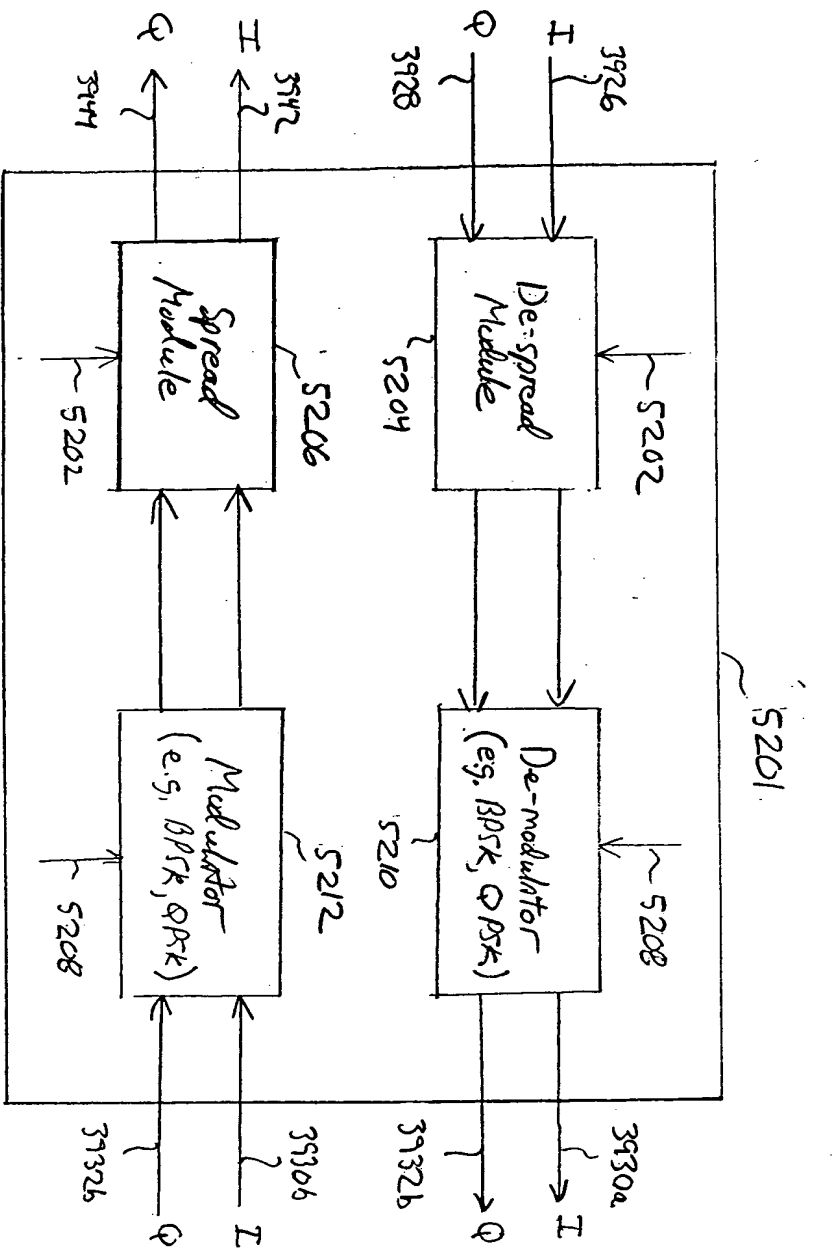


Fig. 52C

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Item	Quantity	Reference	Part	Part Number	Manufacturer
1	10	C/R7,C/R15,C16,C17,C18,C19,C21,C22,C23,C24	0.1uF	GRM39Y5V104Z016	Murata
2	6	C1,C3,C6,C8,C9,C12	22pF	GRM39COG220J050	Murata
3	3	C2,C4,C11	0.1uF	GRM39X7R104K016	Murata
4	2	C5,C15	47pF	GRM39COG470J050	Murata
5	2	C10,C7	1000pF	GRM39X7R102K050	Murata
6	1	C13	100pF	GRM39X7R101J050	Murata
7	1	C14	3pF	GRM40COG030B50V	Murata
8	2	C20,C25	1uF	GRM40Y5V105Z016	Murata
9	1	JP1	69190-403	69190-403	BERG
10	1	JP2	69190-402	69190-402	BERG
11	4	J1,J2,J3,J4	82MMCX-50-0-1	82MMCX-50-0-1	Suhner
12	2	L3,L1	DNP	L	TOKO
13	2	L4,L2	4.7nH	LL1608-F4N7K	TOKO
14	1	L5	15nH	LL2012FH15NJ	TOKO
15	1	L6	DNP	DNP	TOKO
16	2	Q1,Q2	BFR520	BFR520	Philips
17	2	R1,R3	2K	ERJ3GSYJ202	Panasonic
18	1	R2	51	ERJ3GSYJ510	Panasonic
19	2	R4,R12	221	ERJ3EKF2210	Panasonic
20	6	R5,R6,R8,R13,R14,R16	33.2K	ERJ3EKF3322	Panasonic
21	2	R9,R17	DNP	ERJ3EKF1001	Panasonic
22	2	R10,R18	249	ERJ3EKF2490	Panasonic
23	2	R11,R19	10	ERJ3GSYJ100	Panasonic
24	1	U1	D2D_V4	D2D_V4	Parker Vision
25	1	U2	1X603	1X603	Anaren
26	1	U3	AD8032AR	AD8032AR	Analog Devices
27	1				

Based 5TB500 LA1.001 V03.00

FIG. 54

Item	Qty	Reference	Part	Description	Part Number	Manufacturer
1	1	CR1	BBY51-E6327	Diode, Varactor	BBY51-E6327	Siemens
2	6	C1,C3,C5,C7,C9,C10	100pF	Capacitor, ceramic, 100pF, 10%, COG, 0603	GRM39COG101K050	Murata
3	2	C29,C2	0.1uF	Capacitor, ceramic, .1uF, 10%, X7R, 0603	GRM39X7R104K016AD	Murata
4	3	C4,C8,C17	.01uF	Capacitor, ceramic, .01uF, 10%, X7R, 0603	GRM39X7R103K050	Murata
5	1	C6	220pF	Capacitor, ceramic, 220pF, 5%, COG, 0603	GRM39COG221J025	Murata
6	1	C11	3.3pF	Capacitor, ceramic, 3.3pF, 5%, COG, 0603	GRM39COG33B100V	Murata
7	1	C12	6.8pF	Capacitor, ceramic, 6.8pF, +/-25pF, COG, 0603	GRM39COG68C100V	Murata
8	4	C13,C35,C36,C37	1000pF	Capacitor, ceramic, 1000pF, 10%, X7R, 0603	GRM39X7R102K016	Murata
9	1	C14	1500pF	Capacitor, ceramic, 1500pF, 10%, X7R, 0603	GRM39X7R152K016	Murata
10	1	C15	12pF	Capacitor, ceramic, 12pF, 5%, COG, 0603	GRM39COG12J050	Murata
11	1	C16	4700pF	Capacitor, ceramic, 4700pF, 10%, 0603	GRM39X7R472K016	Murata
12	2	C20,C18	22pF	Capacitor, ceramic, 22pF, 10%, COG, 0603	GRM36COG220K050	Murata
13	4	C22,C32,C33,C34	DNP	Capacitor, ceramic, . . . , 0603		
14	3	C23,C24,C27	4.7uF	Capacitor, tantalum, 4.7uF, 10%, 3216	T491A475K006AS	Kemet
15	2	R16,C31, R17	0 ohm	Resistor, zero ohm, 0603	ERJ3G5Y0R00	Panasonic
16	1	JP1	FTSH-110-02-F-D	Header, dual row 10x2, .050x.050	FTSH-110-02-F-D	Samtec
17	1	JP2	FTSH-105-02-F-D	Header, dual row 5x2, .050x.050	FTSH-105-02-F-D	Samtec
18	1	JP3	TSW-104-08-T-S	Header, single row 4 pin, .100"	TSW-104-08-T-S	Berg
19	2	J5,J6	82MMCX	RF Connector	82MMCX-50-0-1	Suher
20	1	L1	18nH	Inductor, 18nH, 10%, 0805	0805CS-180XJBC	Collcraft
21	1	L3	0 Ohm	Zero Ohm Jumper	RM73ZJUT	KOA
22	6	L4,L6,L9,L10,L11,L12	BLM11A121S	Ferrite Bead, 0603	BLM11A121S	Murata
23	1	L14	82nH	Inductor, 82nH, 10%, 0805	LL2012-F82NK	Toko
24	1	Q1	BFR520	Transistor, NPN	BFR520	Philips
25	5	R1,R2,R3,R11,R30	1K	Resistor, 1K, 5%, 0603	ERJ3G5YJ102	Panasonic
26	1	R4	10	Resistor, 10 ohm, 5%, 0603	ERJ3G5YJ1R0	Panasonic
27	1	R8	2K	Resistor, 2K, 5%, 0603	ERJ3G5YJ202	Panasonic
28	2	R9,R17	75	Resistor, 75 ohm, 5%, 0603	ERJ3G5YJ750	Panasonic
29	1	R10	3300	Resistor, 3.3K, 5%, 0603	ERJ3G5YJ332	Panasonic
30	1	R12	13K	Resistor, 13K, 5%, 0603	ERJ3G5YJ133	Panasonic
31	1	R13	1.5K	Resistor, 1.5K, 5%, 0603	ERJ3G5YJ152	Panasonic

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122F

32	1	R14	220	Resistor, 220 ohm, 5%, 0603	ERJ3GSYJ221	Panasonic
33	1	R15	DNP	Resistor, zero ohm, 0603	ERJ3GSY0R00	Panasonic
34	2	R18,R19	DNP	Resistor, 91 ohm, 5%, 0603	ERJ3GSYJ910	Panasonic
35	1	R36	TBD	Resistor, zero ohm, 0603	ERJ3GSY0R00	Panasonic
36	1	R37	DNP	Resistor, ., 0603		Panasonic
37	1	TP1	Test Point			
38	1	U1	PE3282A	IC, Synthesizer	PE3282A	Peregrine
39	1	U2	CXO-3M-10N-40MHz	Xtal Osc, 40MHz	CXO-3M-10N-40MHZ A/I	Sialek
40	1	U4	TK11233AMTL	Voltage Regulator, 3.5V	TK11235BM	Toko
41	1	U5	74125	IC, BUFFER	MC74LCX125DT	Motorola
42	1	U6	UPC1678GV	IC, RF Amplifier	UPC1678GV	NEC

43 1

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Base 1

Ver.00

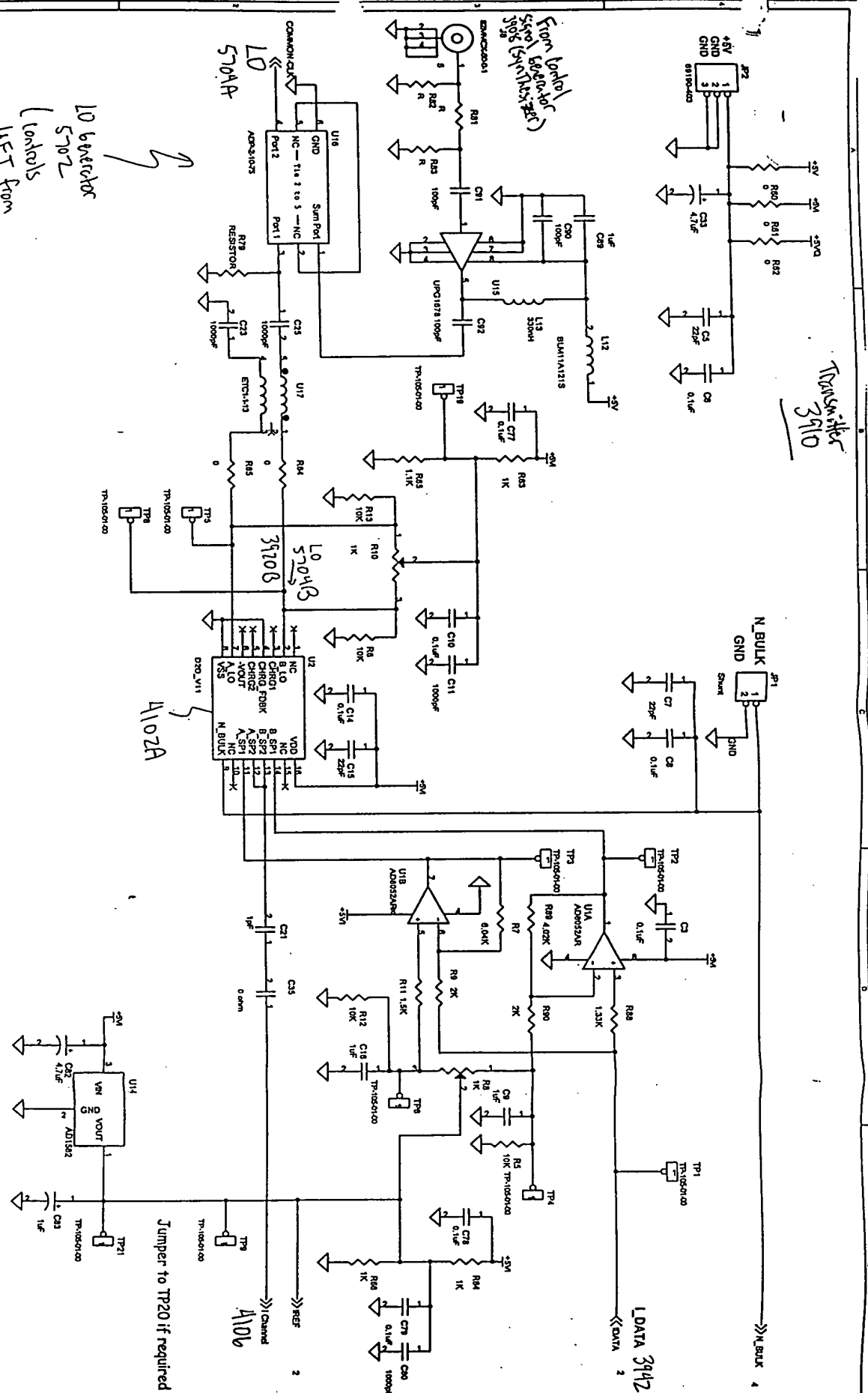
FIG. 56B

09632855 : 080400

LO buffer
5702
(controls
VFT from
base clock)

Transistor
3910

5702

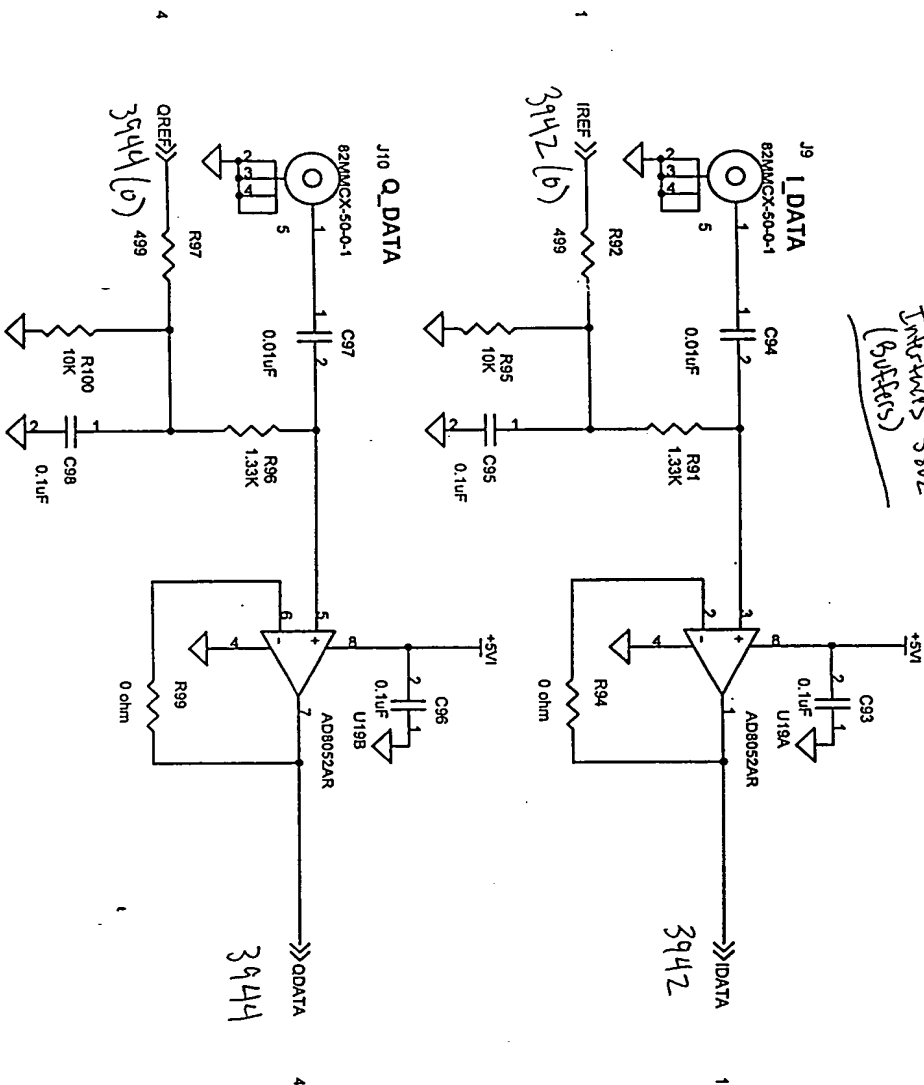


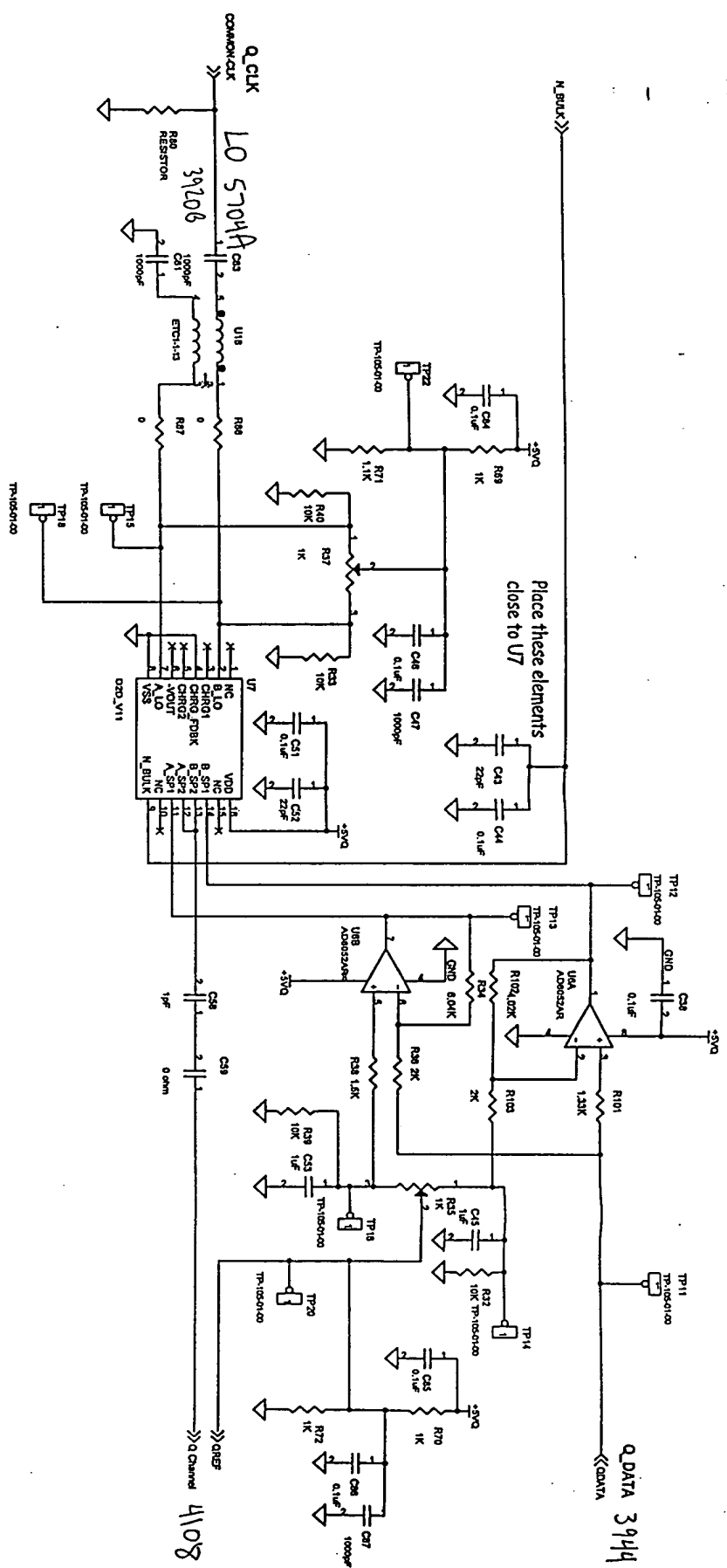
DATA 3942

4106

Jumper to TP20 if required

Data Conditioning
Inputs 5902
(Buffers)





FILE

Bill Of Materials

Item	Quantity	Reference	Part	Part Number	Manufacturer
1	21	C3,C6,C8,C10,C14,C38,C44, C46,C51,C71,C72,C77,C78, C79,C84,C85,C86,C93,C95, C96,C98	0.1uF	GRM39X7R104K016	Murata
2	6	C5,C7,C15,C43,C52,C75	22pF	GRM39COG220J050	Murata
3	5	C9,C16,C45,C53,C89	1uF	GRM40Y5V105Z016	Murata
4	8	C11,C23,C25,C47,C61,C63, C80,C87	1000pF	GRM39X7R102K050	Murata
5	2	C58,C21	1pF	GRM39COG010B50V	Murata
6	2	C82,C33	4.7uF	T491A475K006AS	KEMET
7	2	C59,C35	0 ohm	GRM39COGxxx50V	Murata
8	1	C73	470pF	GRM39COG471J050	Murata
9	1	C83	1uF	T491A105M016AS	Kemet
10	3	C90,C91,C92	100pF	ECU-V1H101JCV	
11	2	C94,C97	0.01uF	GRM39X7R103K016	Murata
12	1	FL1	MDR642E	MDR642E	Soshin
13	1	JP1	Shunt	69190-402	BERG
14	1	JP2	69190-403	69190-403	BERG
15	4	J7,J8,J9,J10	82MMCX-50-0-1	82MMCX-50-0-1	Suhner
16	1	L10	22nH	LL1608-F22NK	Coilcraft
17	1	L12	BLM11A121S	BLM11A121S	Murata
18	1	L13	330nH	LL2012-FR33K	
19	10	R5,R6,R12,R13,R32,R33, R39,R40,R95,R100	10K	ERJ3EKF1002	Panasonic
20	2	R34,R7	6.04K	ERJ3EKF6041	Panasonic
21	4	R8,R10,R35,R37	1K	3224W-1-102	Bourns
22	4	R9,R36,R90,R103	2K	ERJ3EKF2001	Panasonic
23	2	R38,R11	1.5K	ERJ3EKF1501	Panasonic
24	3	R56,R94,R99	0 ohm	ERJ3GSY0R00	Panasonic
25	1	R59	51	ERJ3GSYJ510	Panasonic
26	7	R60,R61,R62,R84,R85,R86, R87	0	ERJ3GSY0R00	Panasonic
27	6	R63,R64,R66,R69,R70,R72	1K	ERJ3EKF1001	Panasonic
28	2	R71,R65	1.1K	ERJ3EKF1101	Panasonic
29	2	R80,R79	RESISTOR		
30	3	R81,R82,R83	R		
31	4	R88,R91,R96,R101	1.33K	ERJ3EKF1331	Panasonic
32	2	R102,R89	4.02K	ERJ3EKF4021	Panasonic
33	2	R92,R97	499	ERJ3EKF4990	Panasonic
34	19	TP1,TP2,TP3,TP4,TP5,TP6,	TP-105-01-00		

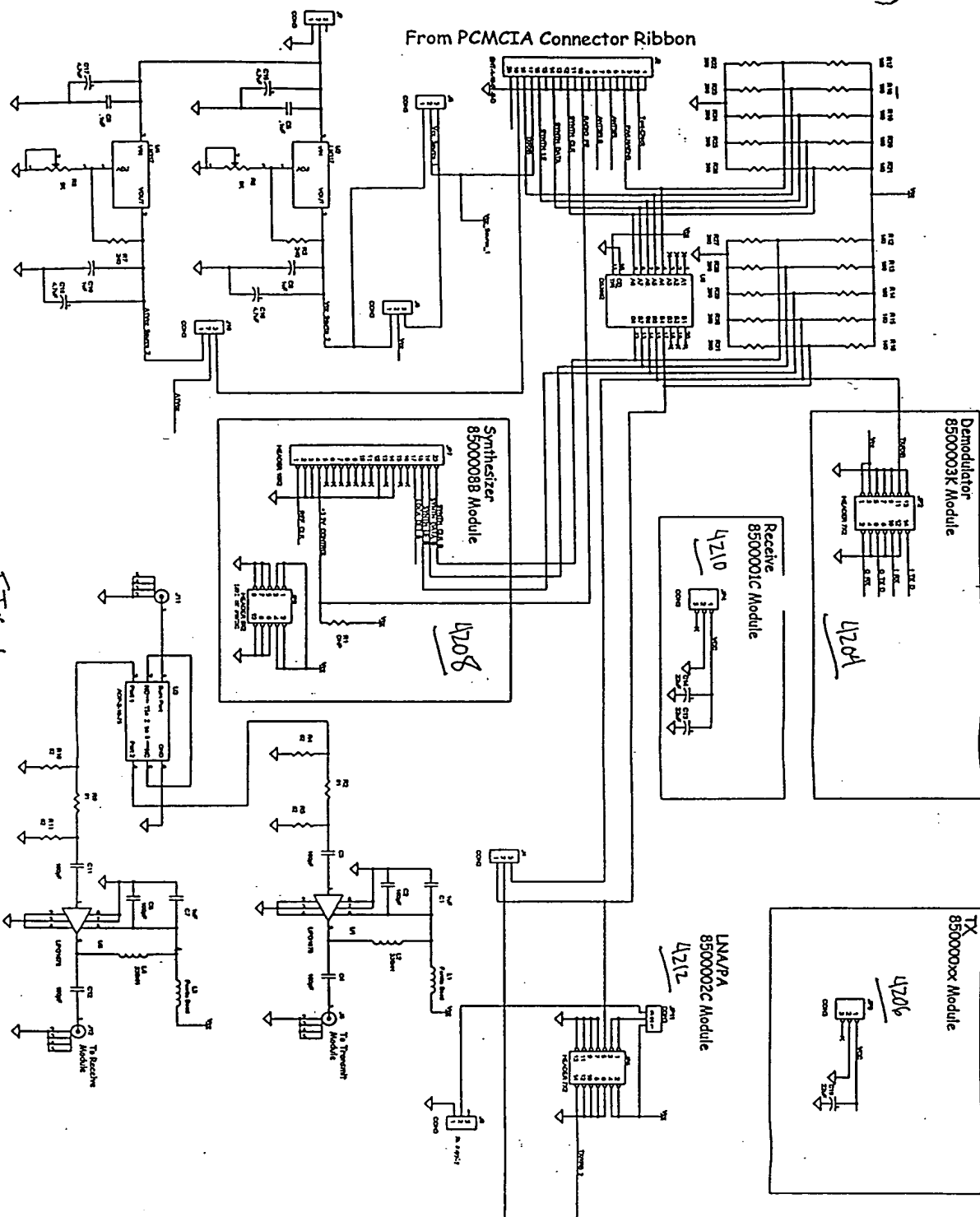
FIG. b1A

		TP8,TP9,TP11,TP12,TP13,			
		TP14,TP15,TP16,TP18,TP19,			
		TP20,TP21,TP22			
35	3	U1,U6,U19	AD8052AR	AD8052AR	Analog Devices
36	2	U7,U2	D2D_V11	D2D_V11	Parker Vision
37	1	U11	MAAM22010	MAAM22010	MACOM
38	1	U12	1X603	1X603	Anaren
39	1	U14	AD1582	AD1582	Analog Devices
40	1	U15	UPG1678	UPG1678GV	NEC
41	1	U16	ADP-2-10-75	ADP-2-10-75	Mini-Circuits

BOARD

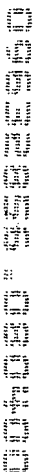
B500.641.021 V05.10

FIG. 6B



FILE 6.62
MOTHERBOARD FOR PCMCIA TEST BED

42/2



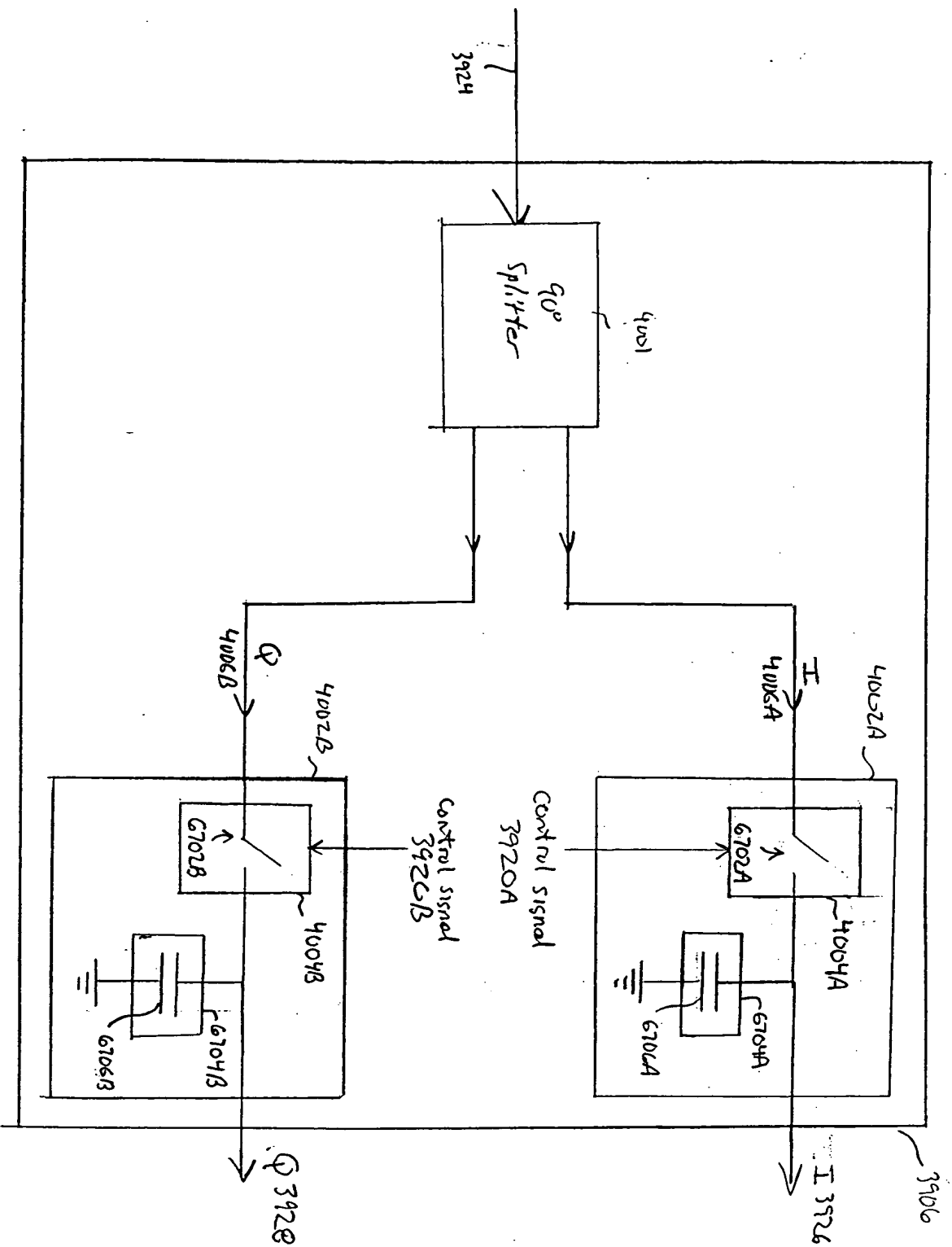


Fig. 674

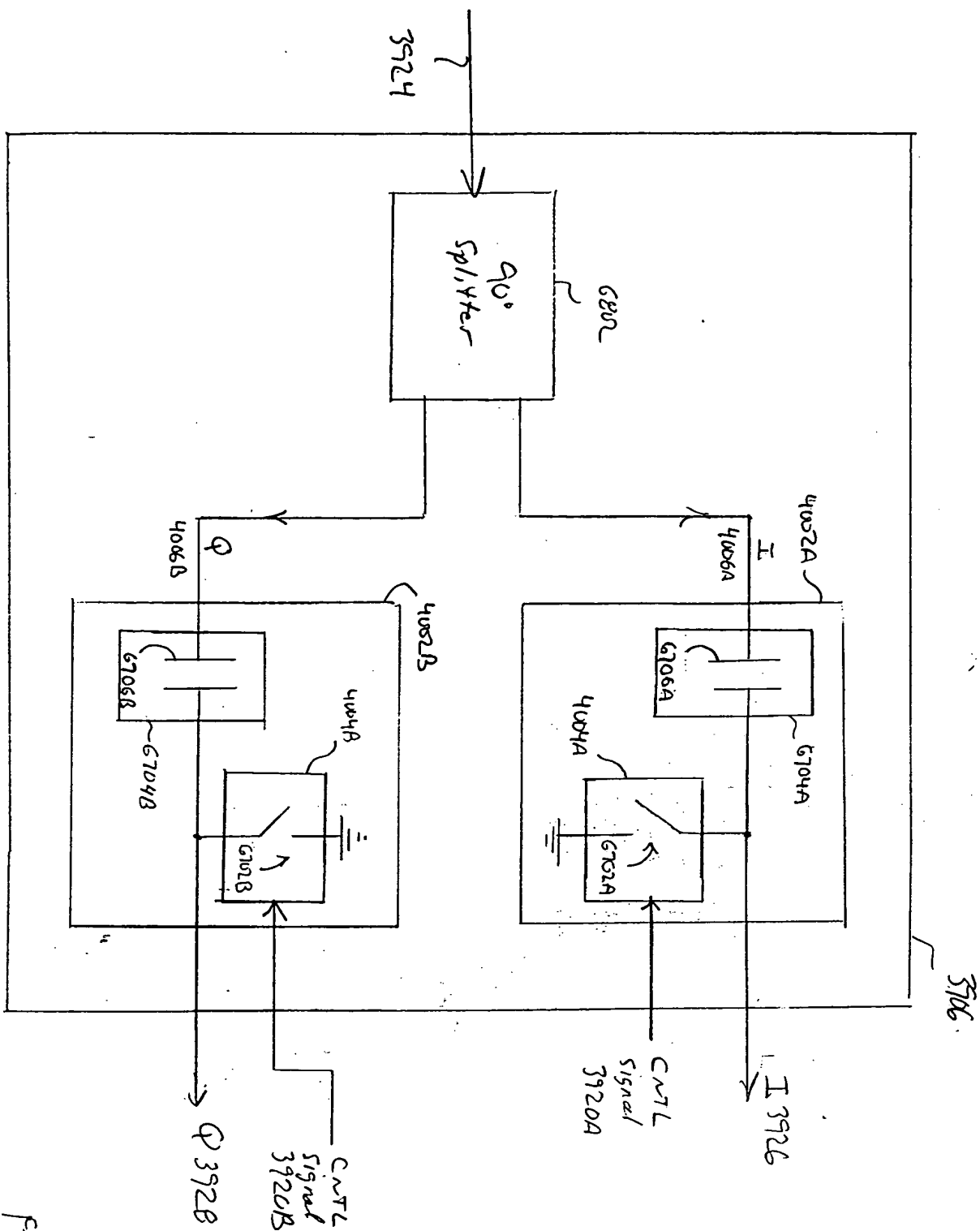
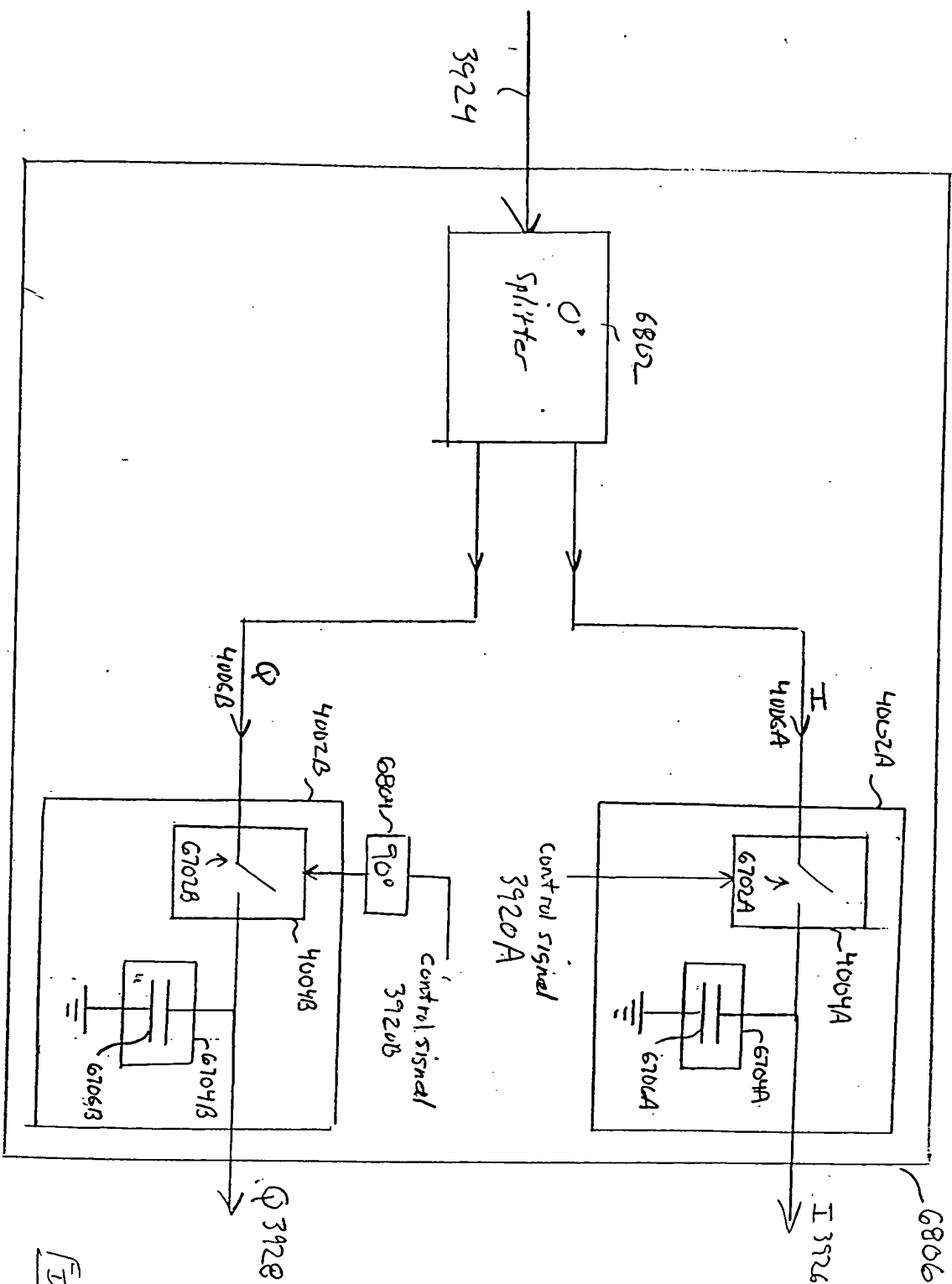
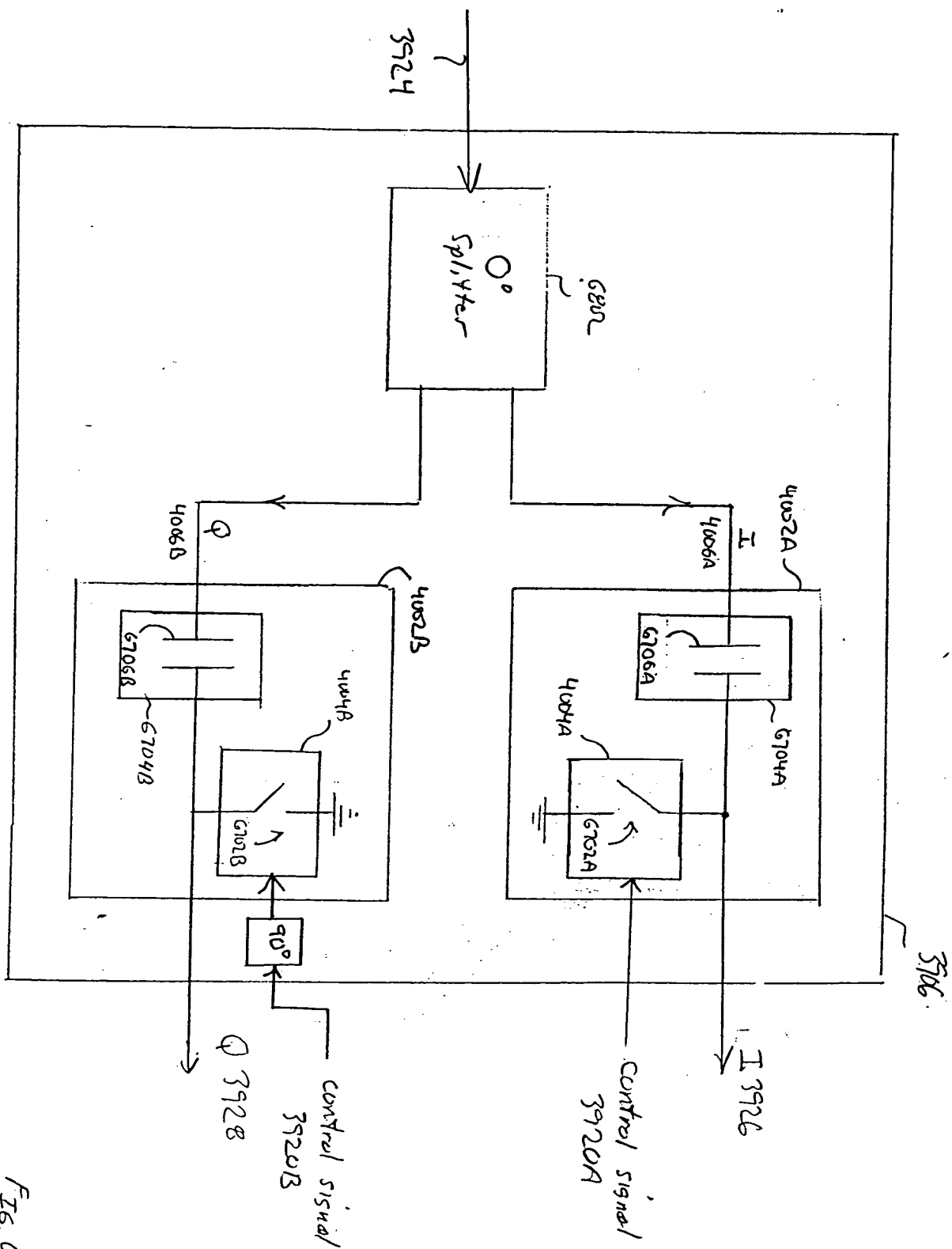


FIG. 67B

U.S. PAT. OFF. PHOTOGRAPHIC SERVICE





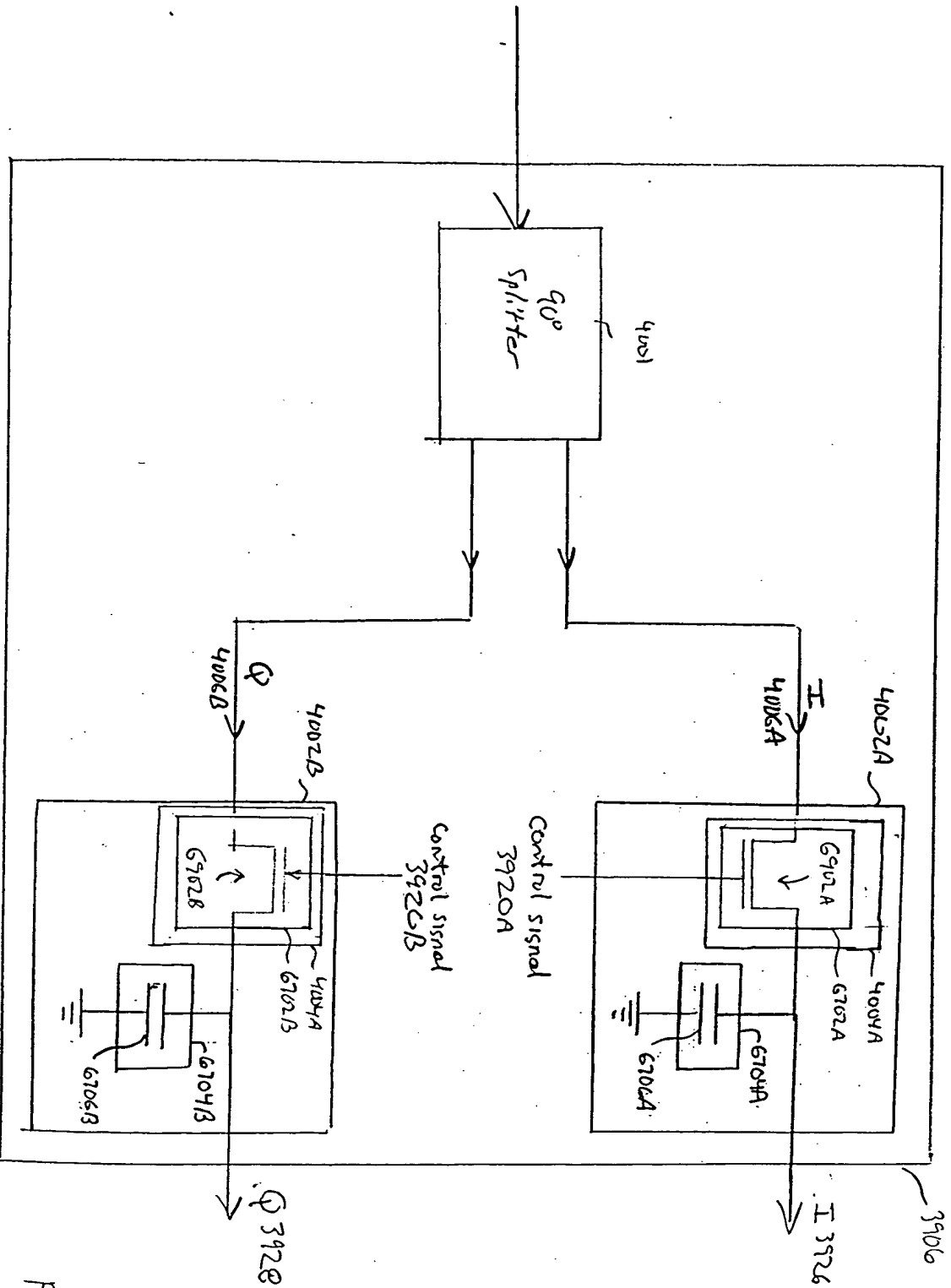


FIG. 61A

FIG. 61A is a block diagram of a differential signal processing system. The system includes a q0 Splitter (4001) that receives an input signal and outputs two signals, I (4006A) and Q (4006B). The I signal is fed into a differential amplifier (3906) which includes a differential pair of transistors (4002A, 4002B), a common-mode feedback circuit (6702A, 6704A, 6704B), and a control signal input (3920A). The output of the I path is I 3926. The Q signal is fed into a similar differential amplifier (3926B) which includes a differential pair of transistors (4002A, 4002B), a common-mode feedback circuit (6702B, 6704B, 6704A), and a control signal input (3920B). The output of the Q path is Q 3928. Both differential amplifiers are controlled by a common Control signal (3920A/B).

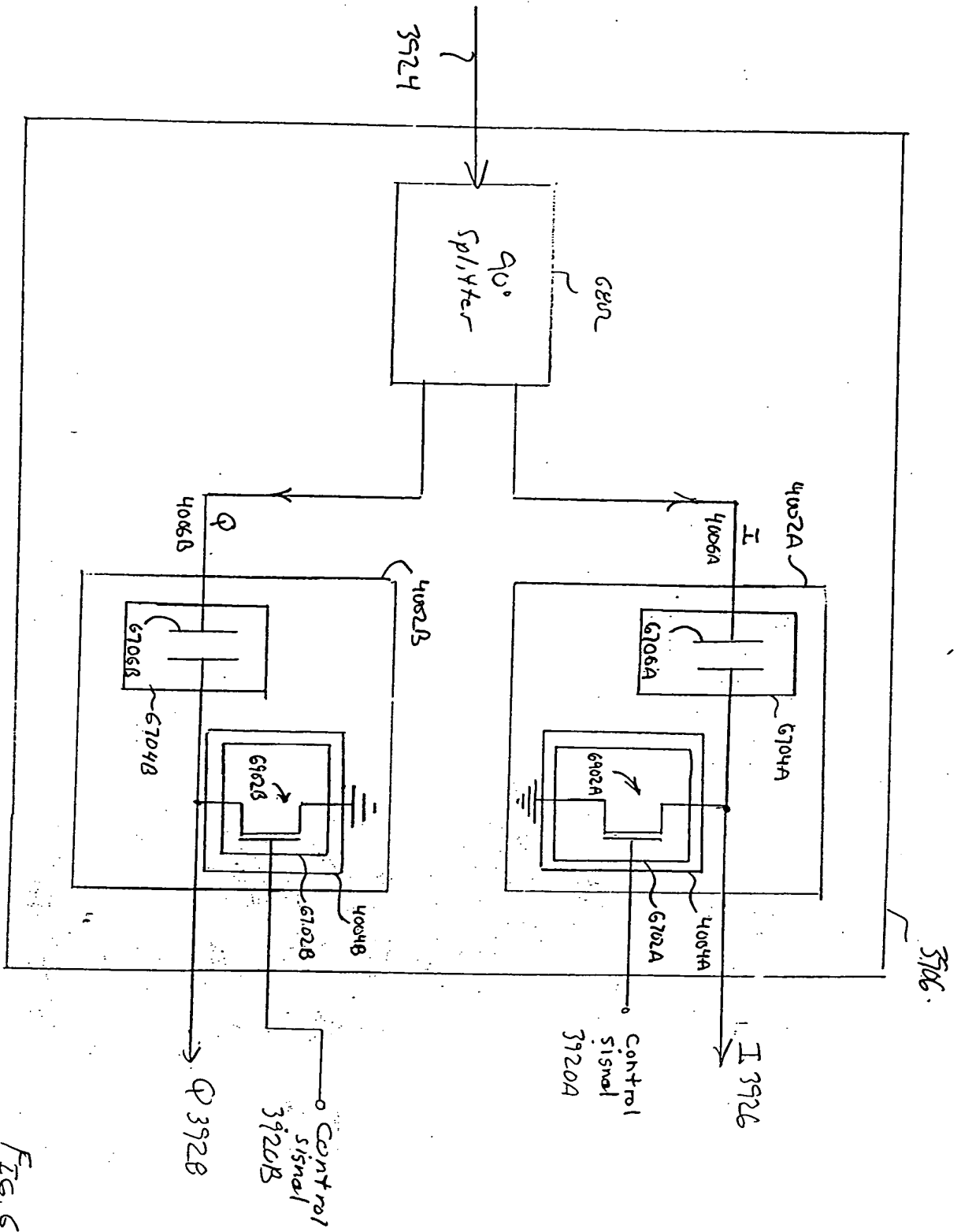


FIG. 67B

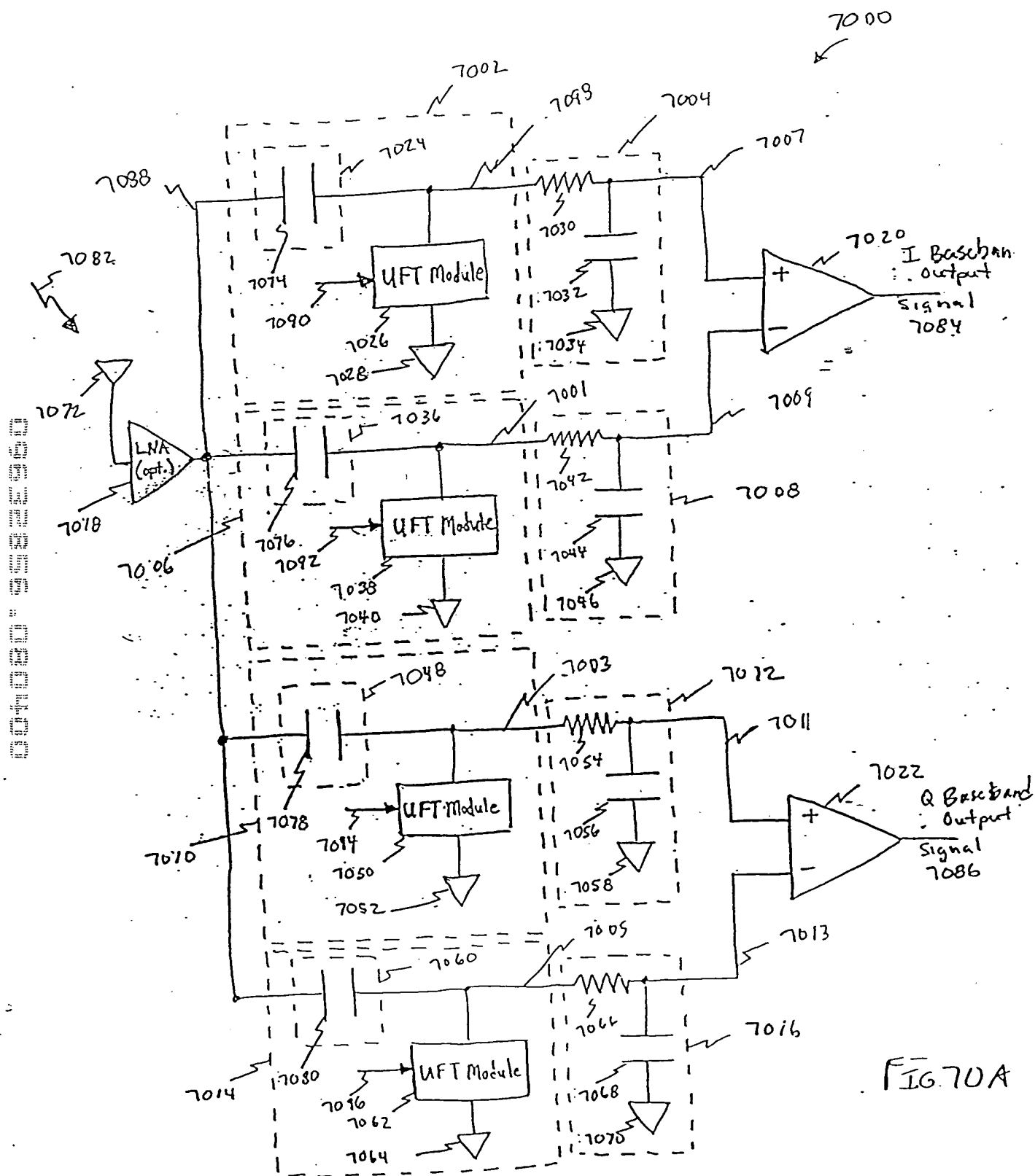
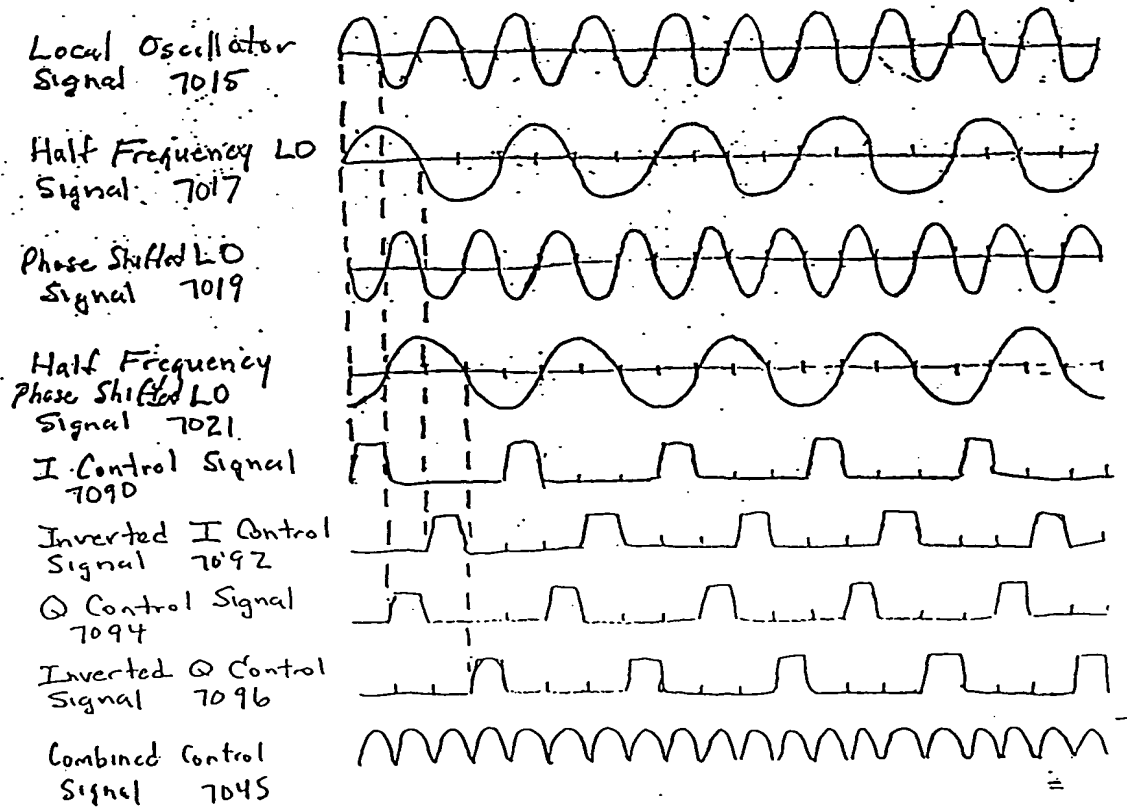
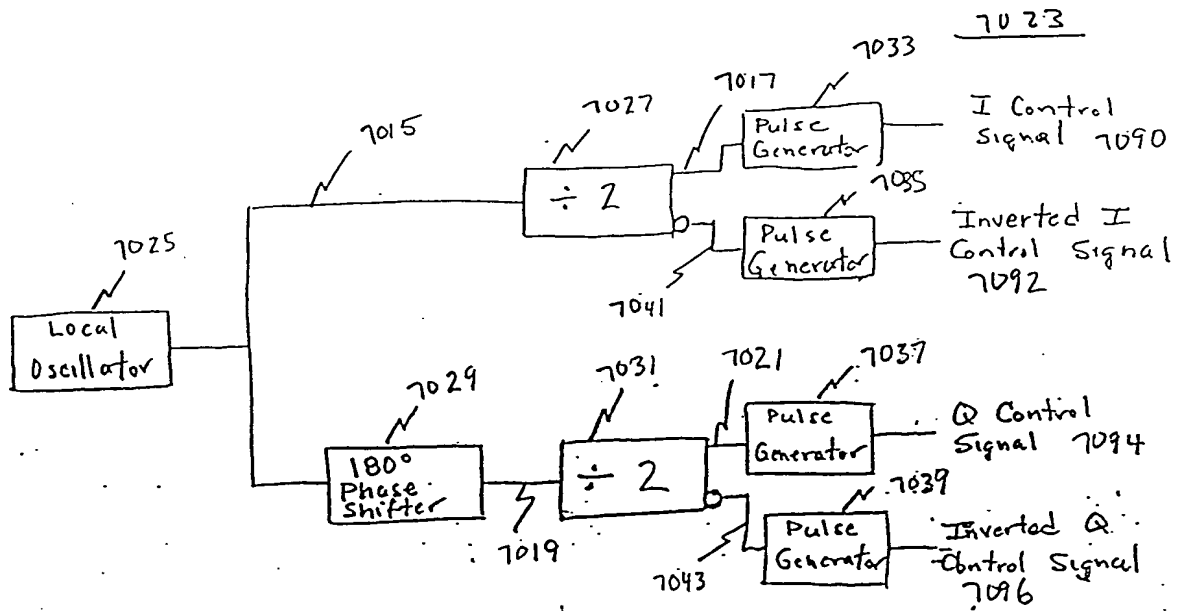


FIG. 70A



(A) IQDEMOM PULSE RELATIONSHIPS TO INPUT RF CARRIER

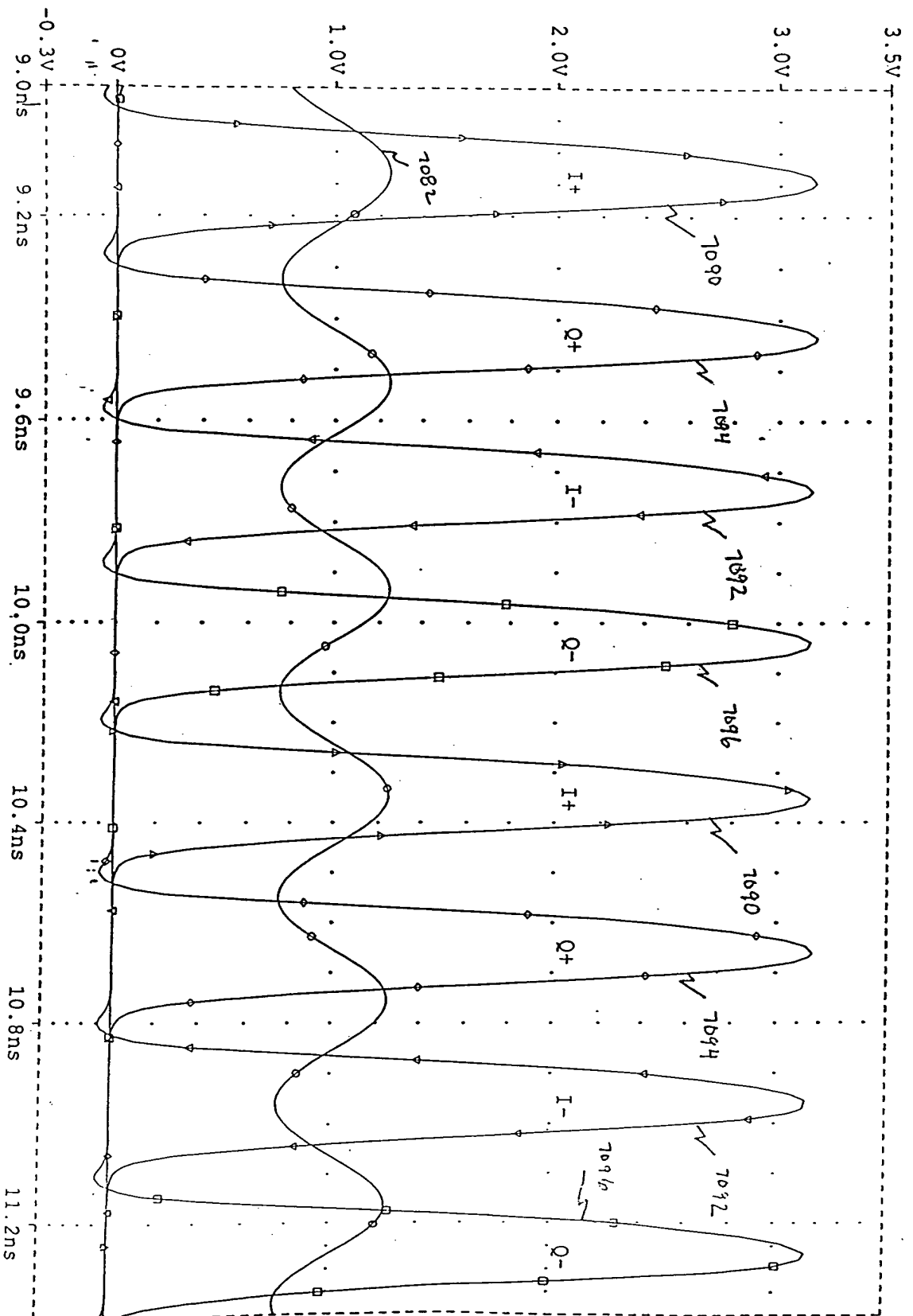


FIG. 70D

[illegible]

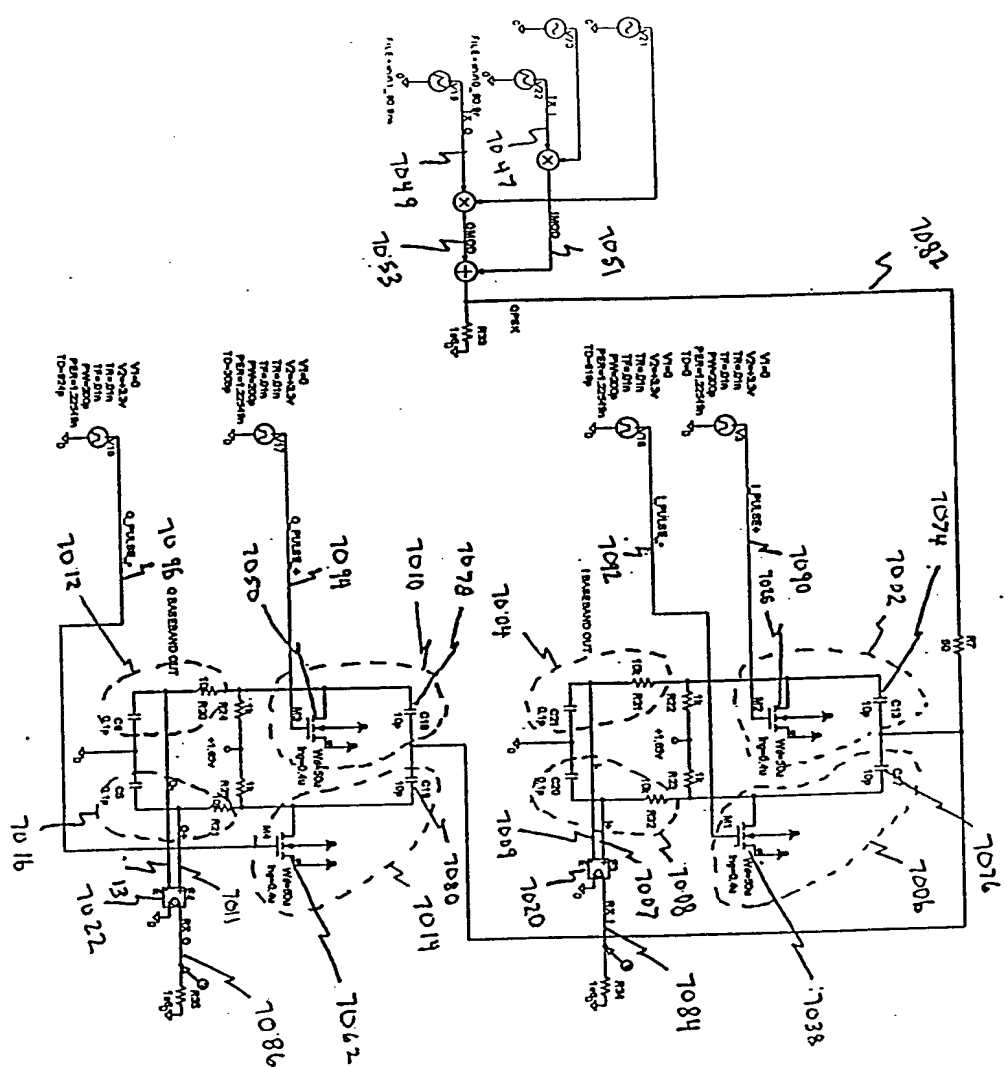
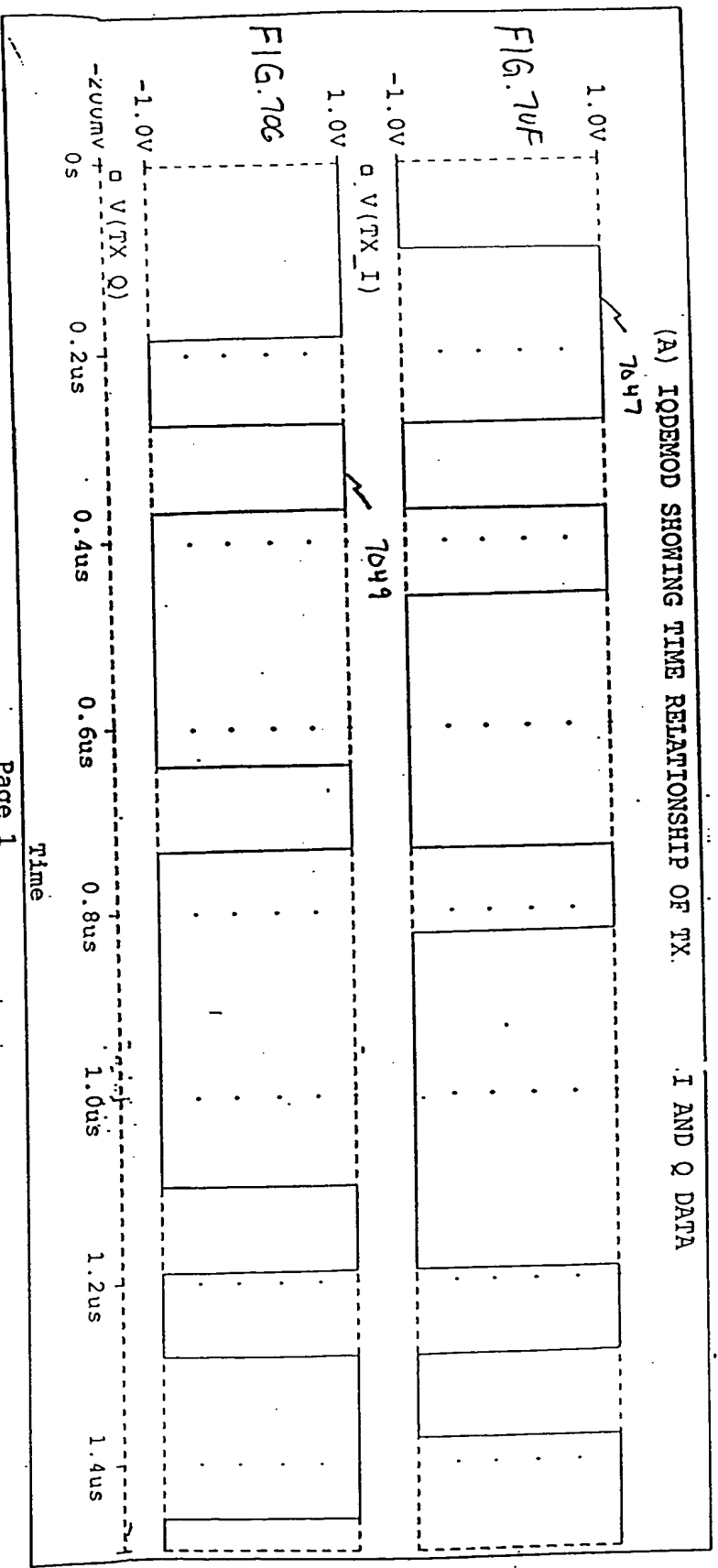


FIG. 70E

219 00

(A) IQDEM0D SHOWING TIME RELATIONSHIP OF TX. I AND Q DATA



(B) IQDEM0D SHOWING QPSK MOD OUTPUT (TOP) WITH IMOD AND QMOD AND I AND Q DATA (BOTTOM)

FIG. 70H

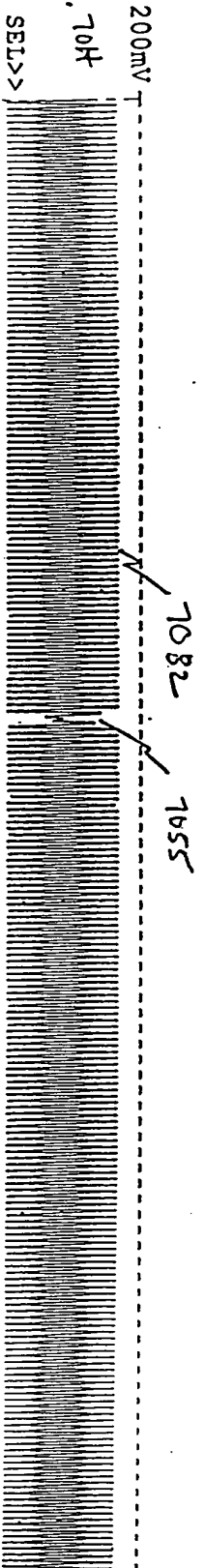


FIG. 70I

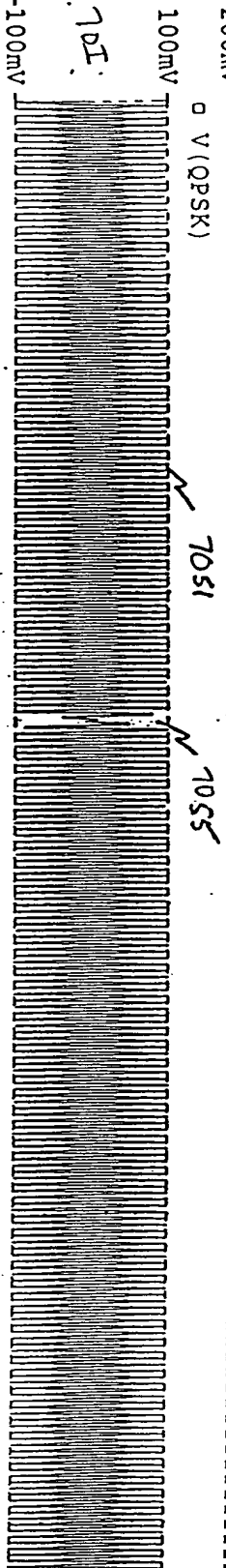


FIG. 70J

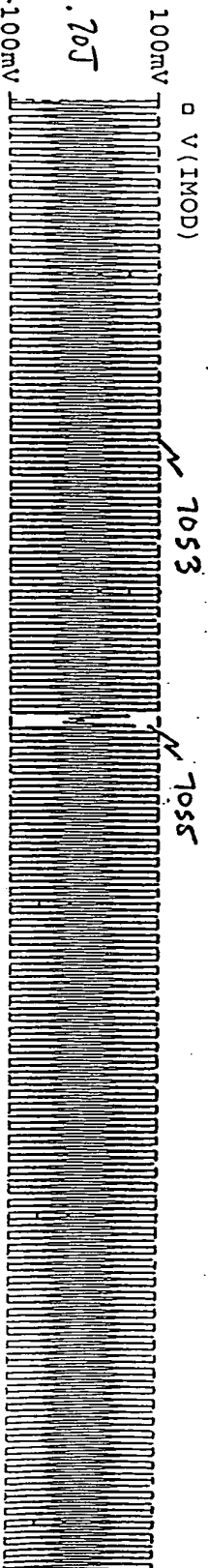


FIG. 70K

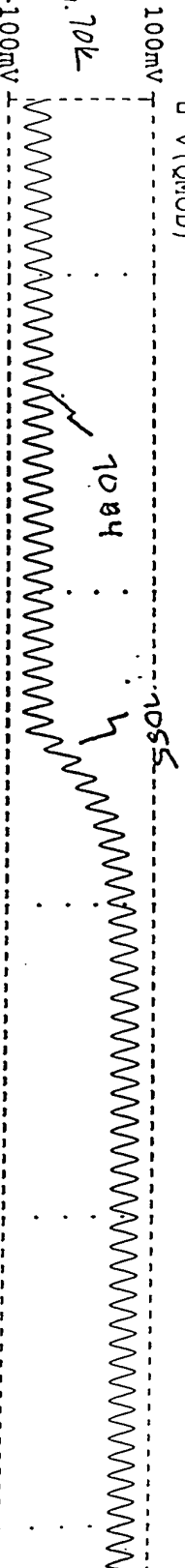
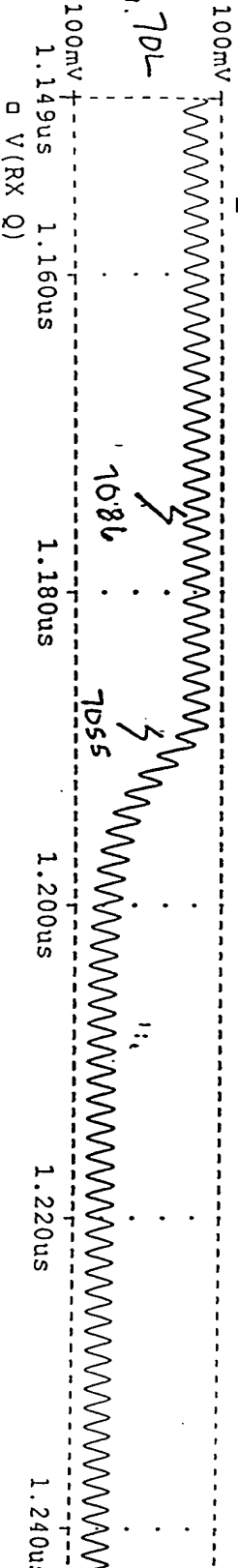


FIG. 70L



Time

(B) IQDEMOD RELATIONSHIP OF I AND Q RECEIVED DATA DIFFERENTIAL (BOTTOM) AND SINGLE ENDED AFTER DIFF AMP...

FIG. 704

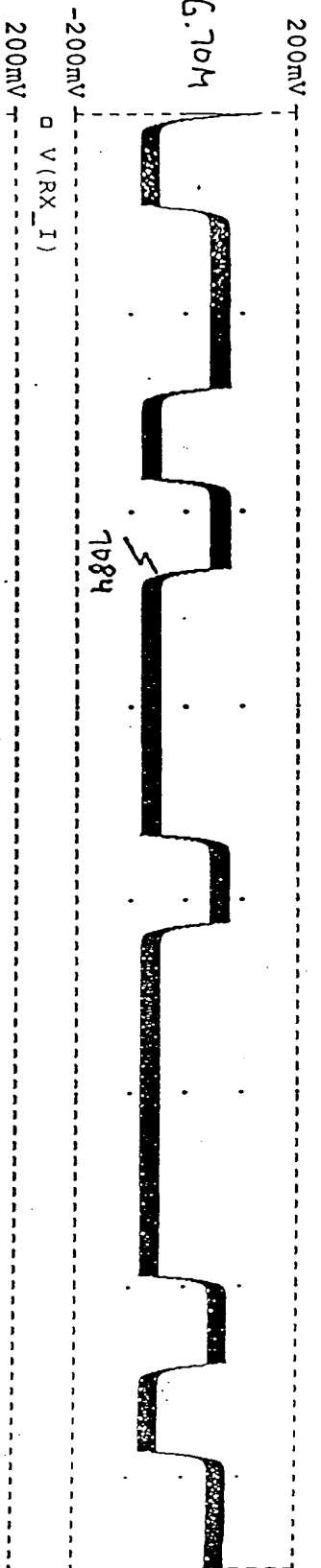


FIG. 705

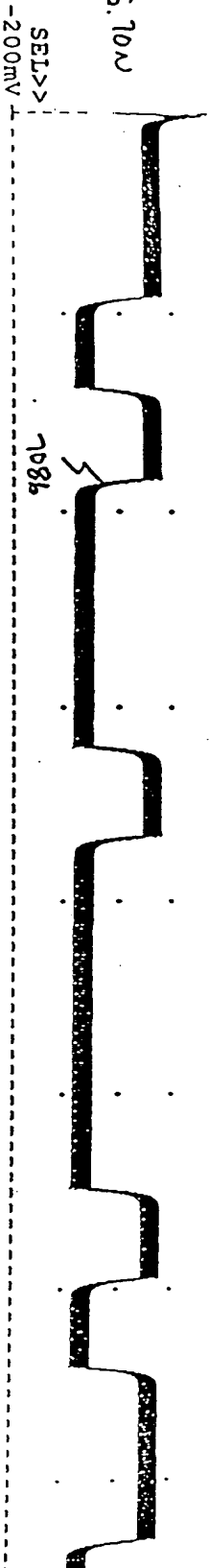


FIG. 706

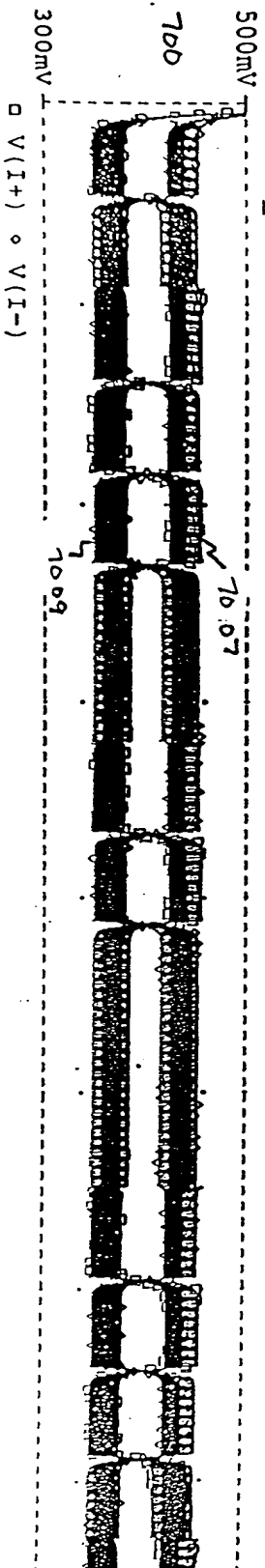
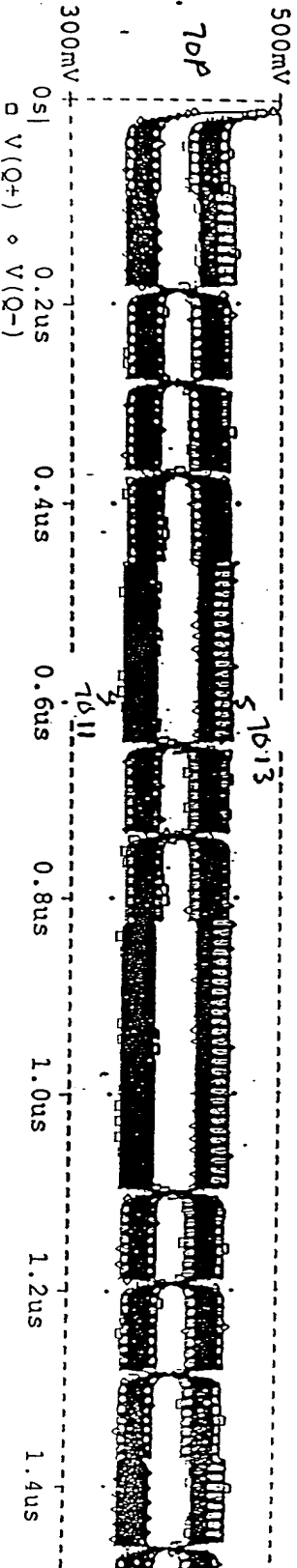


FIG. 707



Time

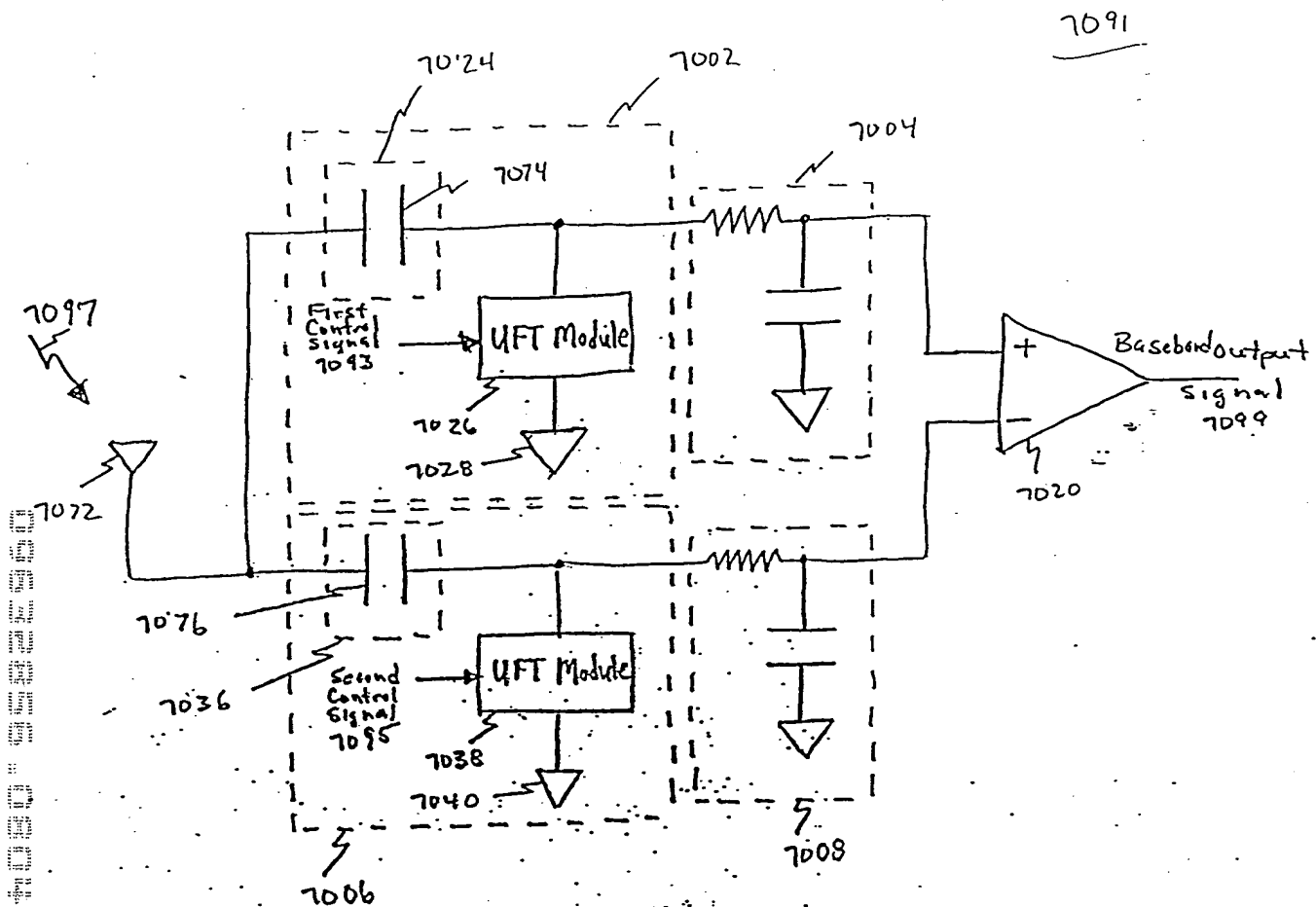


FIG. 70Q

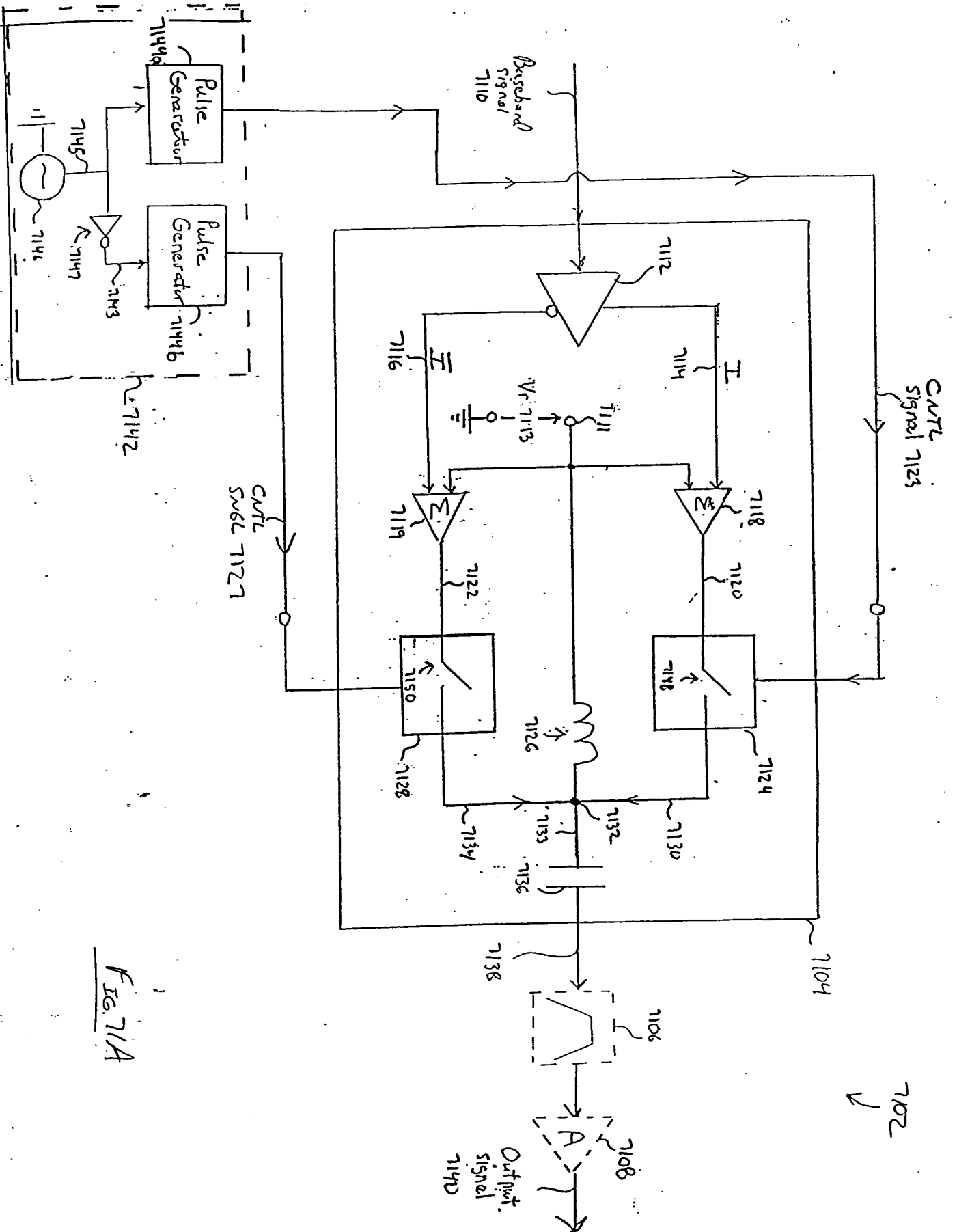


FIG. 71A

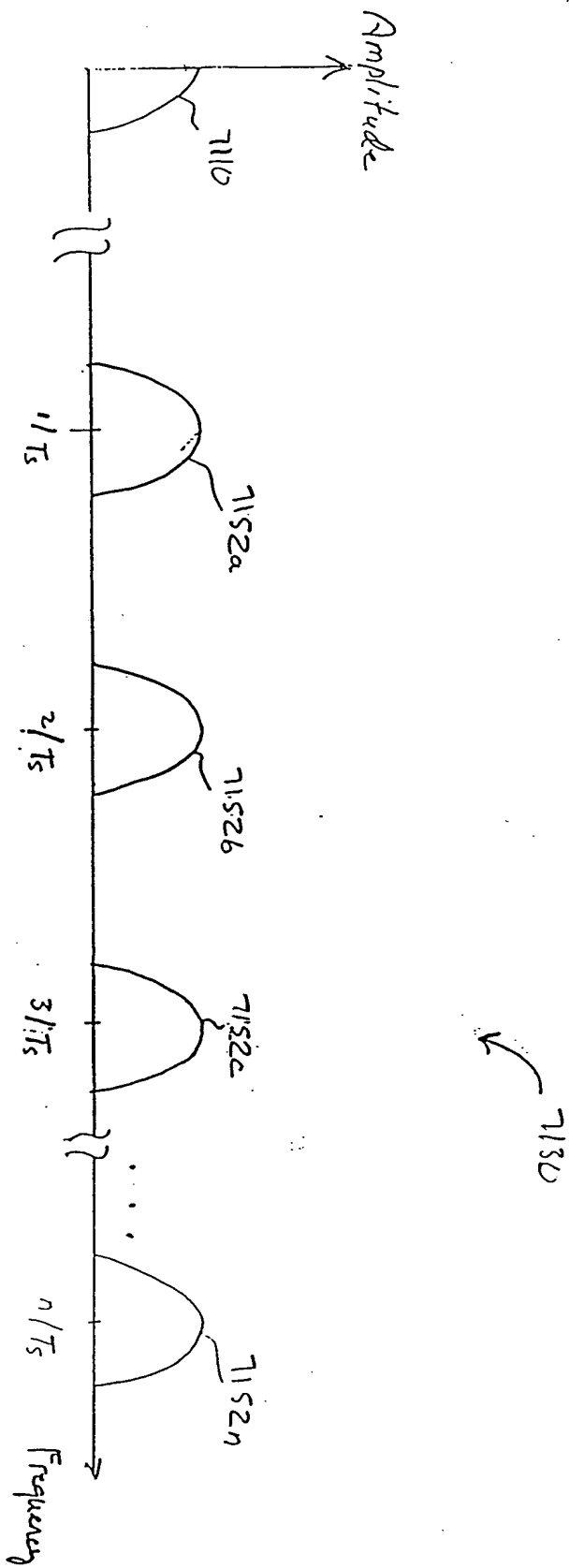


FIG. 71B



71C. 71C

1000
 900
 800
 700
 600
 500
 400
 300
 200
 100
 0

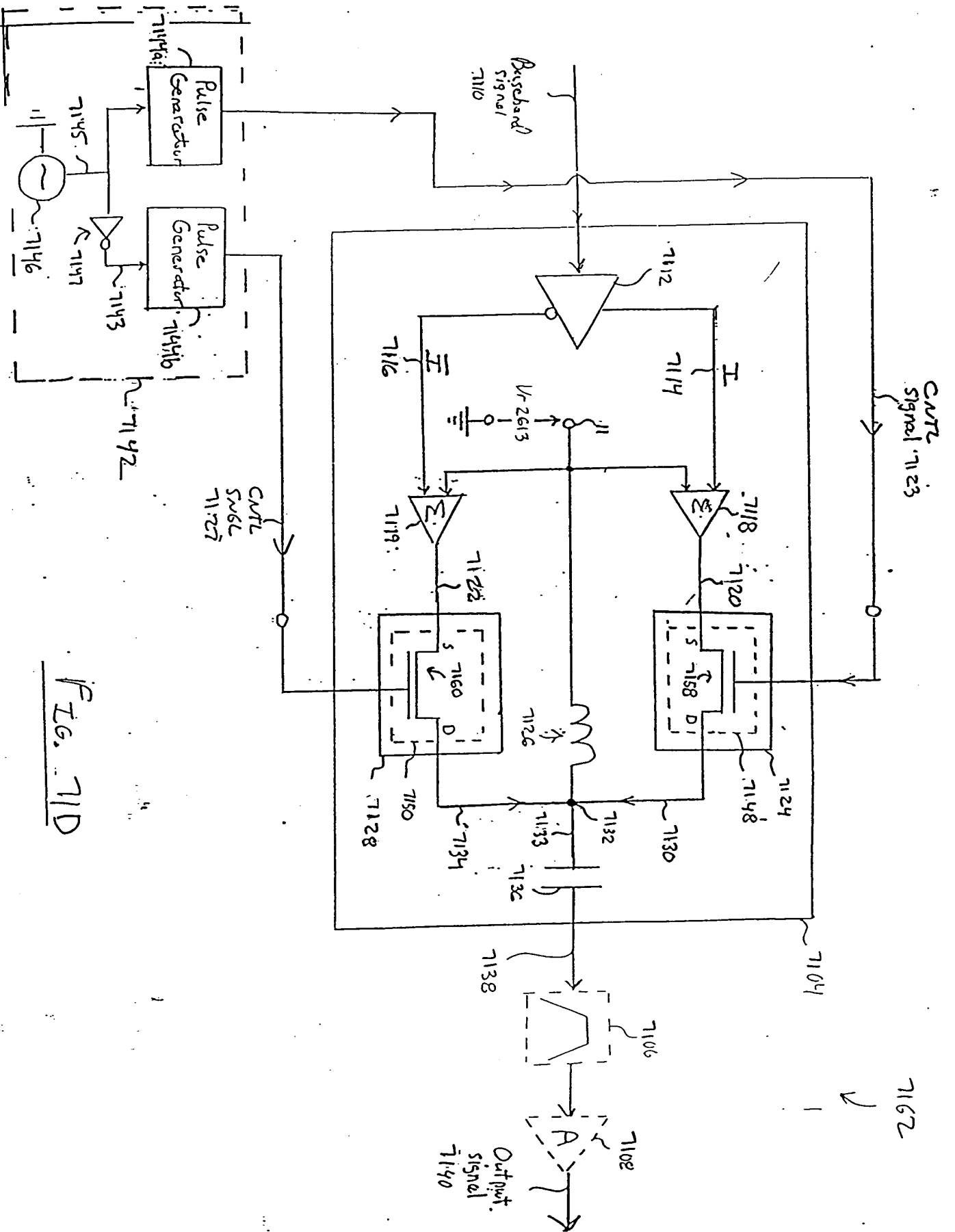


Fig. 71D

FIG. 72A

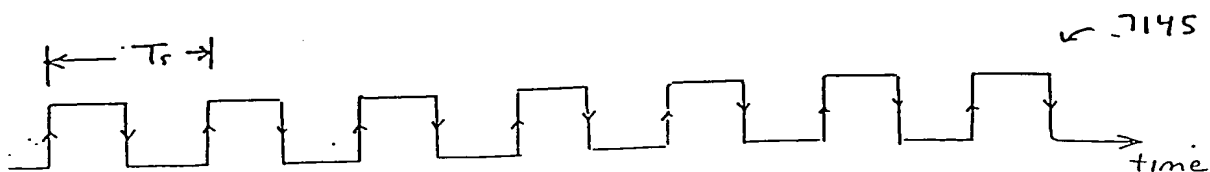


FIG. 72B

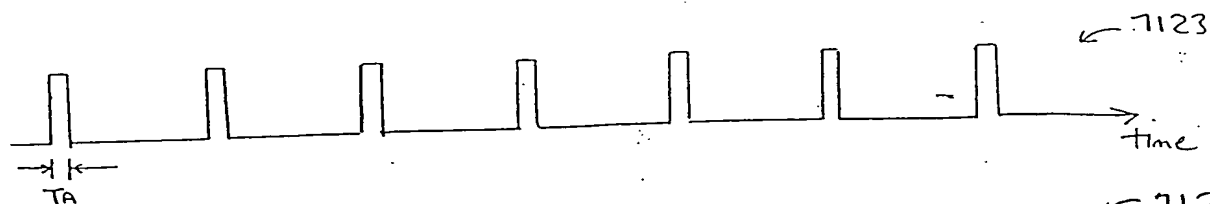


FIG. 72C

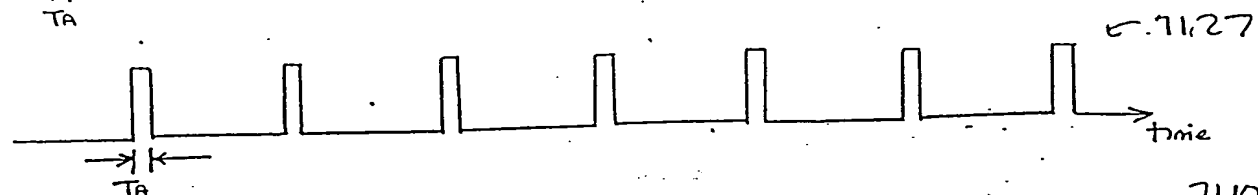


FIG. 72D

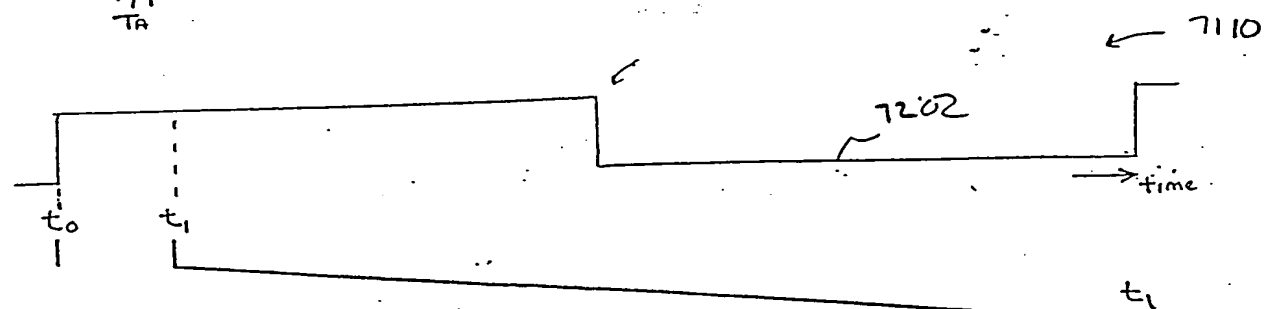


FIG. 72E

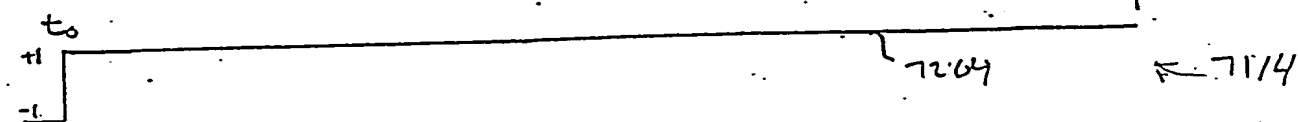


FIG. 72F



FIG. 72G

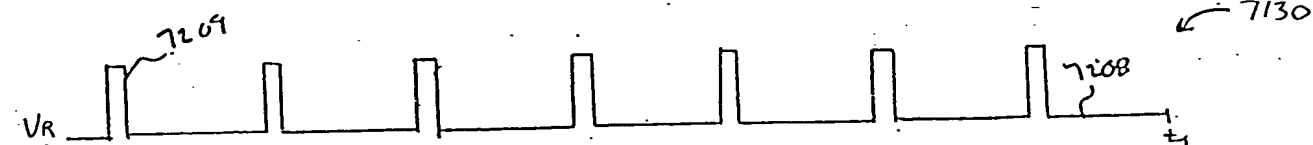


FIG. 72H

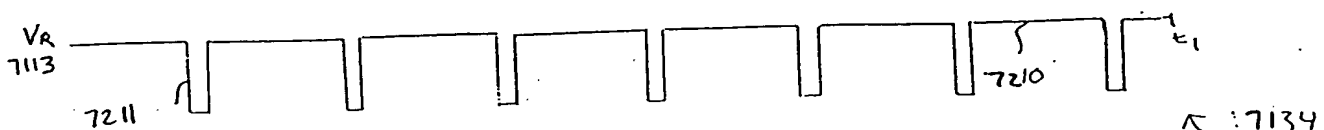
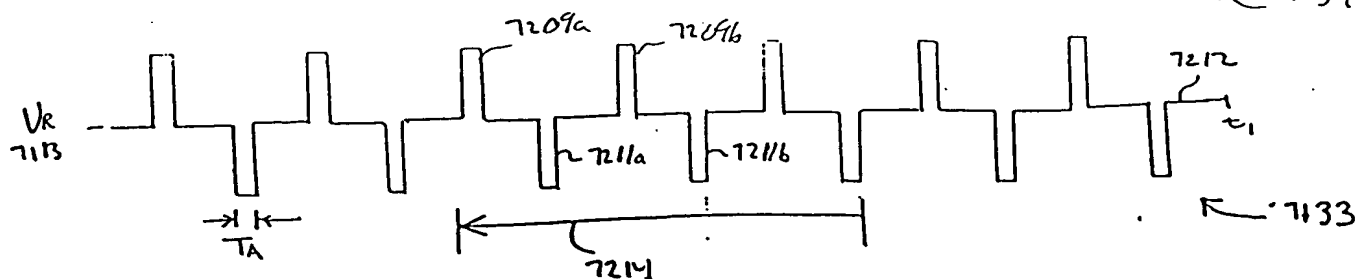


FIG. 72I



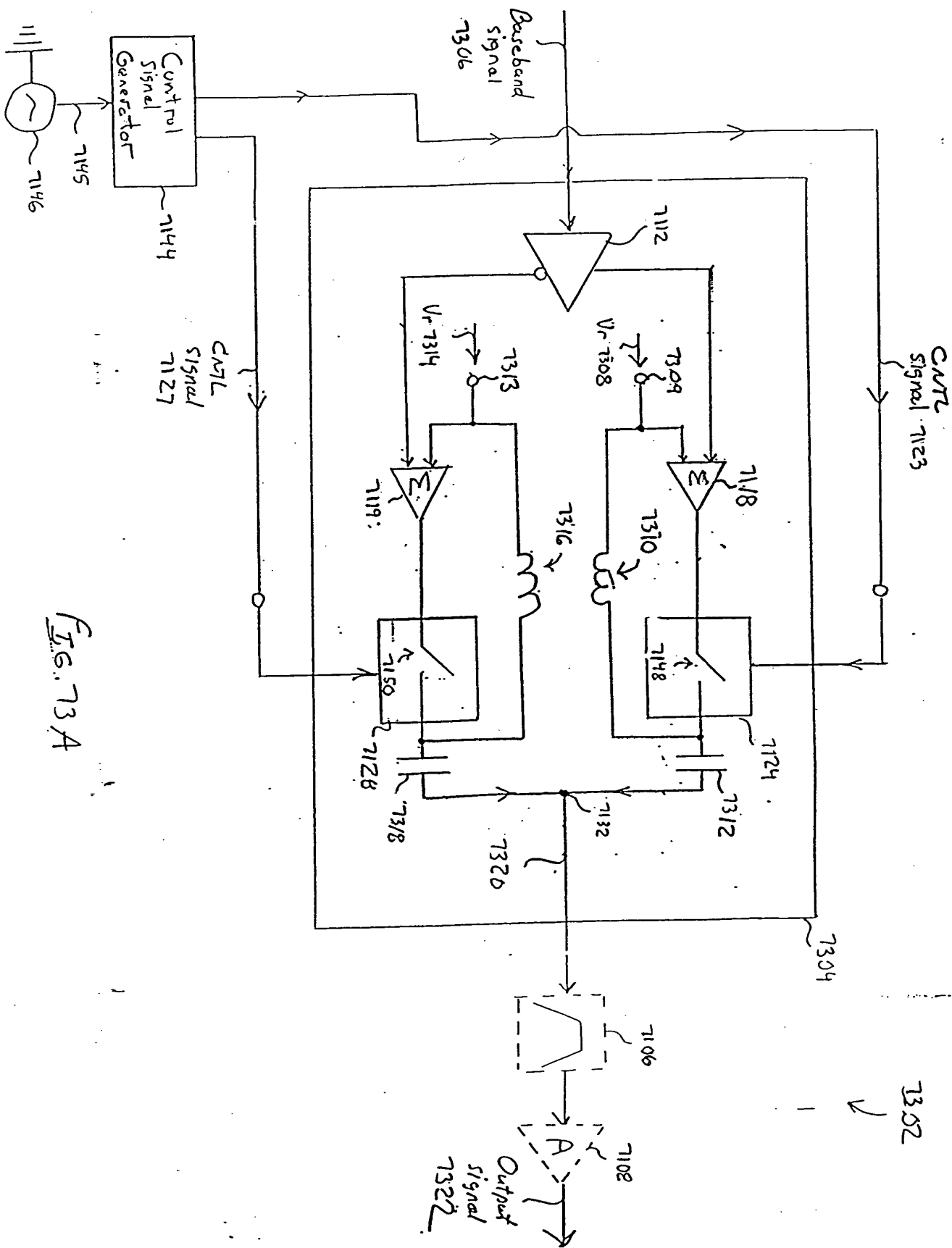


Fig. 73A

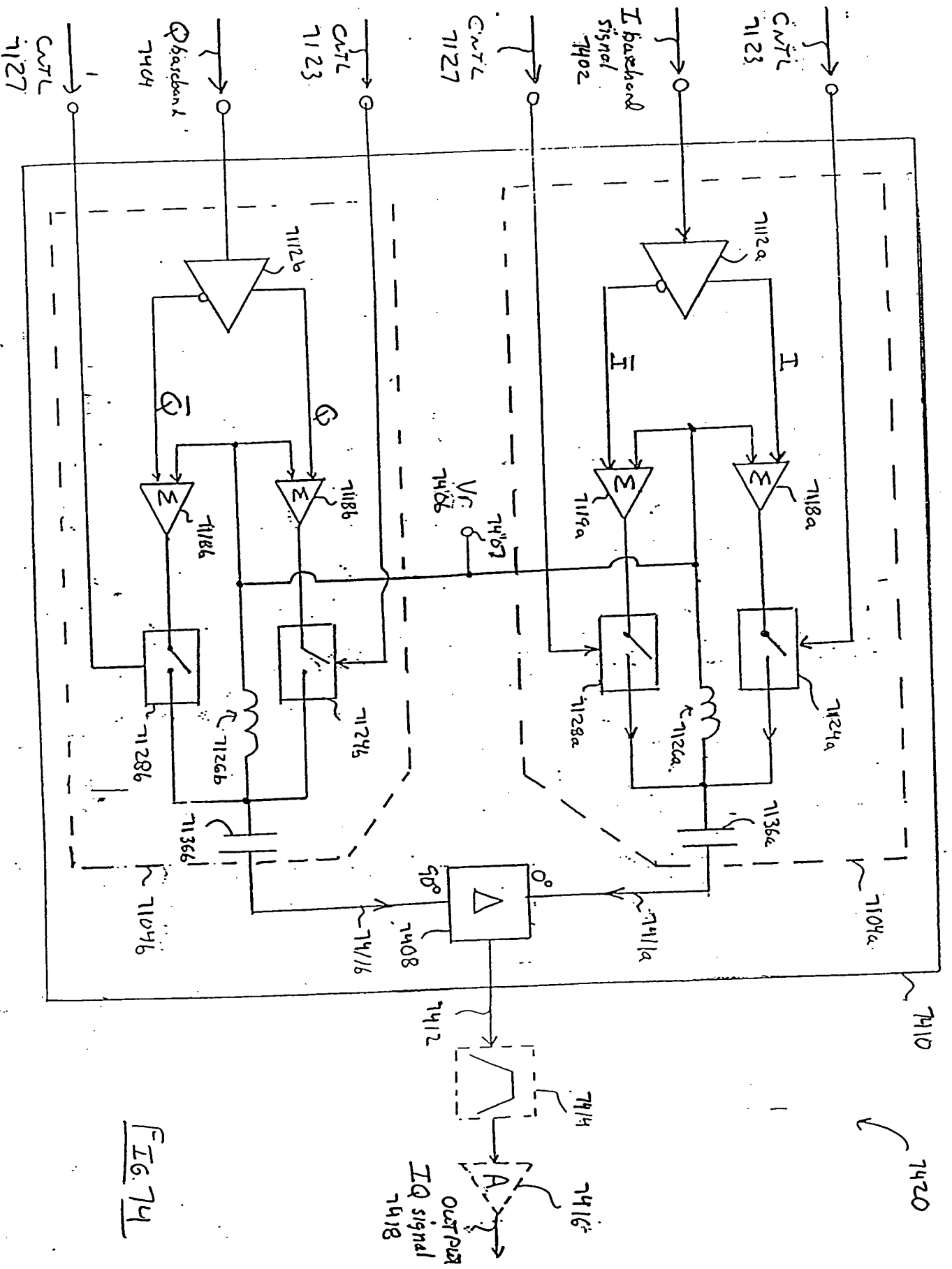


FIG. 74

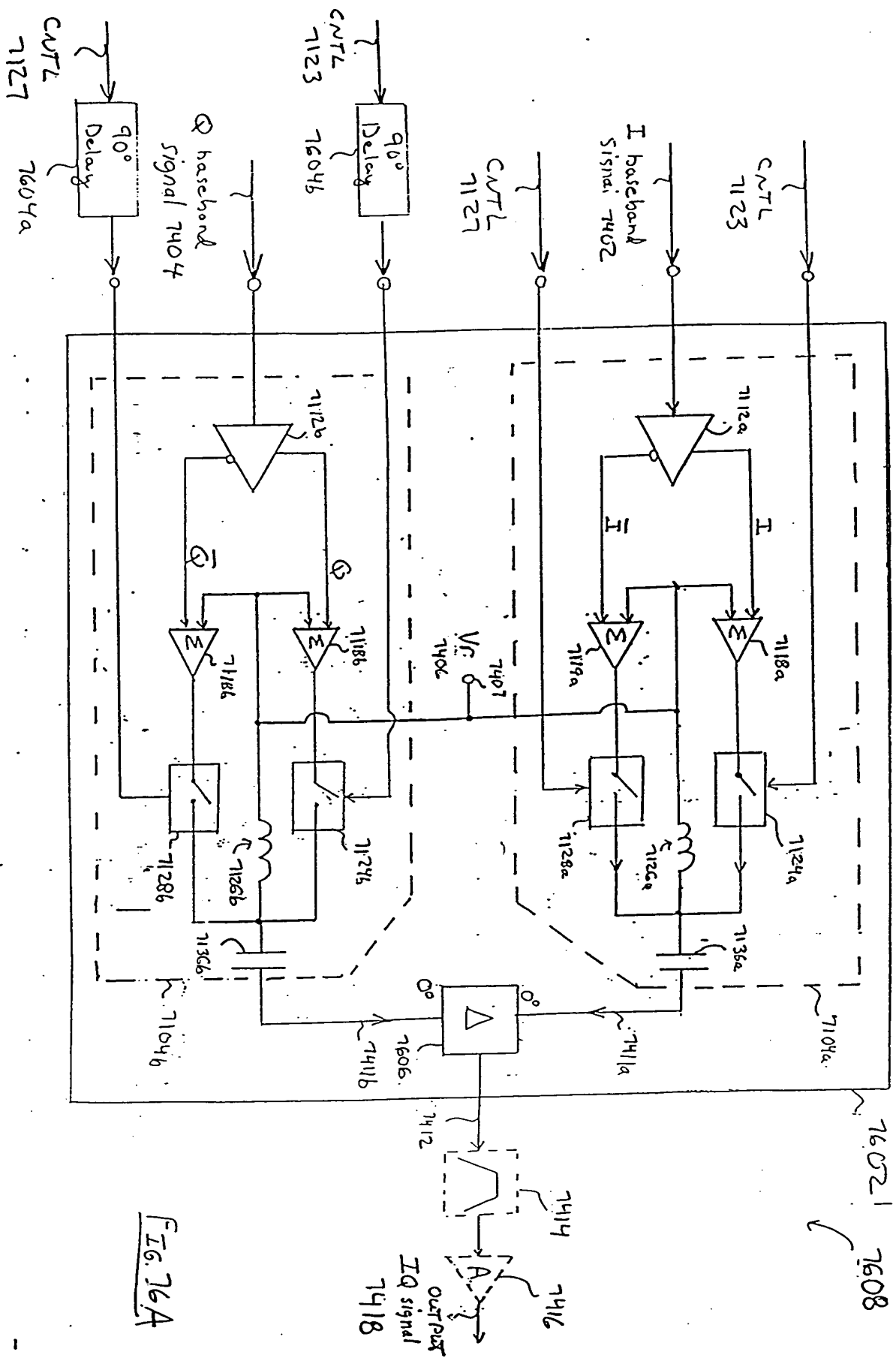


Fig. 76A

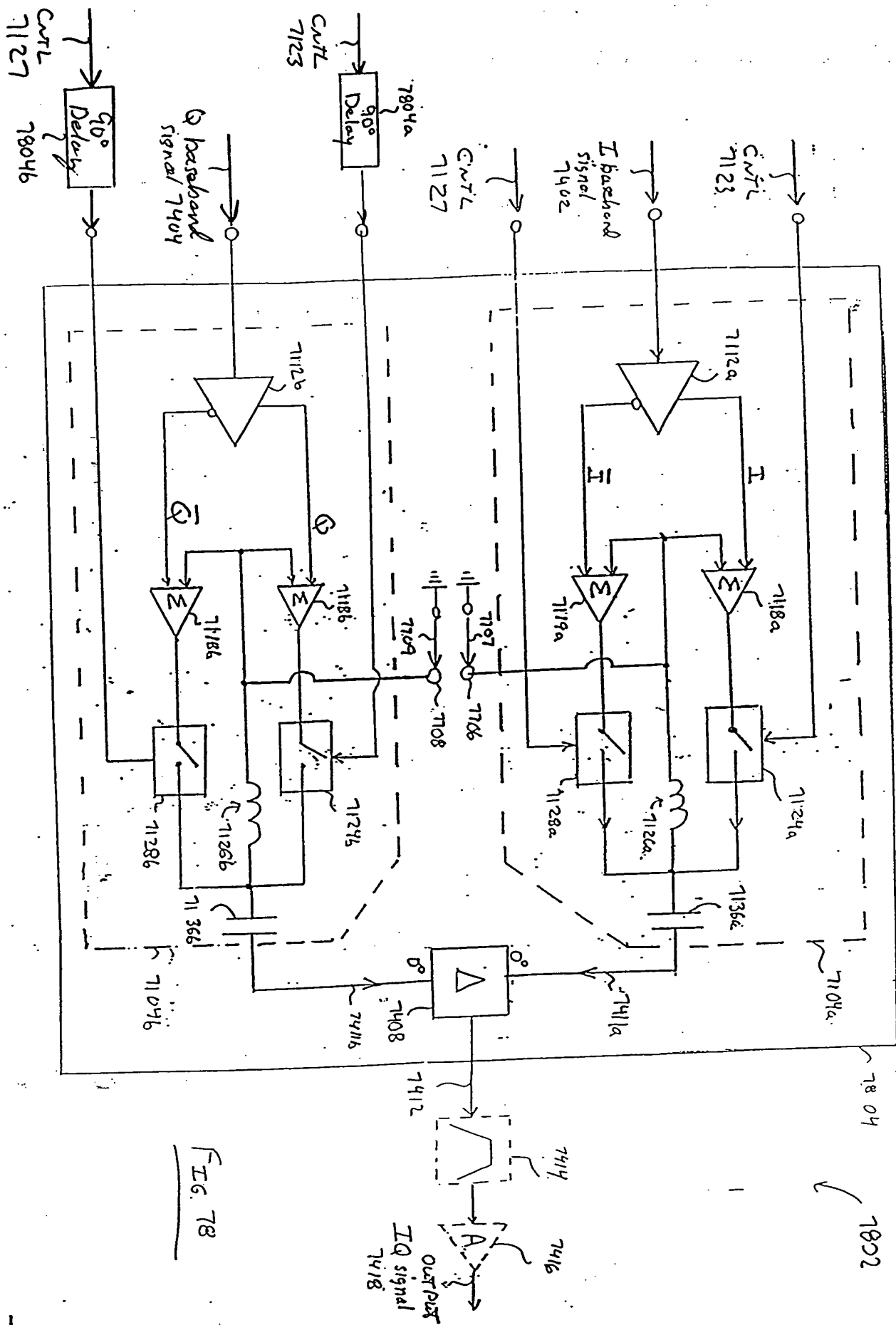


FIG. 78

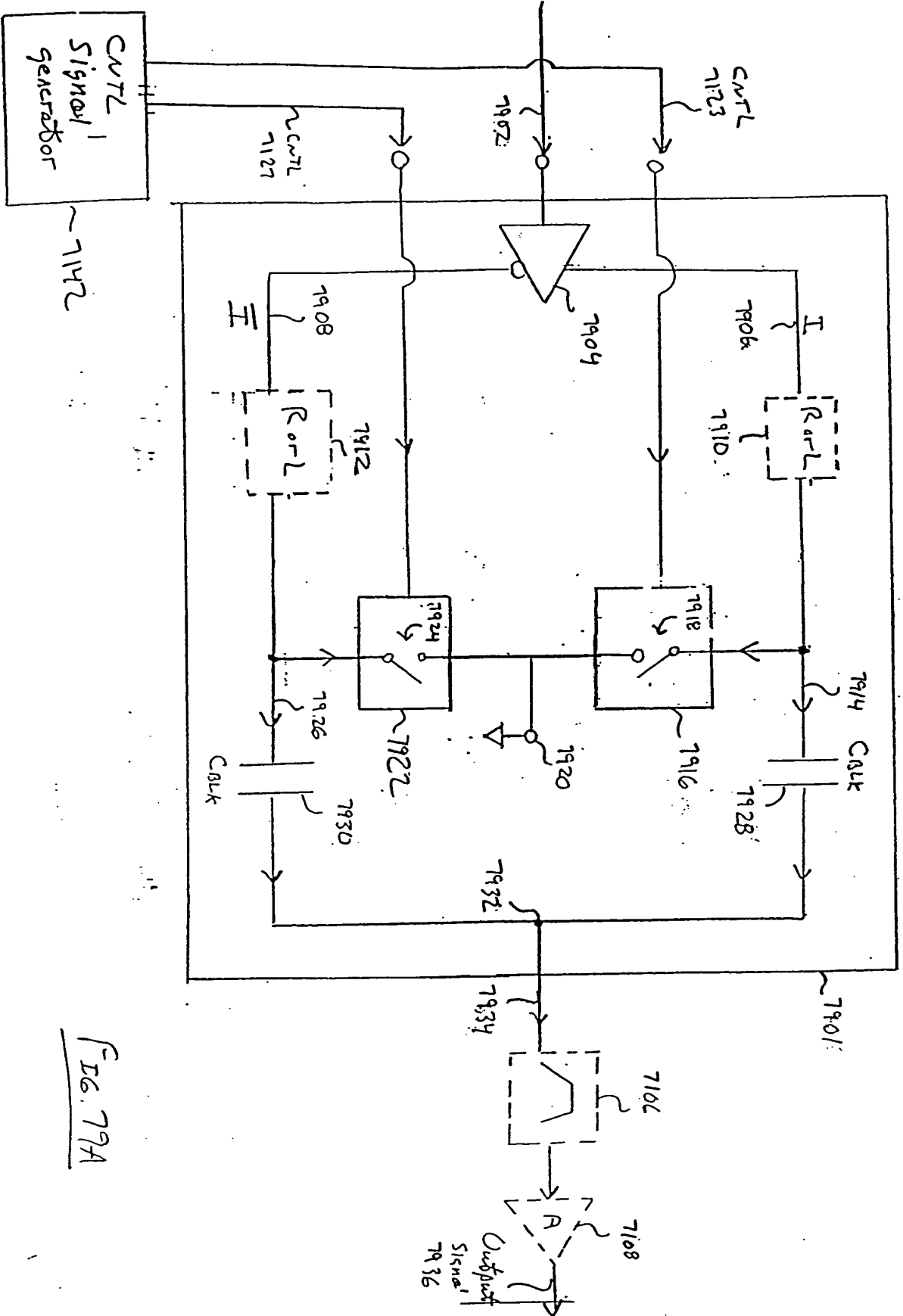


Fig. 79A

Hand-drawn schematic diagram of a digital circuit, likely a 7490 decade counter.

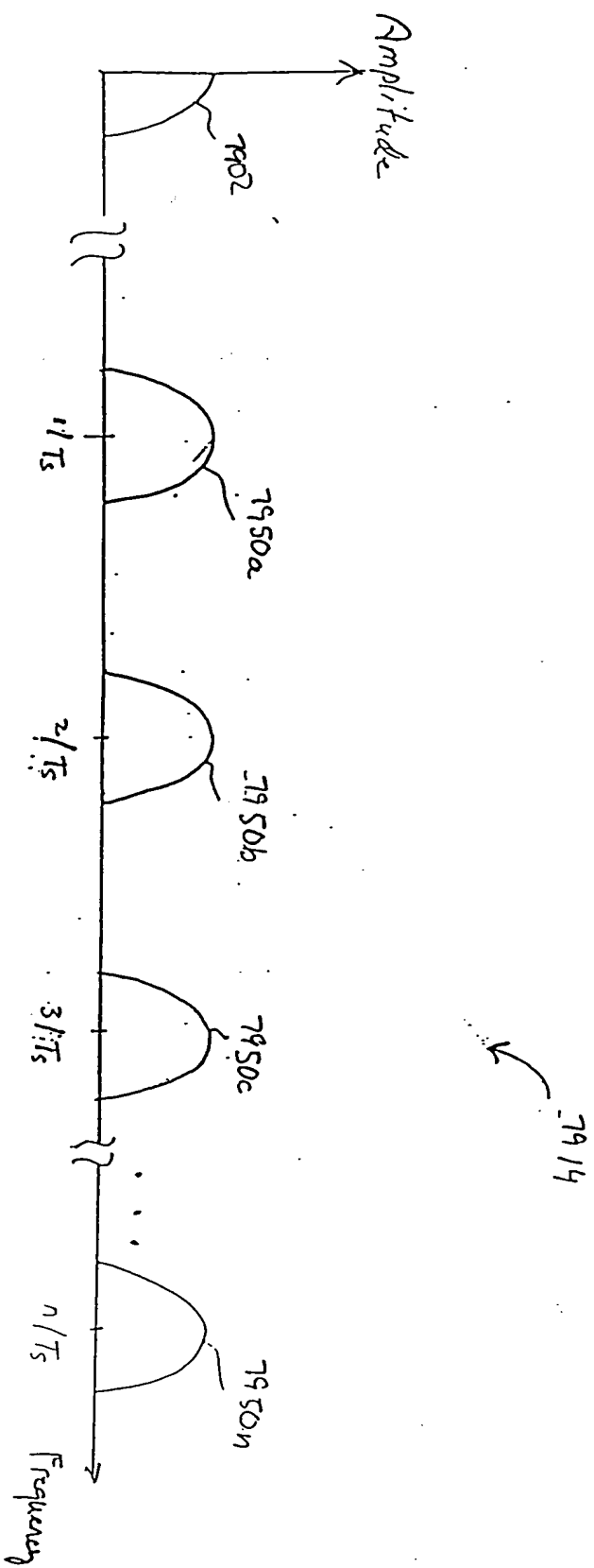


FIG. 79B

05030300 050400

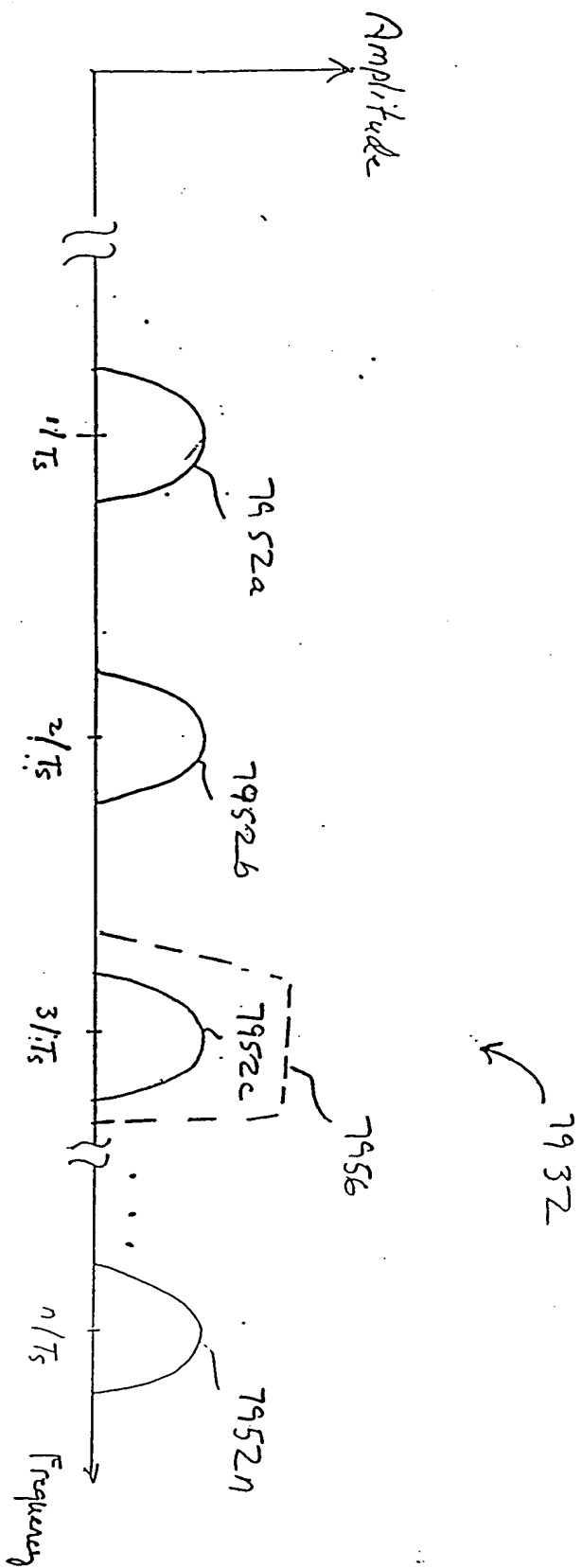


FIG. 79c

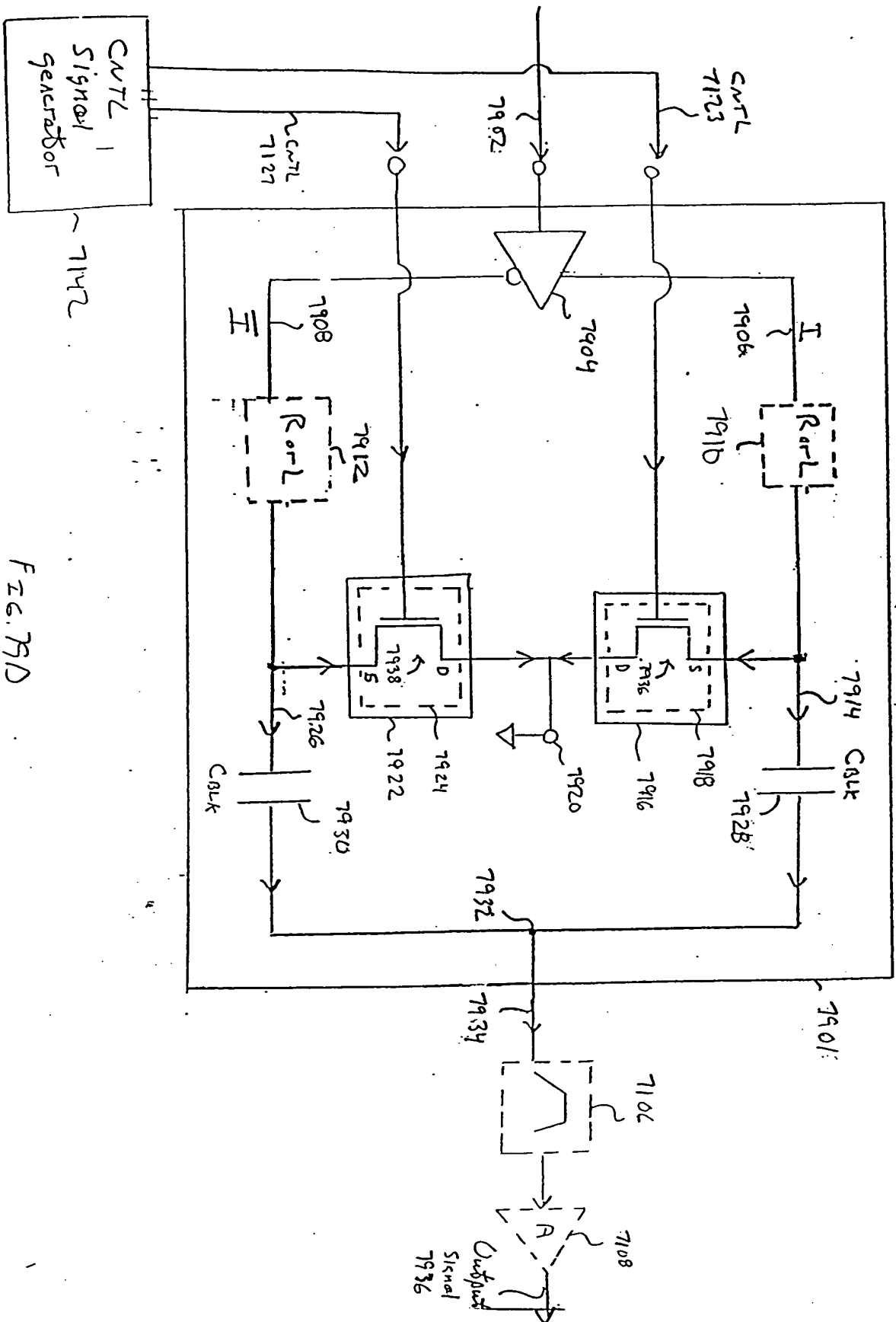


Fig. 7D

7900

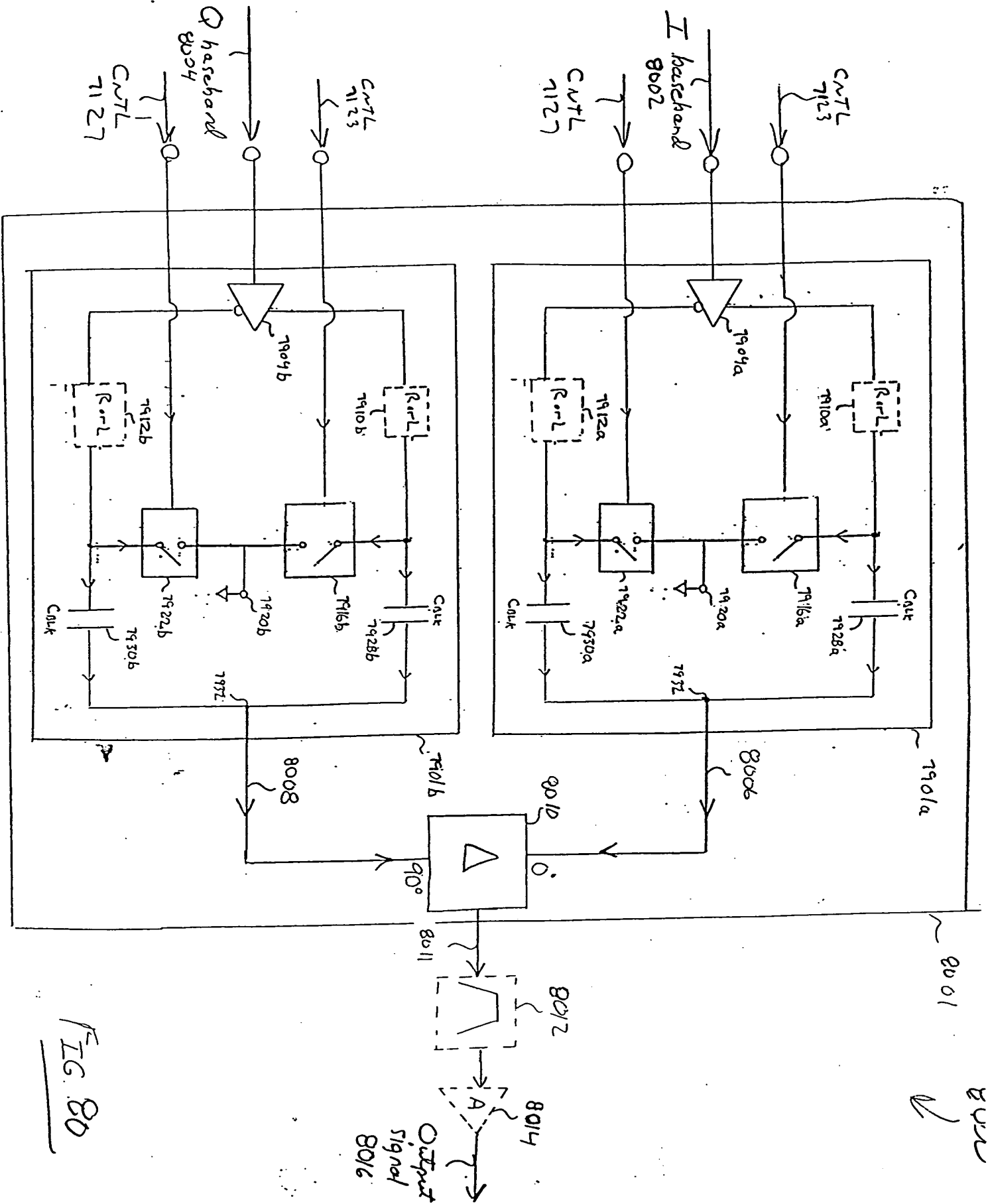
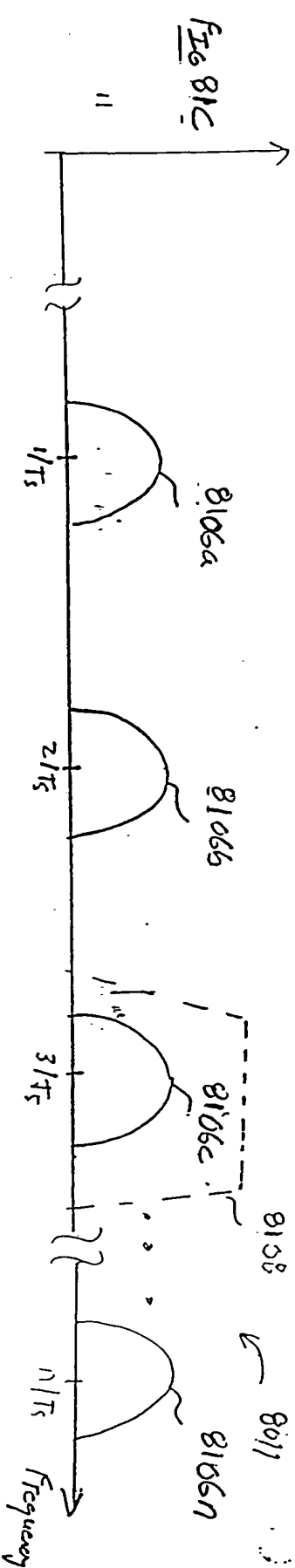
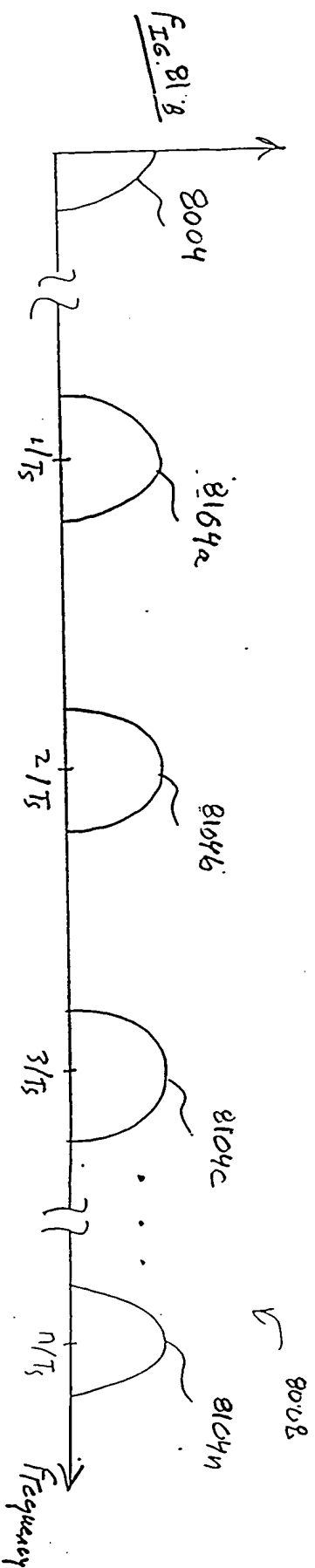
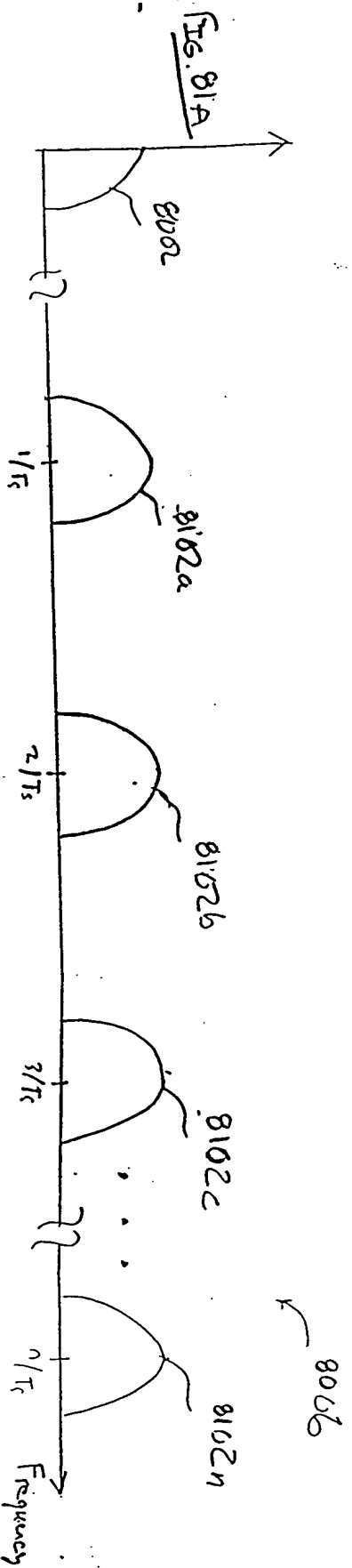


FIG. 80



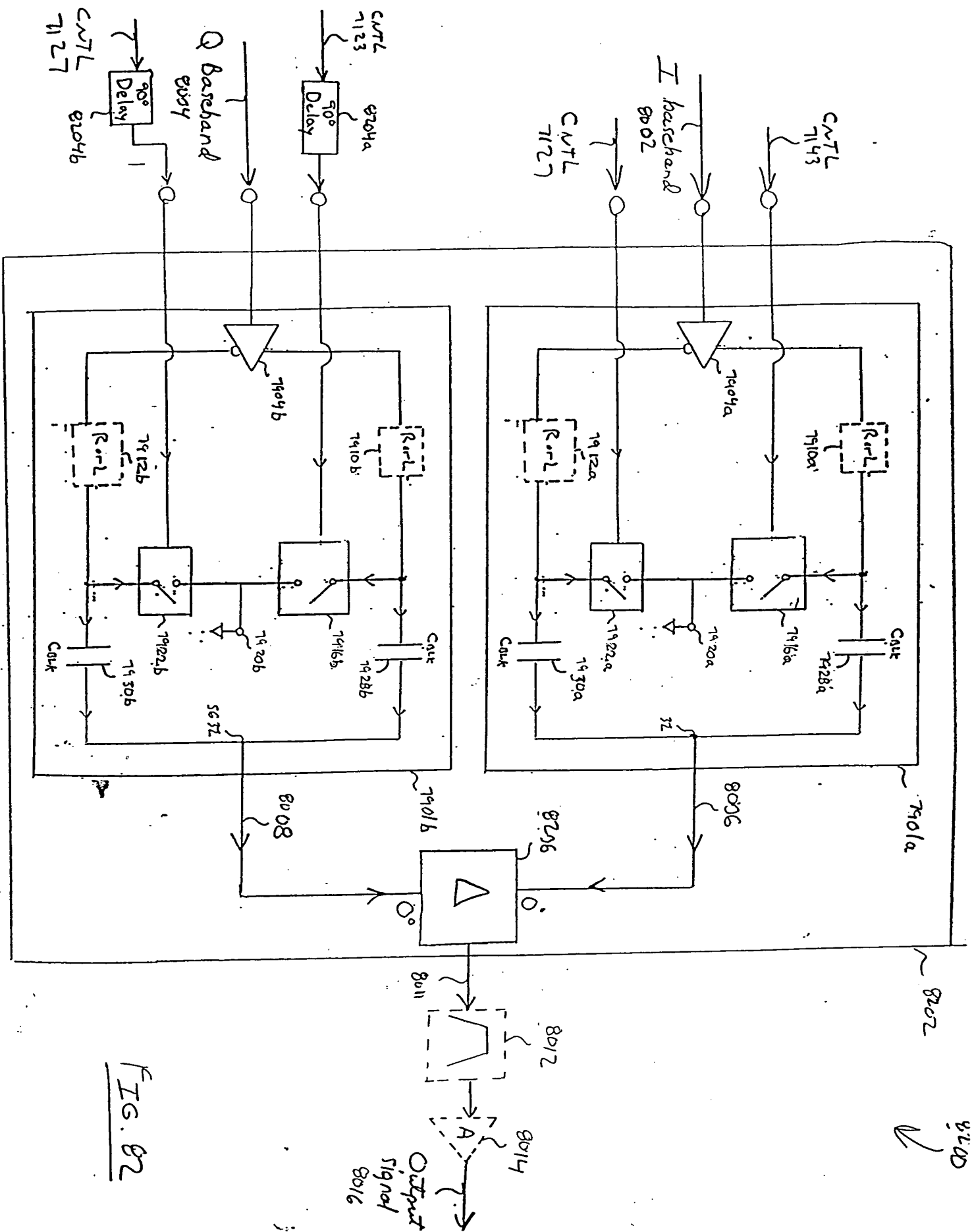


Fig. 82

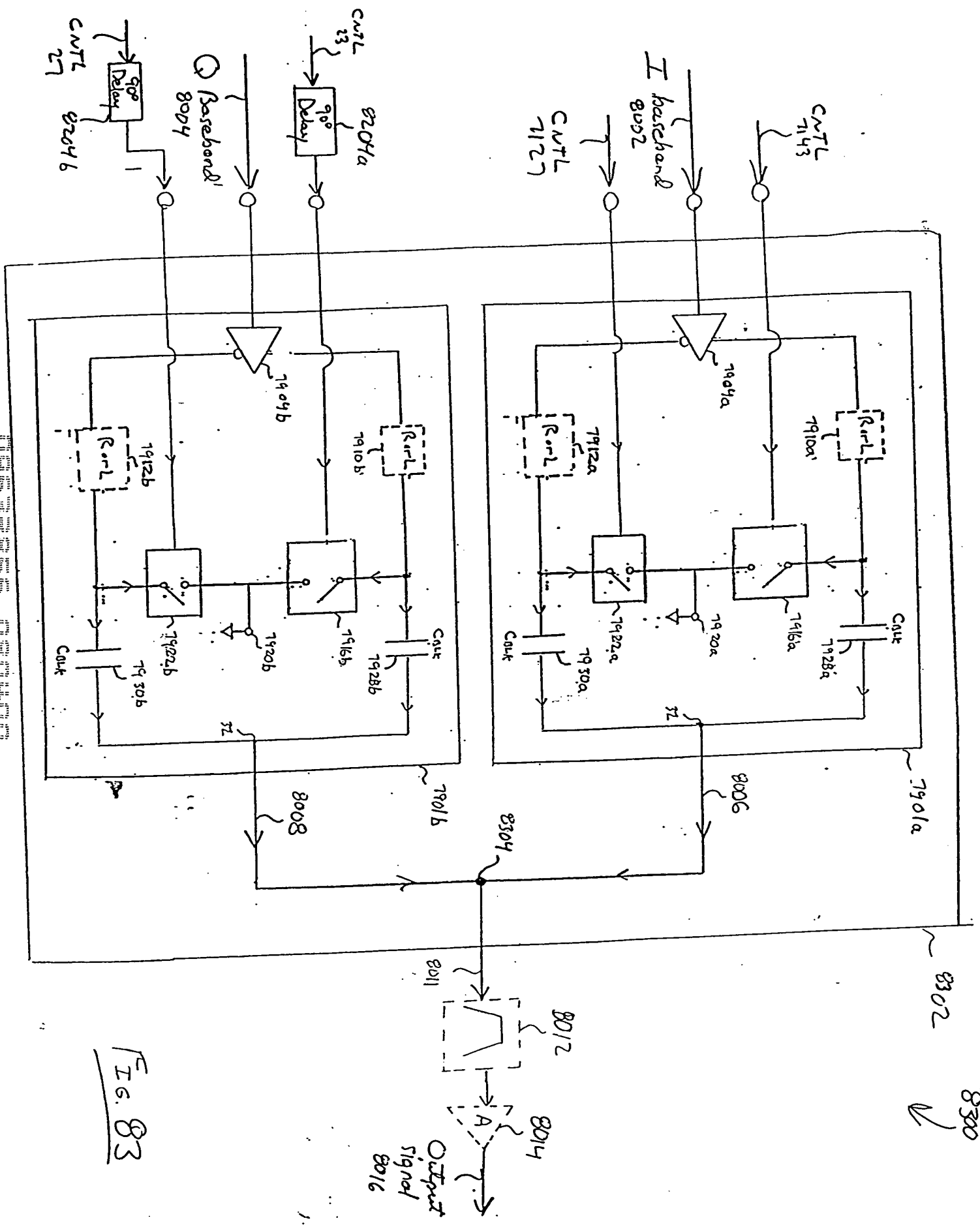


Fig. 83

8300

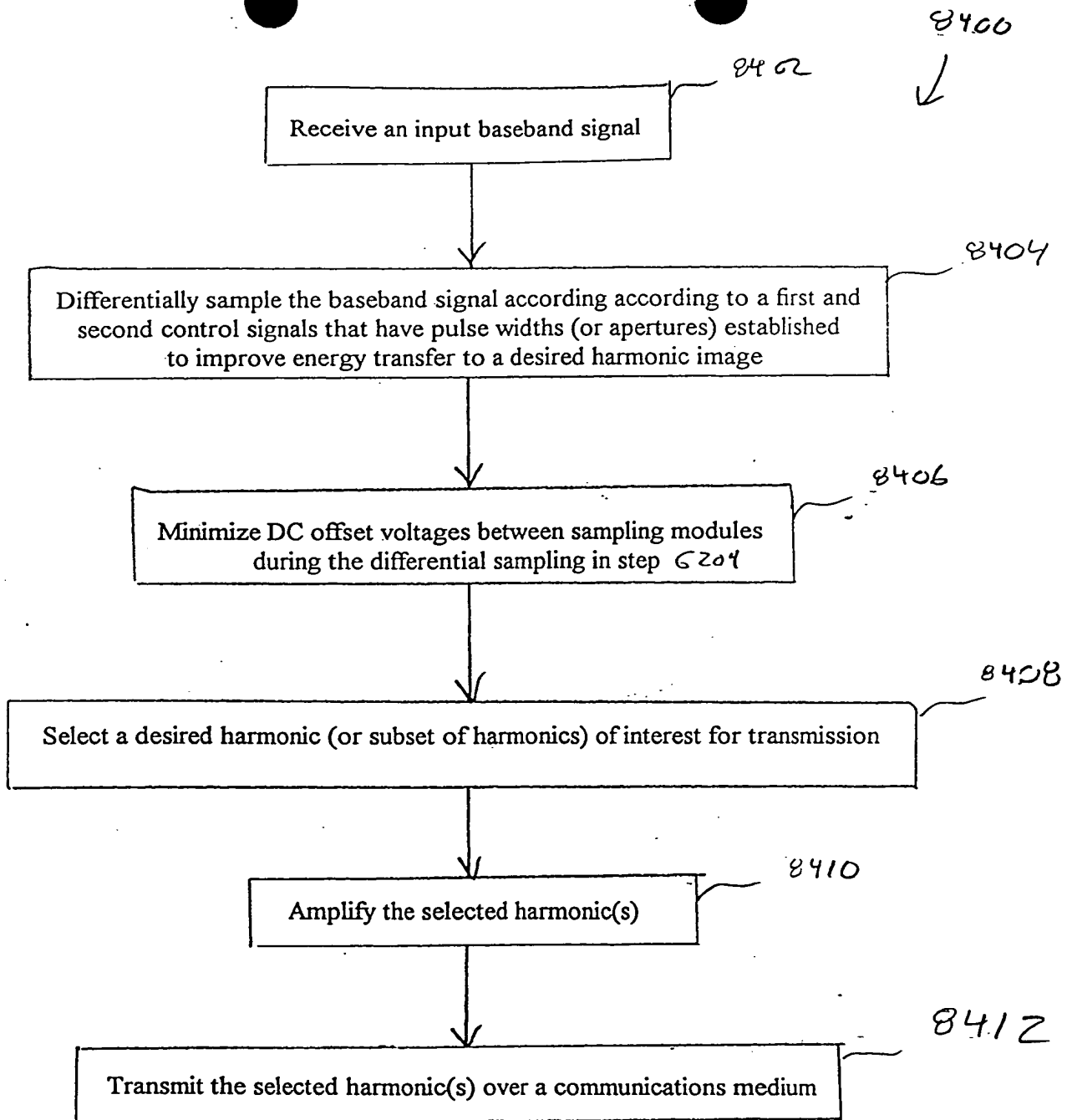


FIG. 84

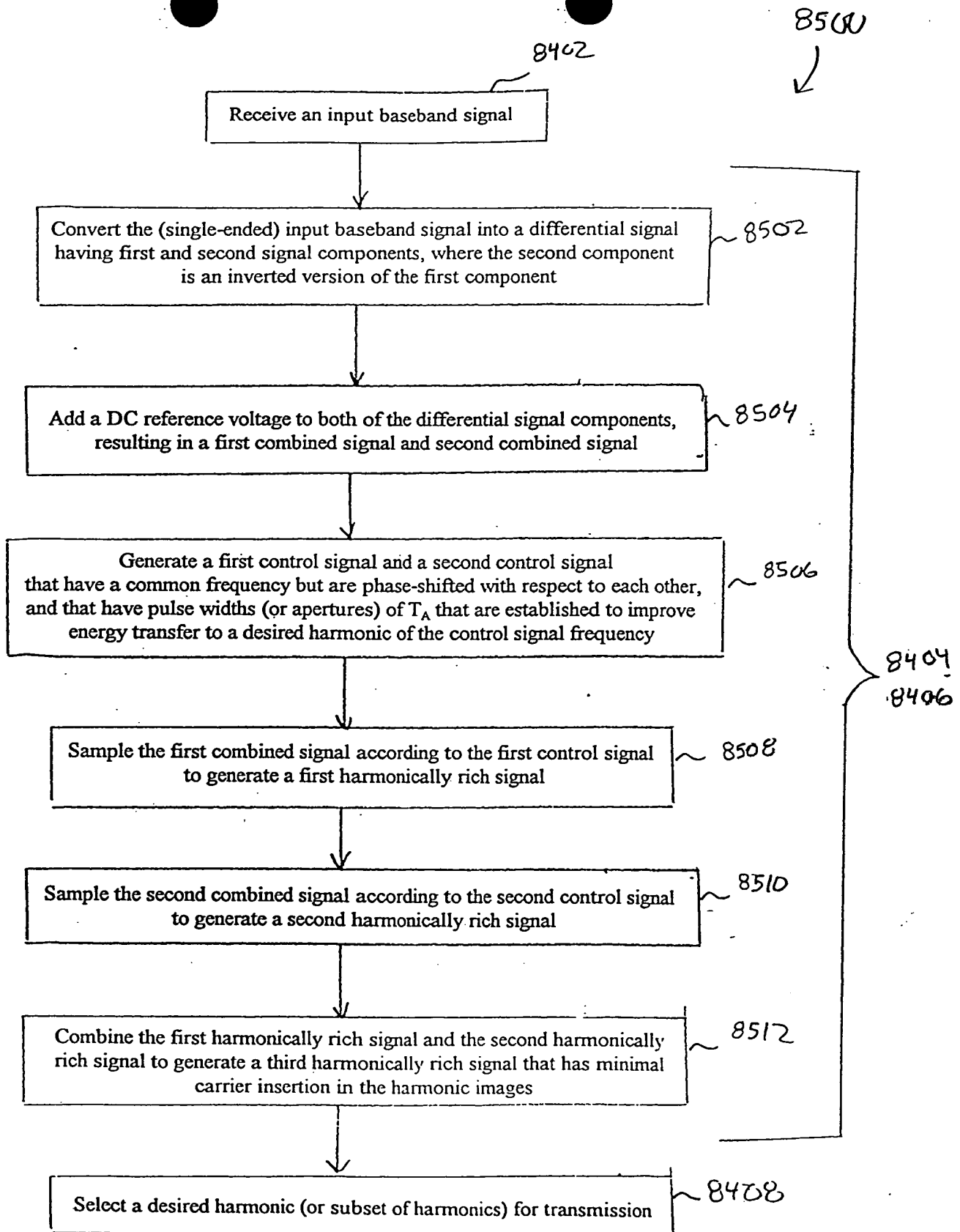


FIG. 85

8600

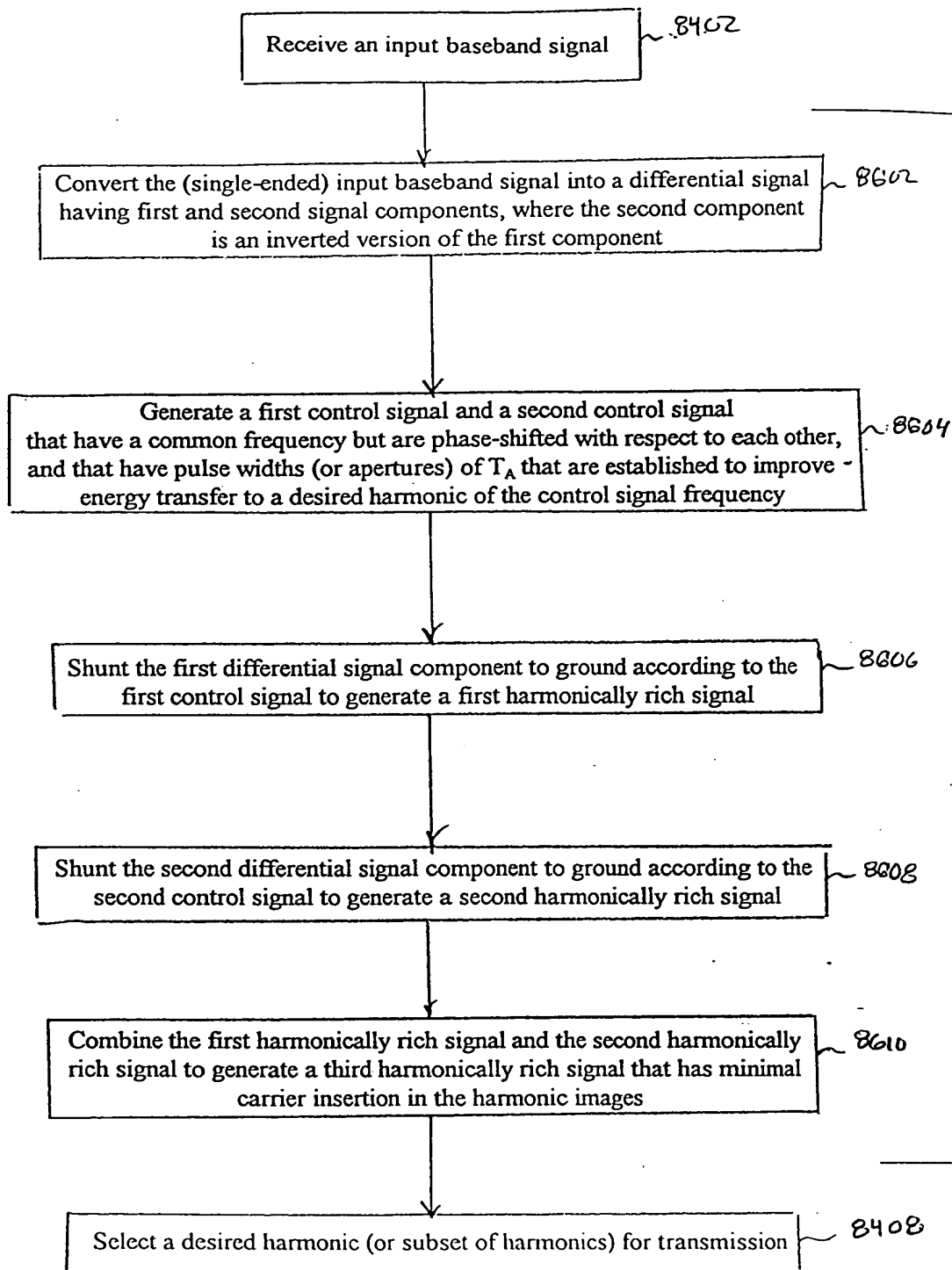


FIG. 86

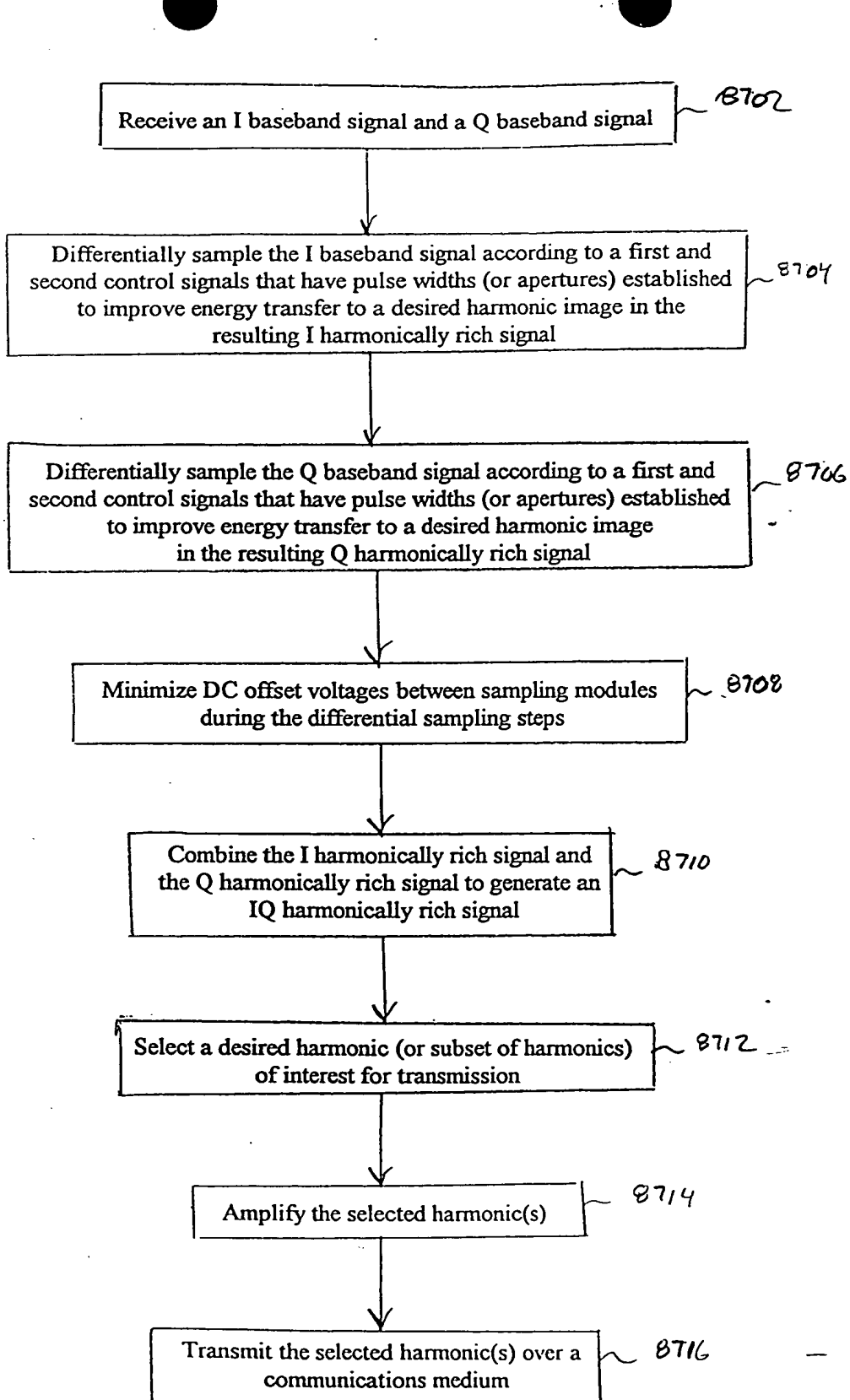
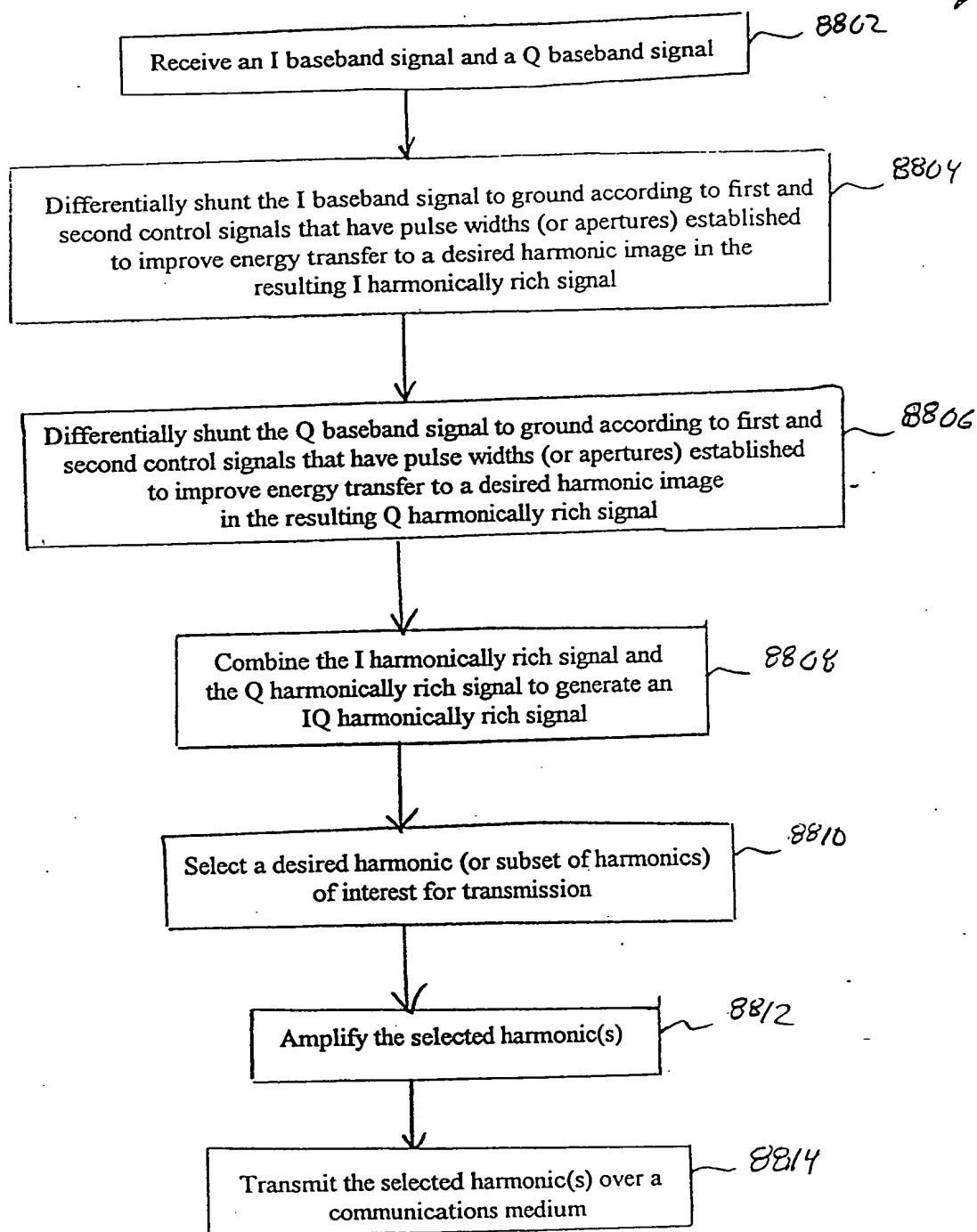
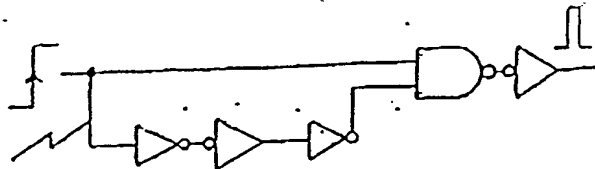


FIG. 87

8800

FIG. 88

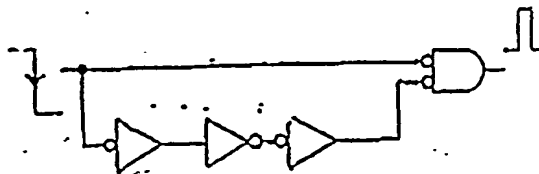
8912
↓



A. rising edge pulse generator

FIG. 89D

8916
↓



B. falling-edge pulse generator

FIG. 89E

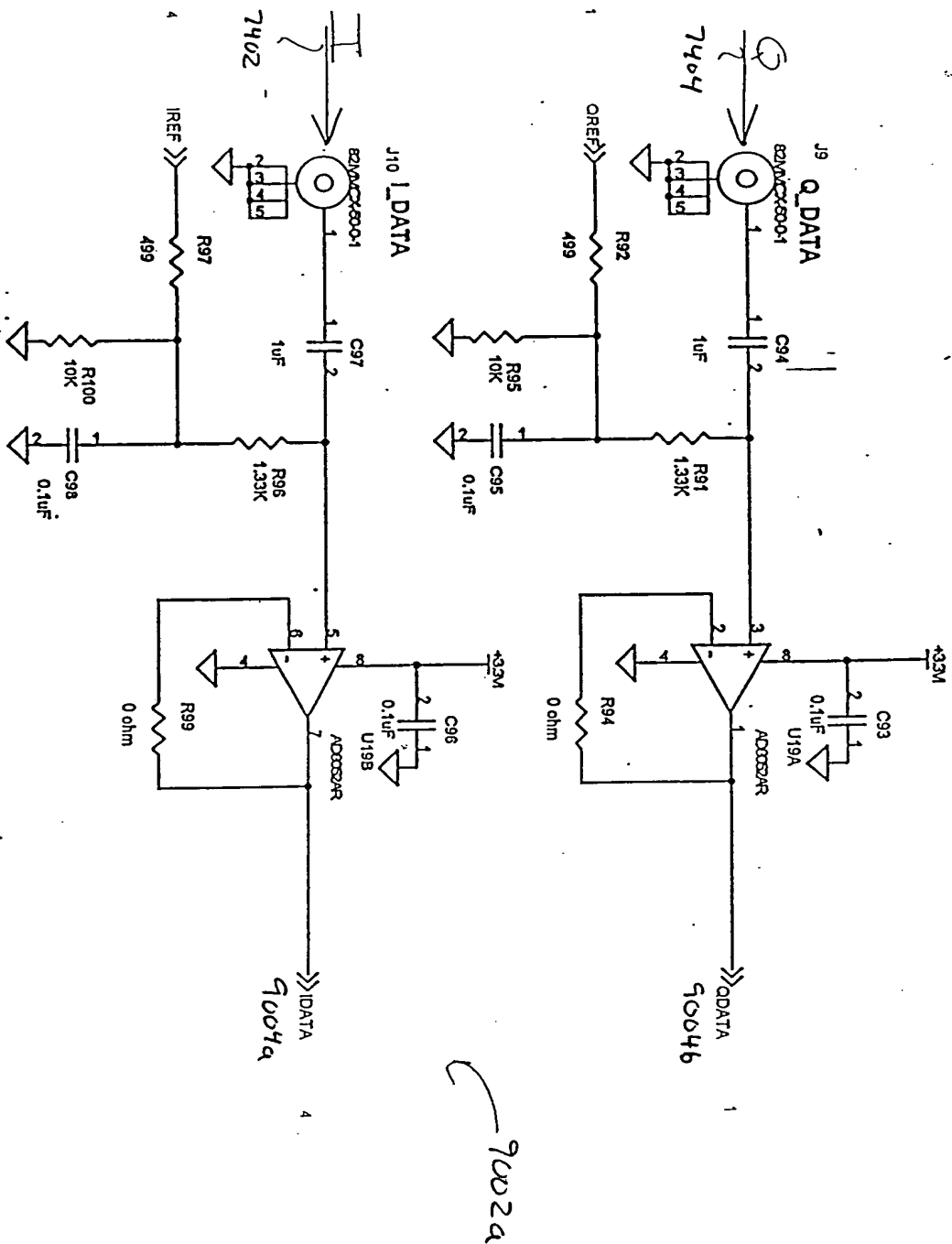


FIG. 90A



F_{IS. 90C}

Q Channel
9008-

Jumper to TP20 if required

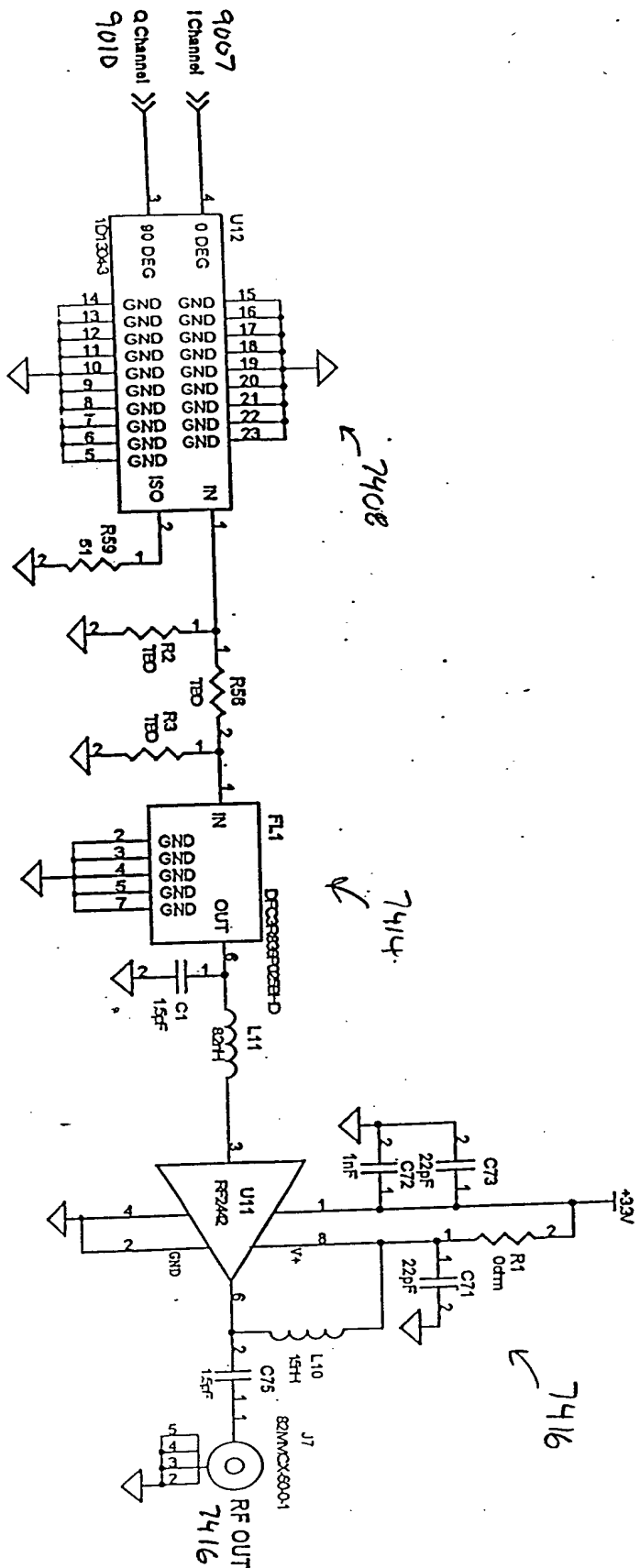


FIG 90D

Combiner
90D

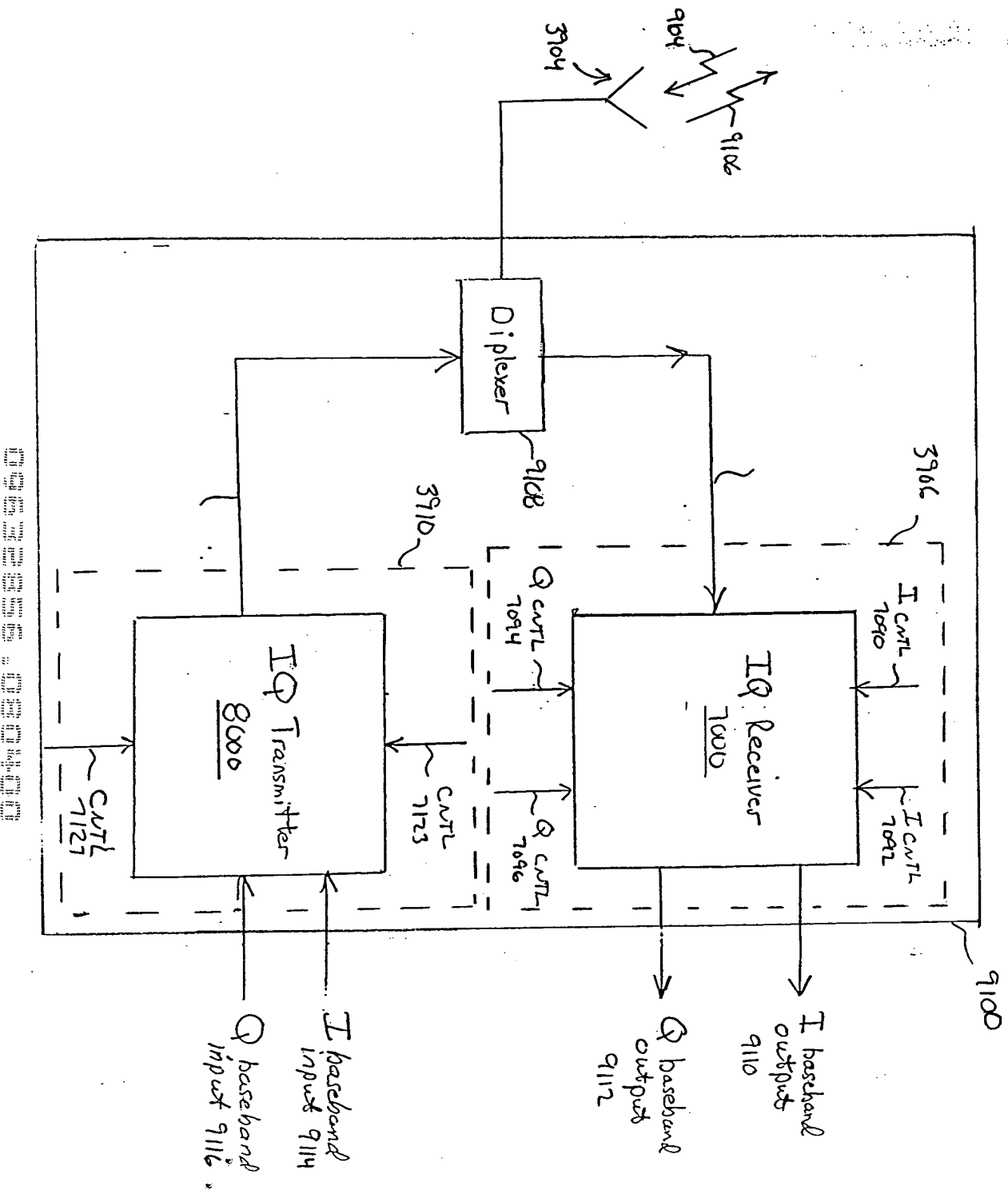
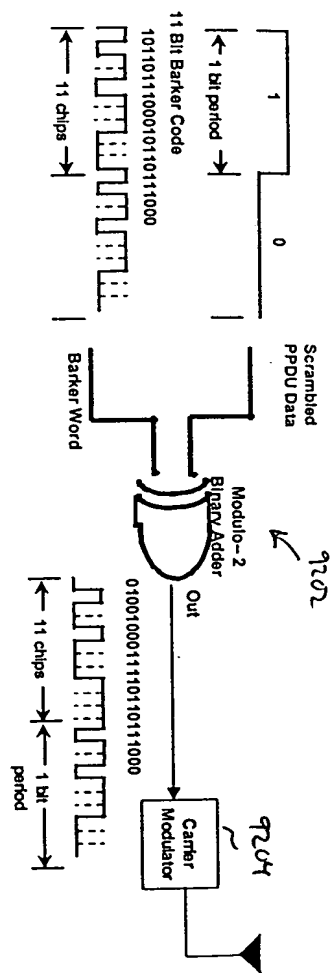
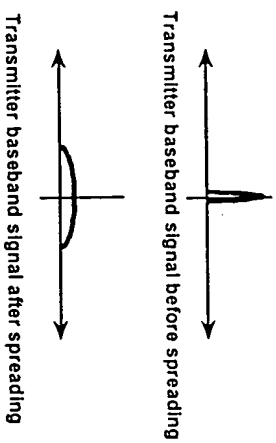


Fig. 91



Transmit Spectrum



Receiver Spectrum

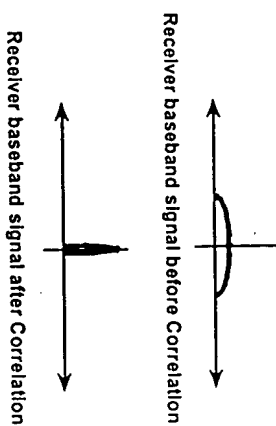


Fig 92

FIG. 95A

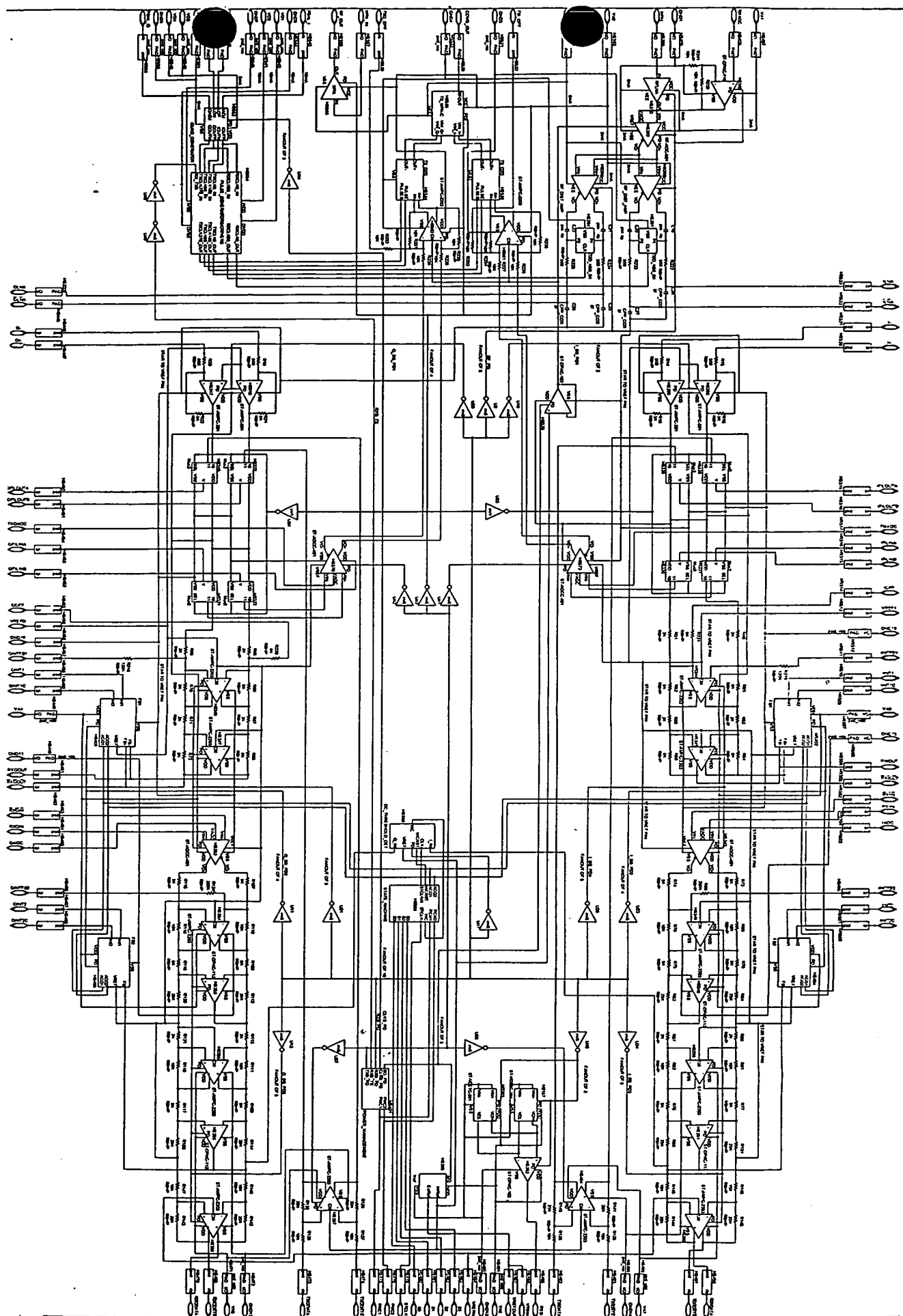
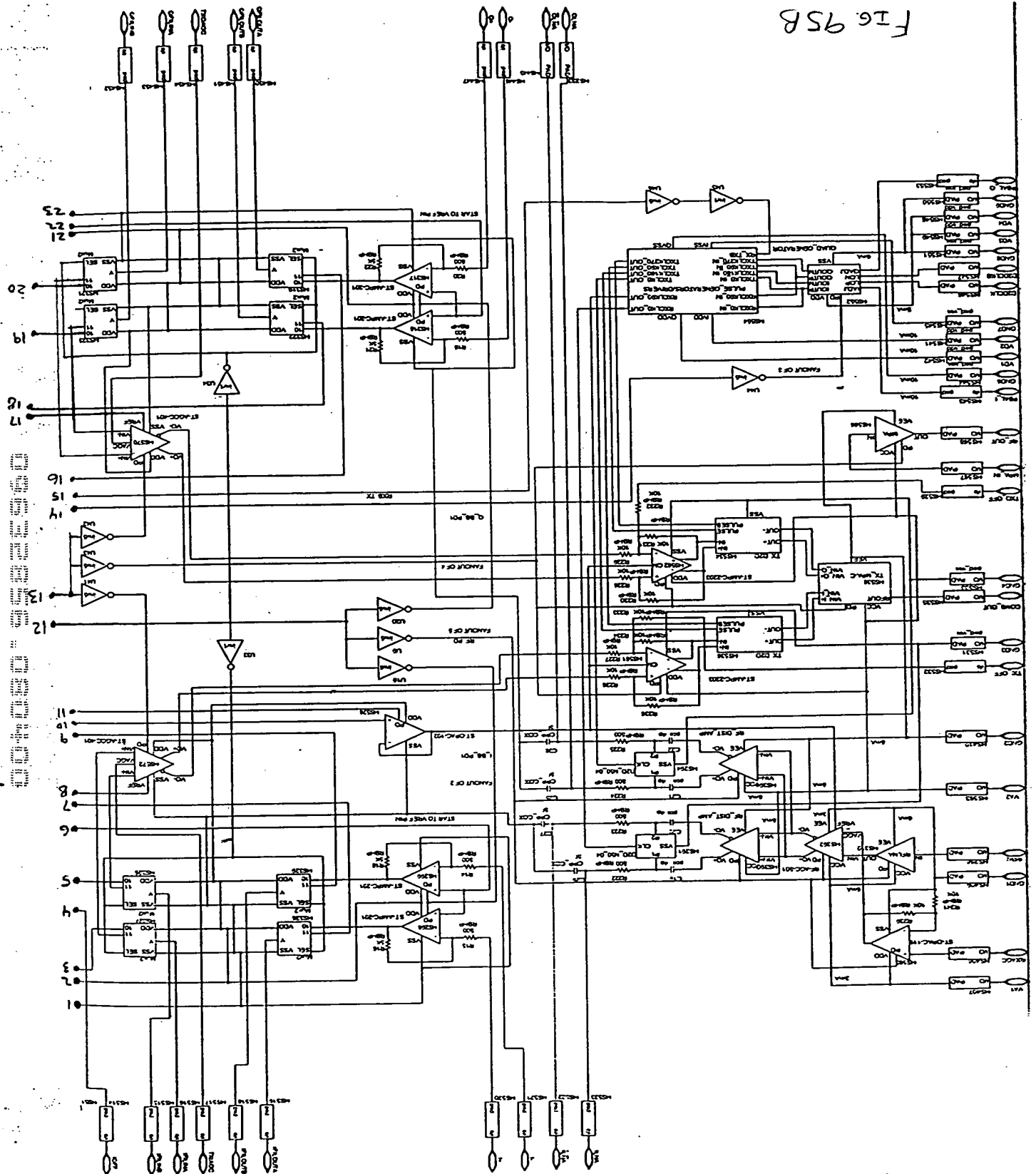


Fig. 95B



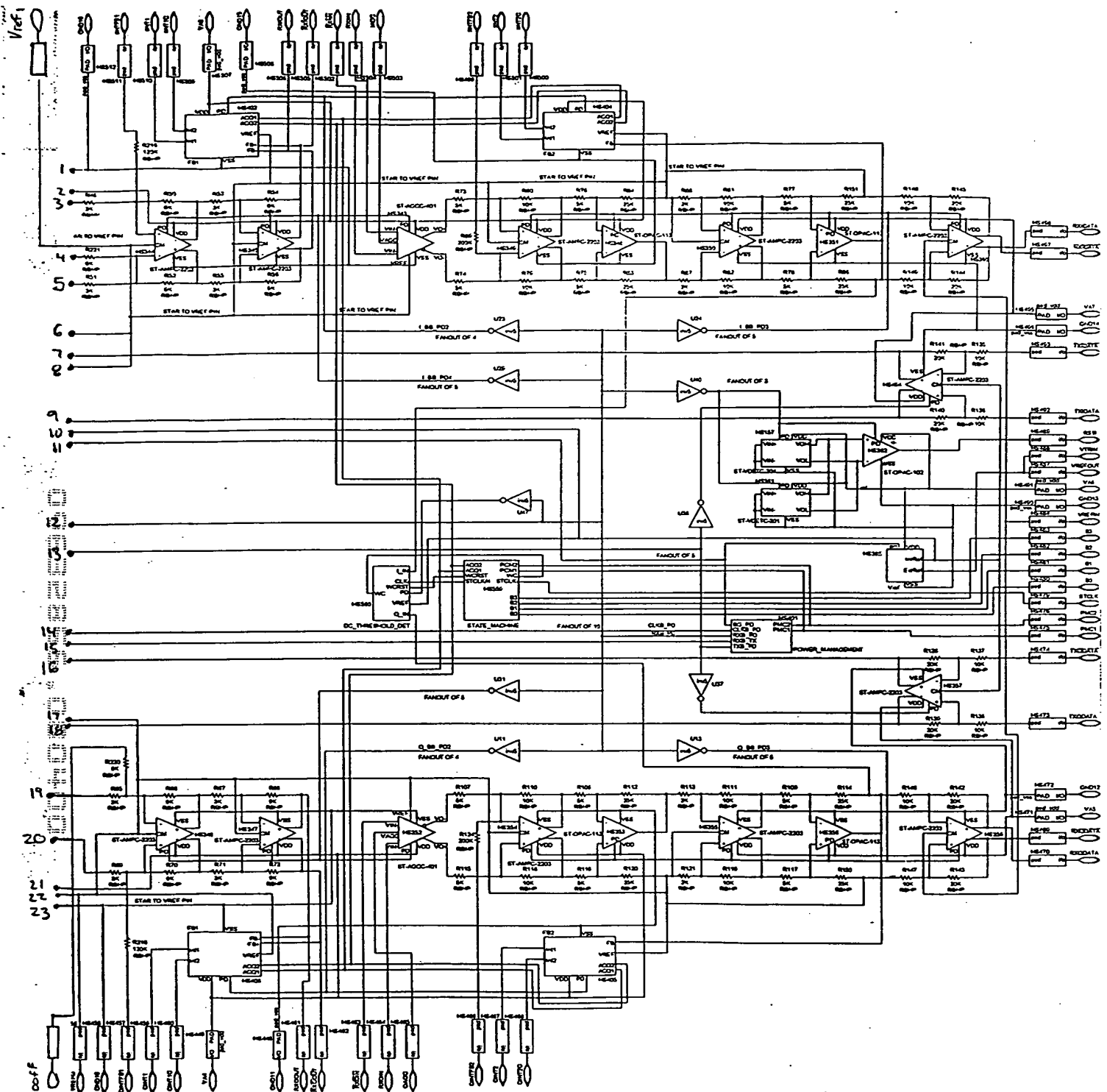


FIG. 95C

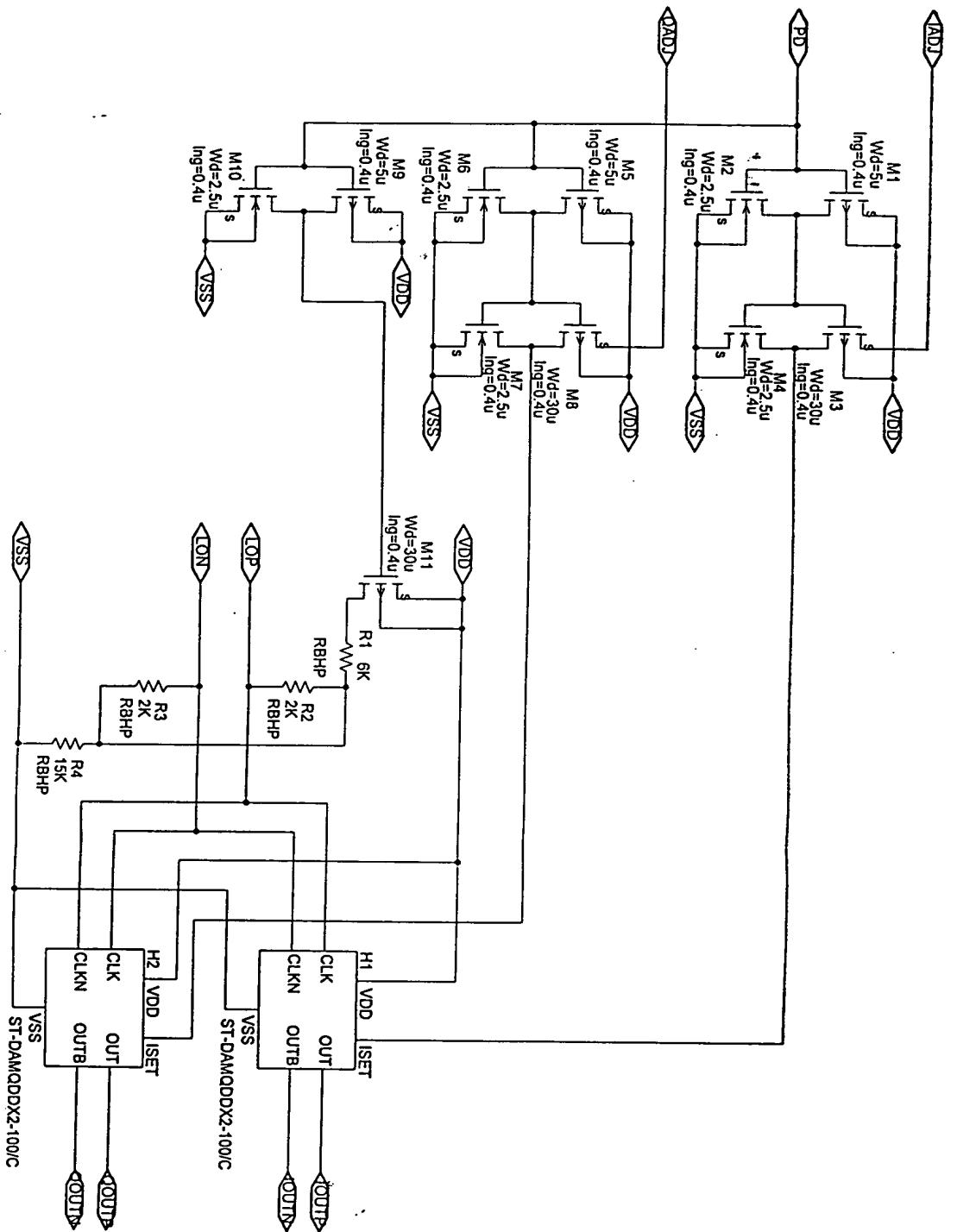


FIG. 76

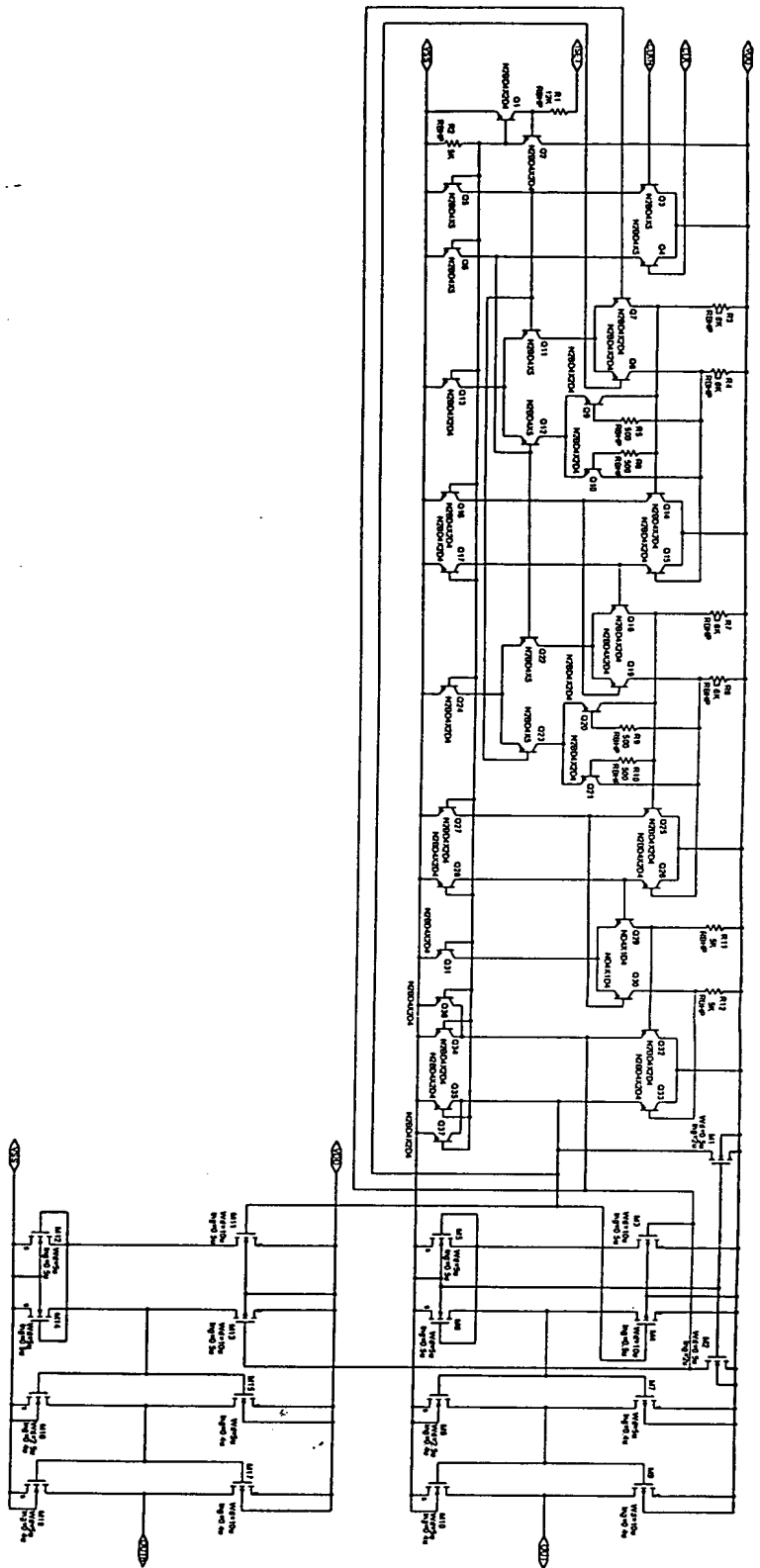


FIG. 77

FIG. 77 is a schematic diagram of a multi-stage electronic circuit, likely a vacuum tube amplifier. The circuit is organized into several horizontal sections. The top section features a power supply or heater circuit with components like 6X4, 6X5, 6X6, 6X7, 6X8, 6X9, 6X10, 6X11, 6X12, 6X13, 6X14, 6X15, 6X16, 6X17, 6X18, 6X19, 6X20, 6X21, 6X22, 6X23, 6X24, 6X25, 6X26, 6X27, 6X28, 6X29, 6X30, 6X31, 6X32, 6X33, 6X34, 6X35, 6X36, 6X37, 6X38, 6X39, 6X40, 6X41, 6X42, 6X43, 6X44, 6X45, 6X46, 6X47, 6X48, 6X49, 6X50, 6X51, 6X52, 6X53, 6X54, 6X55, 6X56, 6X57, 6X58, 6X59, 6X60, 6X61, 6X62, 6X63, 6X64, 6X65, 6X66, 6X67, 6X68, 6X69, 6X70, 6X71, 6X72, 6X73, 6X74, 6X75, 6X76, 6X77, 6X78, 6X79, 6X80, 6X81, 6X82, 6X83, 6X84, 6X85, 6X86, 6X87, 6X88, 6X89, 6X90, 6X91, 6X92, 6X93, 6X94, 6X95, 6X96, 6X97, 6X98, 6X99, 6X100. The middle section contains a series of vacuum tube stages, each with its own set of resistors and capacitors. The bottom section shows a series of output stages, each with its own set of resistors and capacitors. The diagram is a technical drawing of an electronic circuit, showing the internal connections and components of a device.

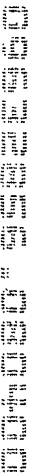


FIG. 98

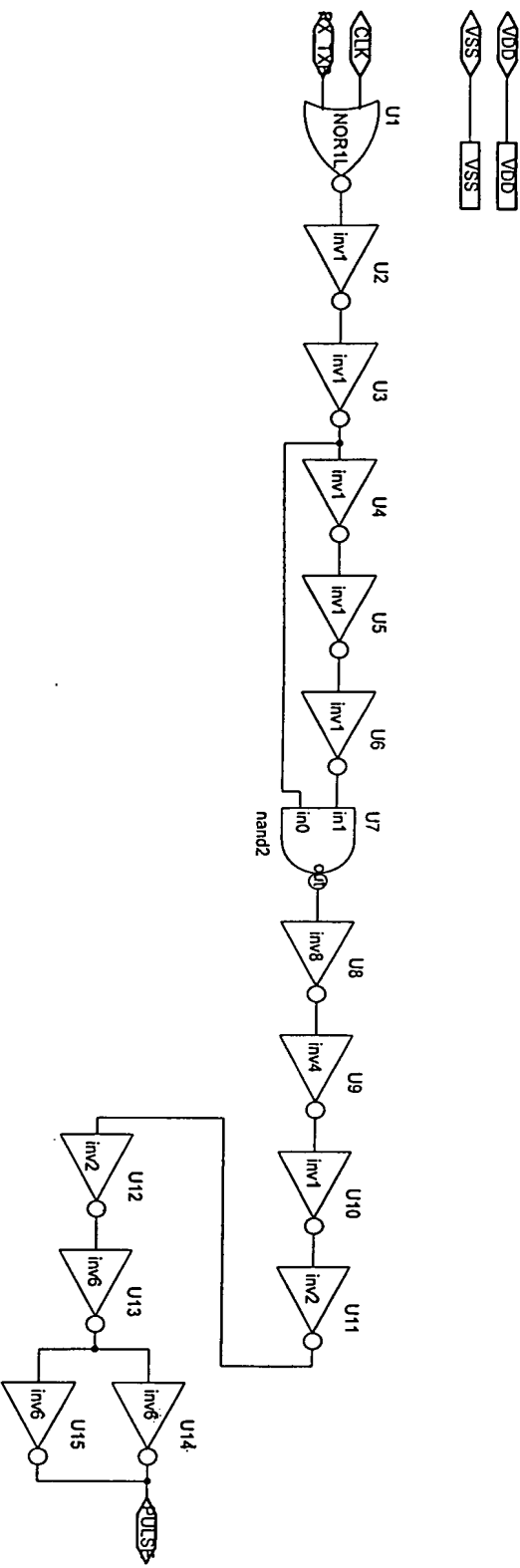


Fig. 99

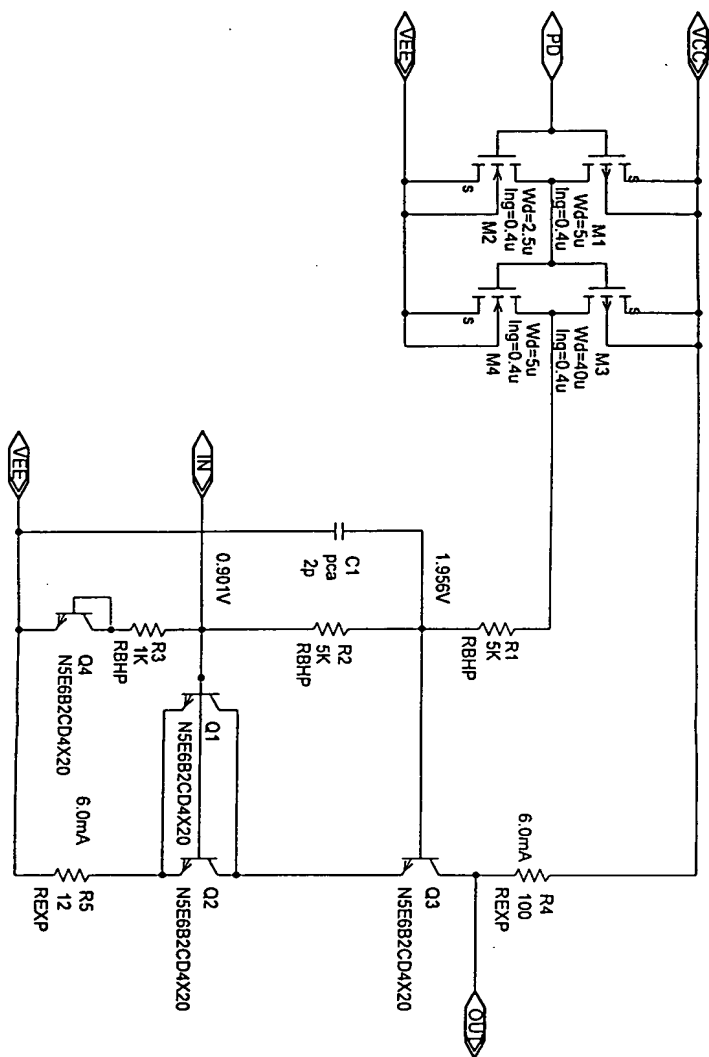


Fig. 100

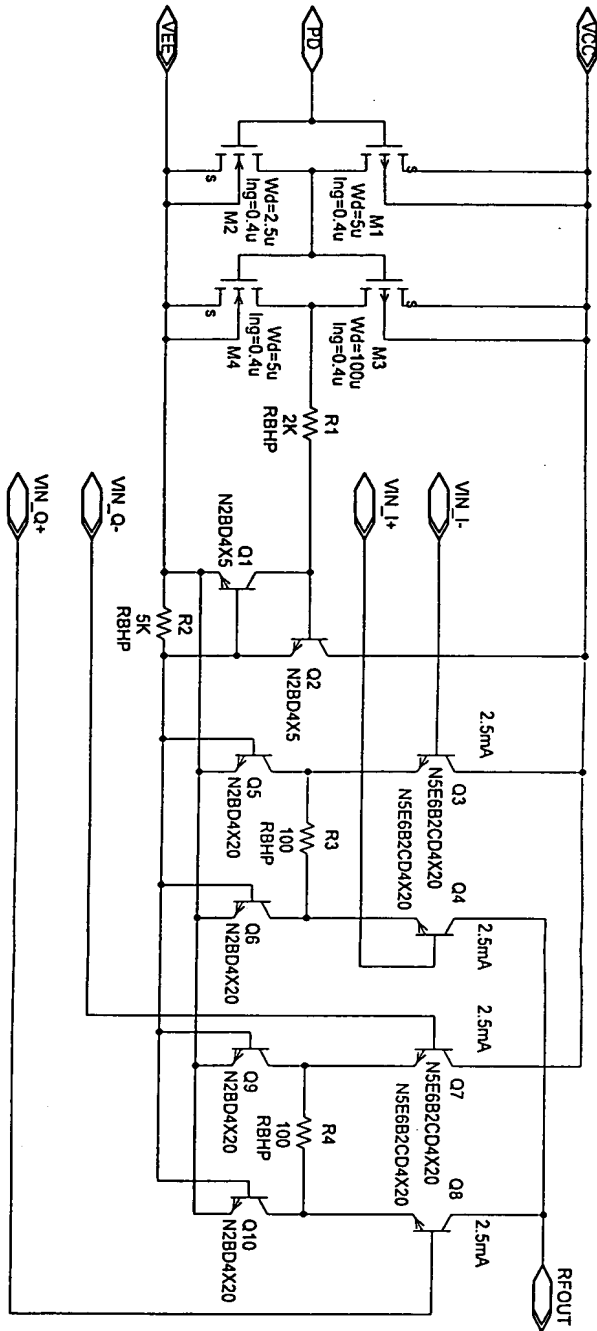


FIG. 101

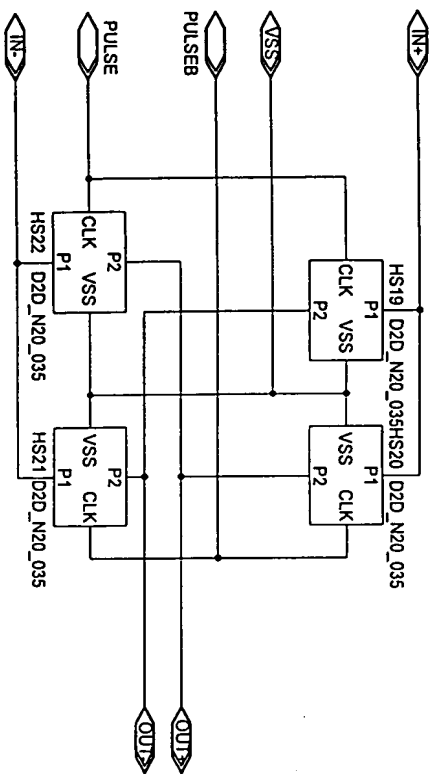


FIG. 102

FIG. 102 is a schematic diagram of a differential signal processing circuit, showing two input stages and two output stages, each with a differential pair of transistors and associated control signals.

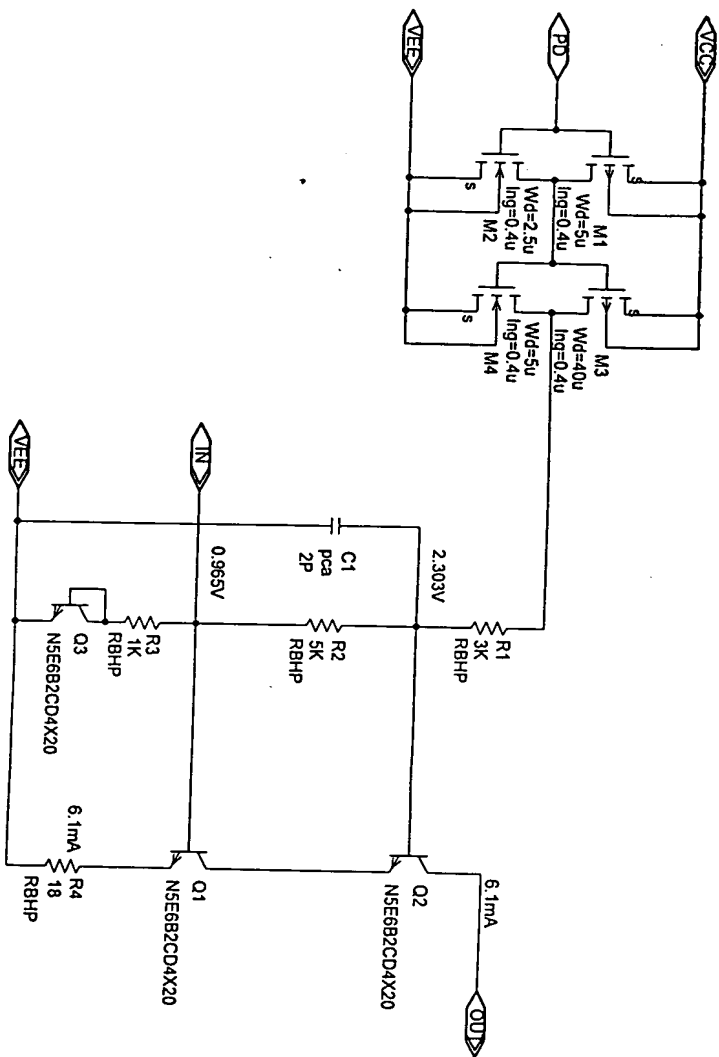


FIG. 103

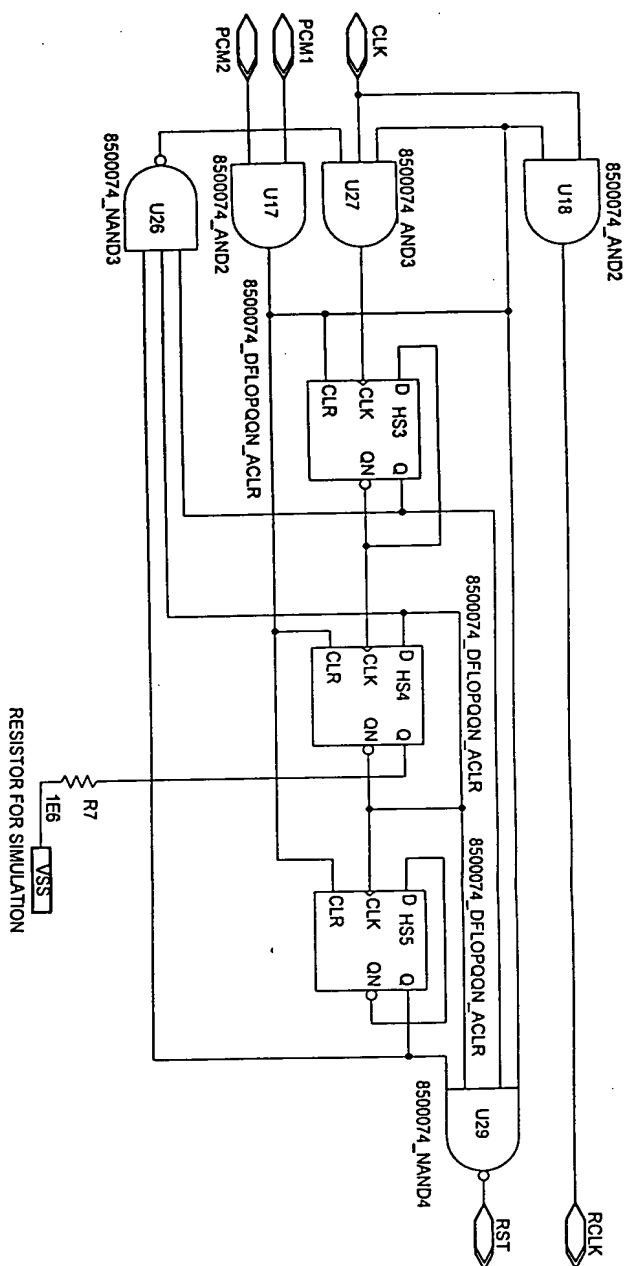
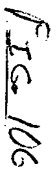


Fig. 164



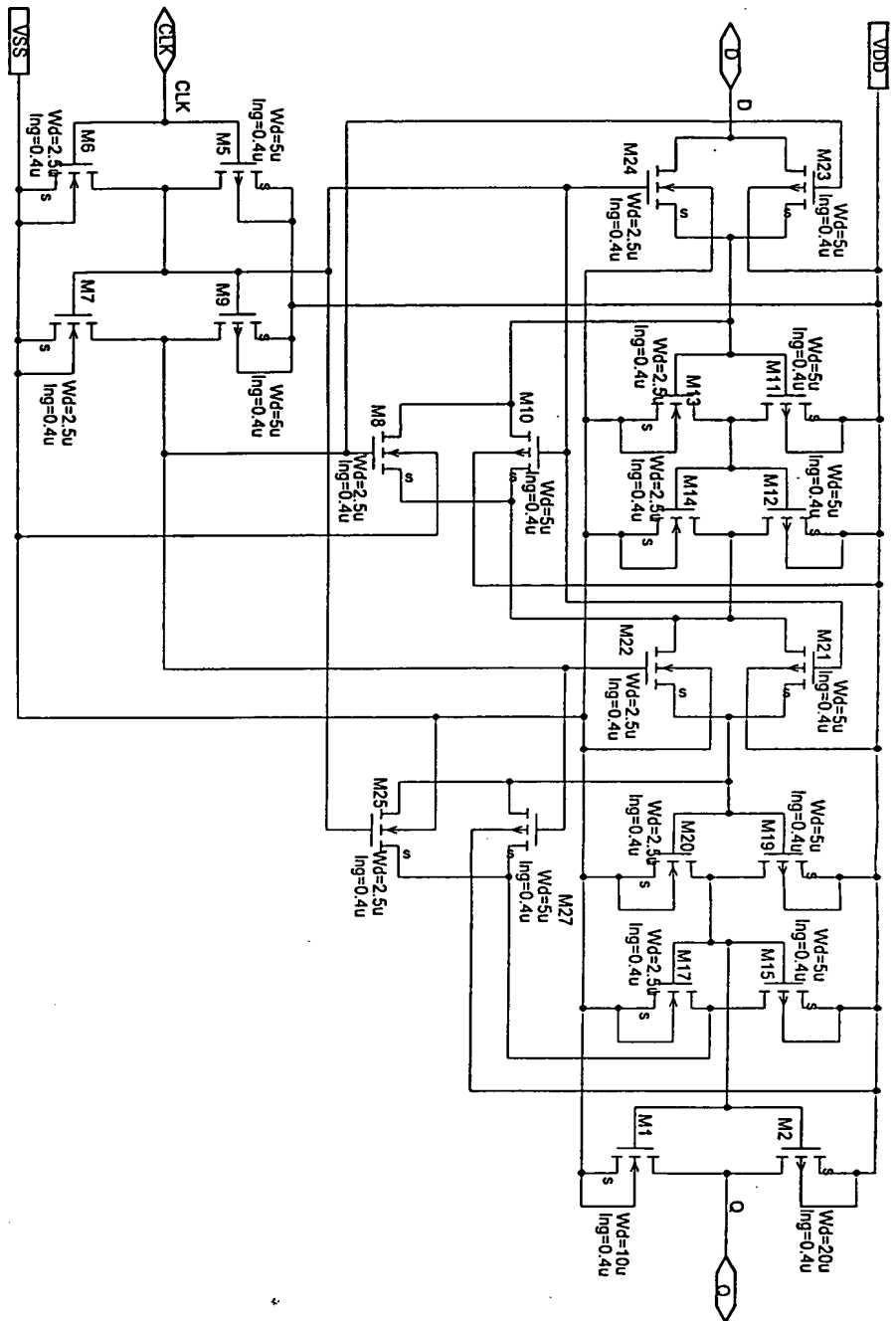


FIG. 107

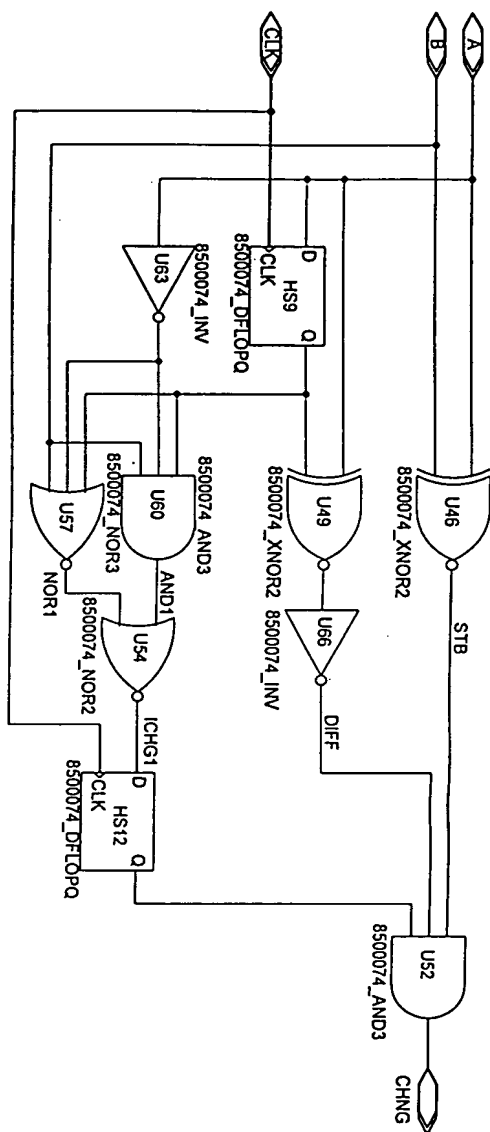


Fig. 108

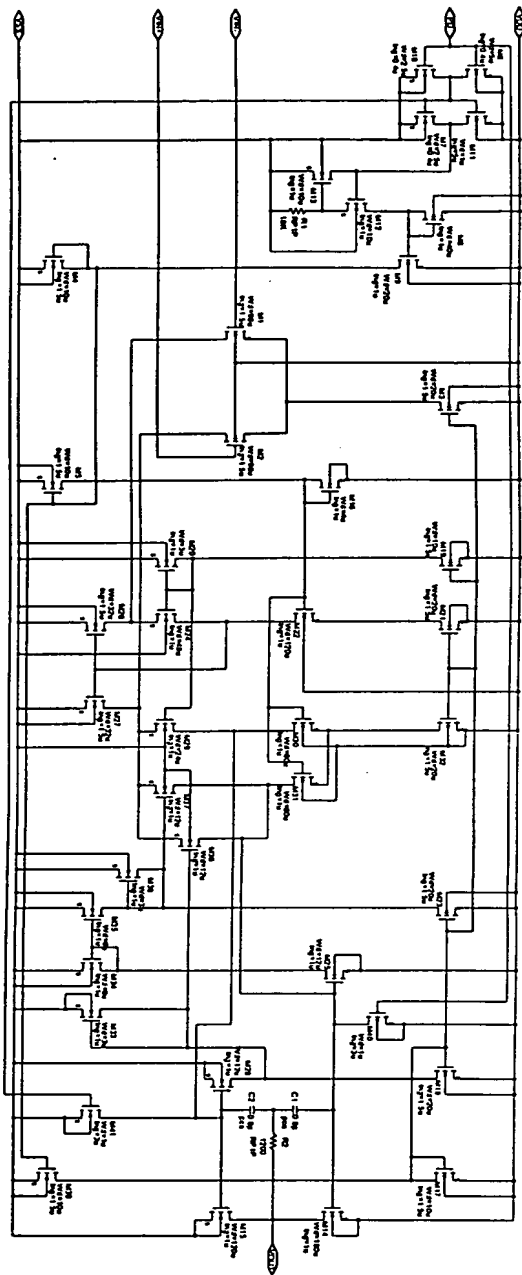


Fig. 109

THIS DOCUMENT CONTAINS NEITHER RECOMMENDATIONS NOR
 CONCLUSIONS OF THE NATIONAL BUREAU OF STANDARDS
 AND IS NOT INTENDED TO BE USED IN LEGAL PROCEEDINGS

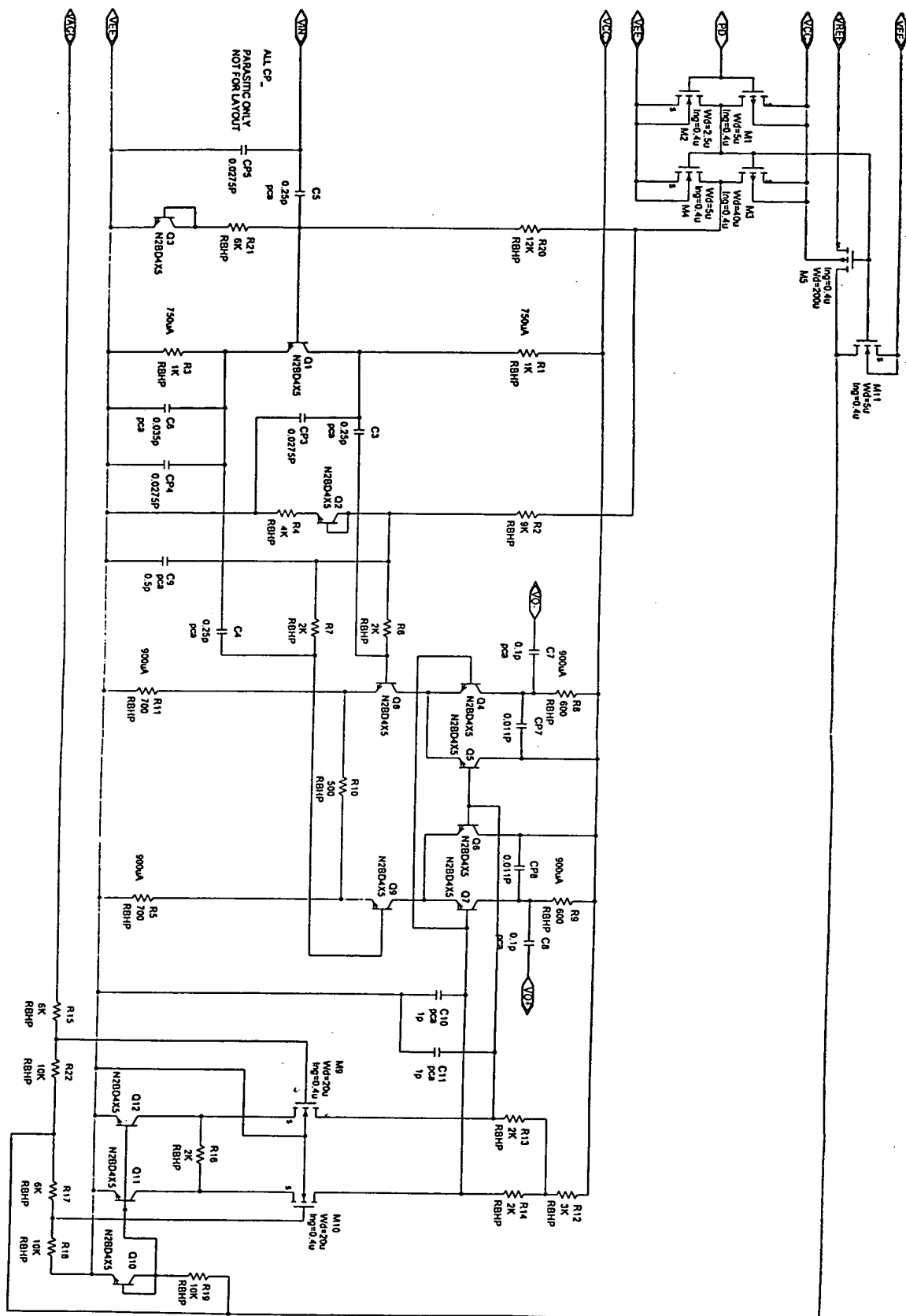


Fig. 110

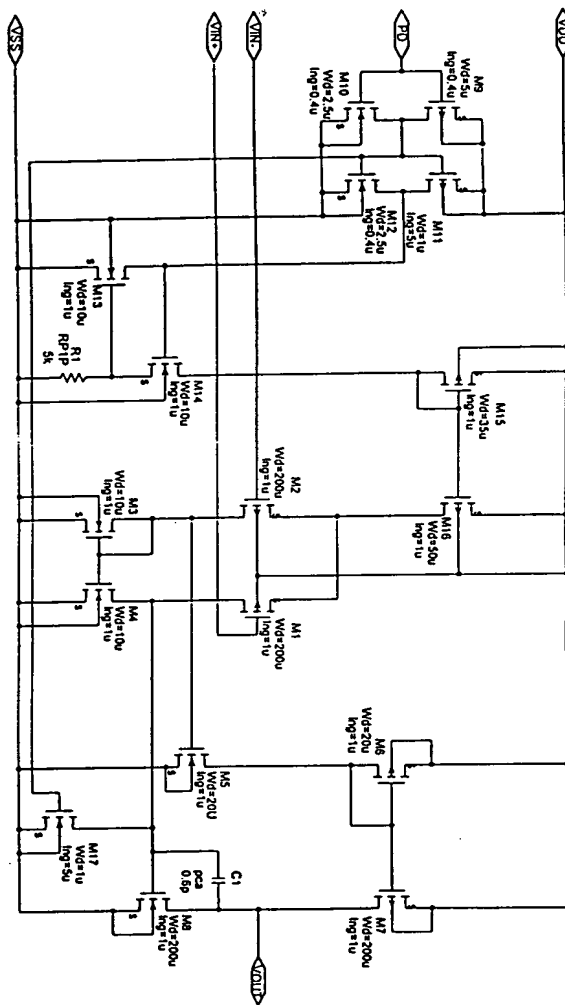


FIG. 111

FIG. 111 is a circuit schematic diagram of a multi-stage CMOS amplifier.



(The following are the names of the persons who have been elected to the various offices of the Association, as reported by the Secretary.)

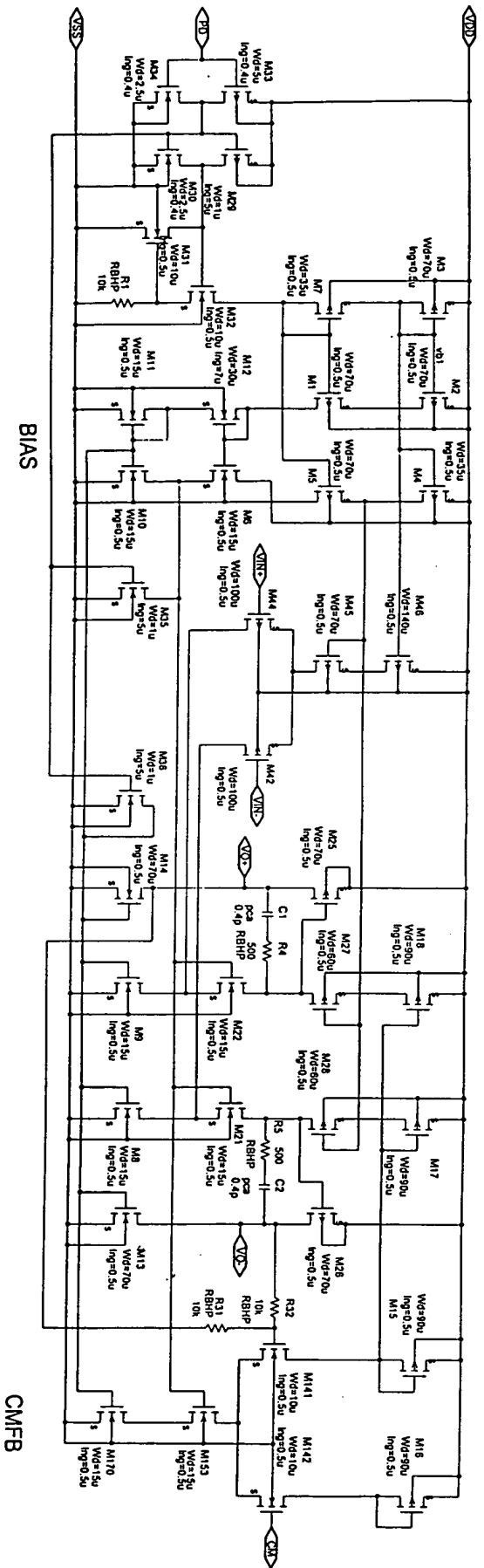


Fig. 113

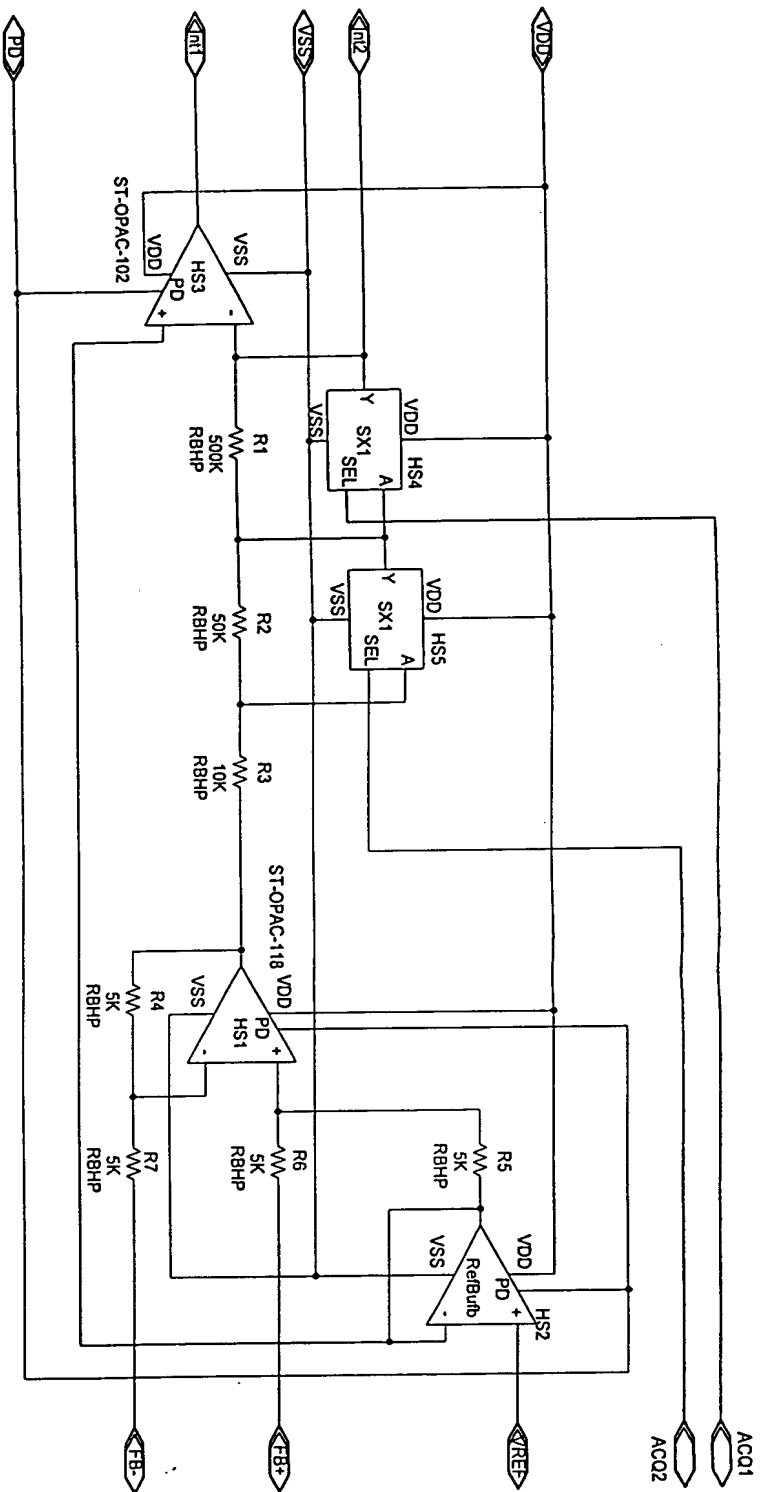


FIG. 114

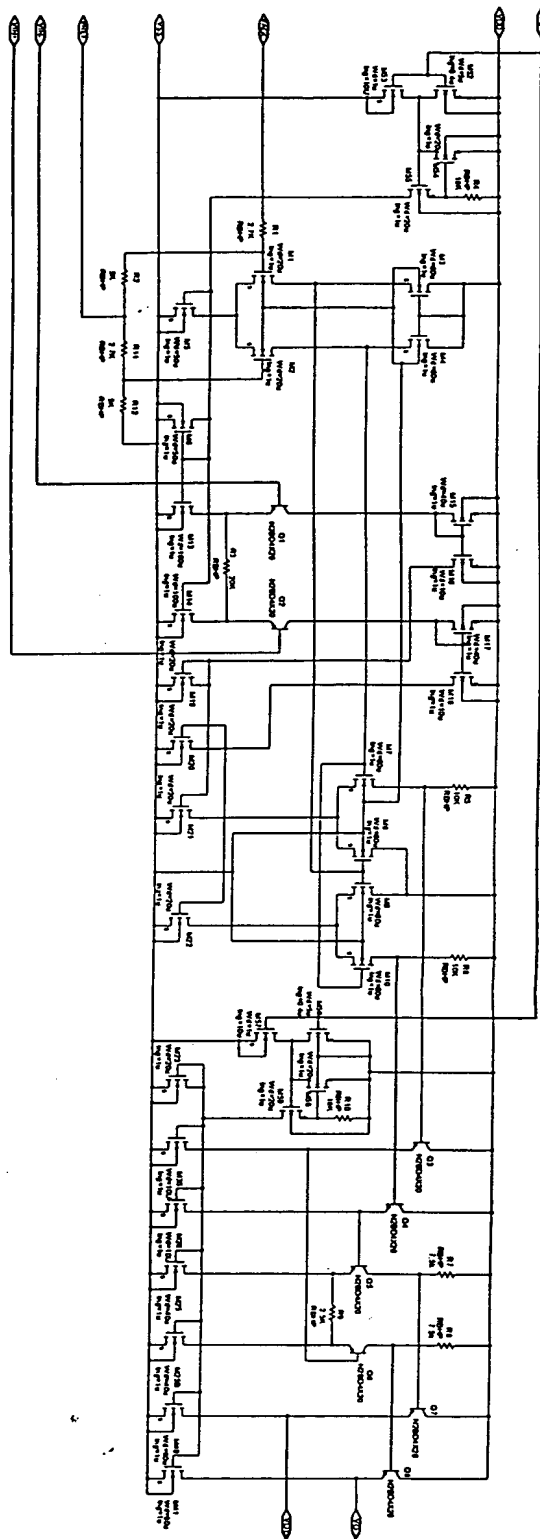


Fig. 115

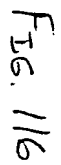


FIG. 116

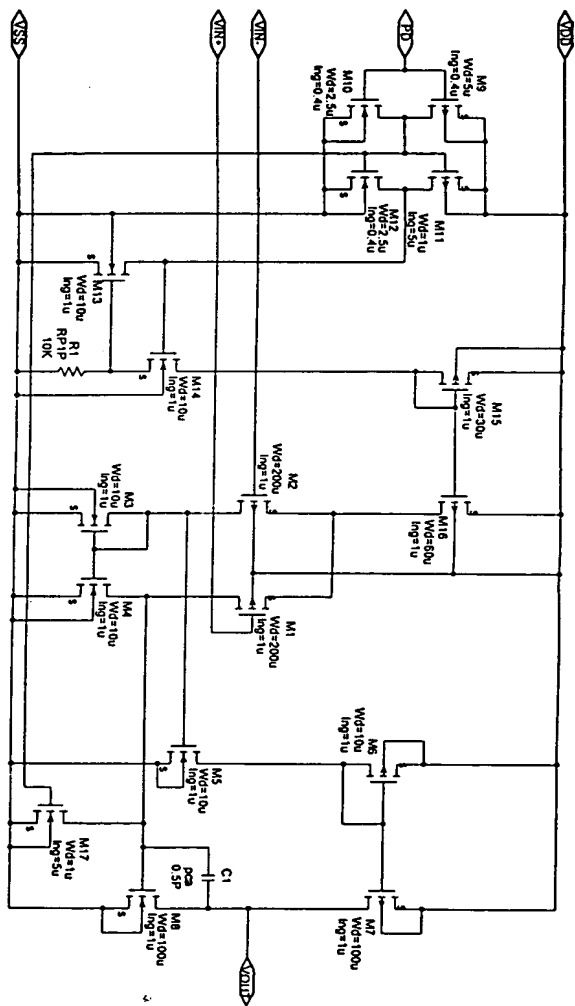


Fig. 117

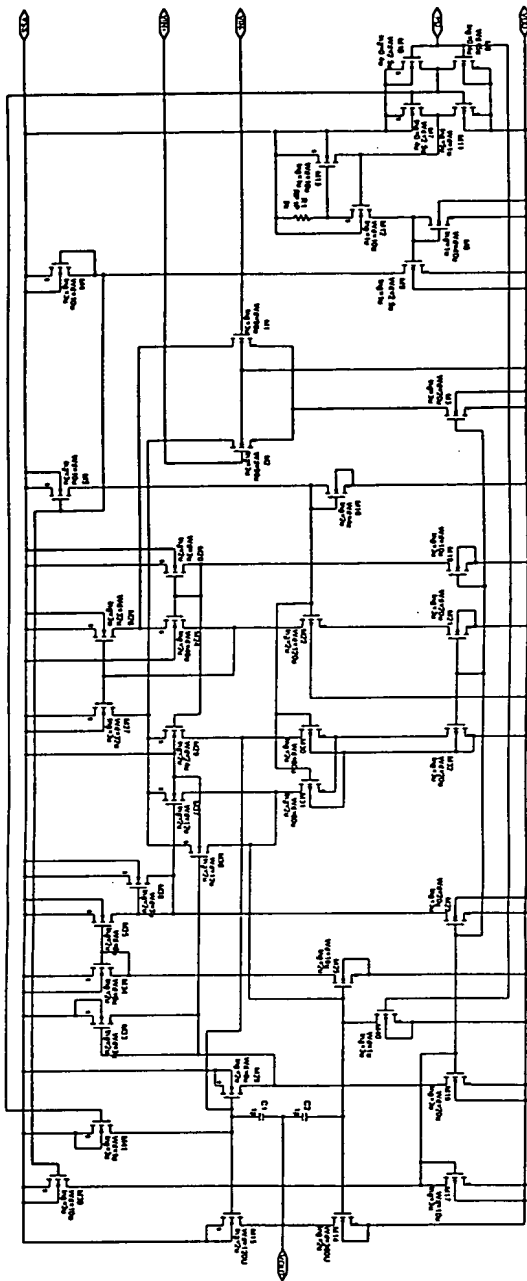
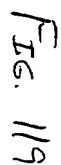
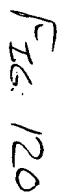


FIG 118

FIG 118 is a schematic diagram of a circuit for a memory array. The circuit includes a grid of horizontal and vertical lines representing data and address lines. The horizontal lines are labeled with addresses 000, 001, 002, 003, 004, 005, 006, 007, 008, 009, 010, 011, 012, 013, 014, 015, 016, 017, 018, 019, 020, 021, 022, 023, 024, 025, 026, 027, 028, 029, 030, 031, 032, 033, 034, 035, 036, 037, 038, 039, 040, 041, 042, 043, 044, 045, 046, 047, 048, 049, 050, 051, 052, 053, 054, 055, 056, 057, 058, 059, 060, 061, 062, 063, 064, 065, 066, 067, 068, 069, 070, 071, 072, 073, 074, 075, 076, 077, 078, 079, 080, 081, 082, 083, 084, 085, 086, 087, 088, 089, 090, 091, 092, 093, 094, 095, 096, 097, 098, 099, 100, 101, 102, 103, 104, 105, 106, 107, 108, 109, 110, 111, 112, 113, 114, 115, 116, 117, 118, 119, 120, 121, 122, 123, 124, 125, 126, 127, 128, 129, 130, 131, 132, 133, 134, 135, 136, 137, 138, 139, 140, 141, 142, 143, 144, 145, 146, 147, 148, 149, 150, 151, 152, 153, 154, 155, 156, 157, 158, 159, 160, 161, 162, 163, 164, 165, 166, 167, 168, 169, 170, 171, 172, 173, 174, 175, 176, 177, 178, 179, 180, 181, 182, 183, 184, 185, 186, 187, 188, 189, 190, 191, 192, 193, 194, 195, 196, 197, 198, 199, 200, 201, 202, 203, 204, 205, 206, 207, 208, 209, 210, 211, 212, 213, 214, 215, 216, 217, 218, 219, 220, 221, 222, 223, 224, 225, 226, 227, 228, 229, 230, 231, 232, 233, 234, 235, 236, 237, 238, 239, 240, 241, 242, 243, 244, 245, 246, 247, 248, 249, 250, 251, 252, 253, 254, 255, 256, 257, 258, 259, 260, 261, 262, 263, 264, 265, 266, 267, 268, 269, 270, 271, 272, 273, 274, 275, 276, 277, 278, 279, 280, 281, 282, 283, 284, 285, 286, 287, 288, 289, 290, 291, 292, 293, 294, 295, 296, 297, 298, 299, 300, 301, 302, 303, 304, 305, 306, 307, 308, 309, 310, 311, 312, 313, 314, 315, 316, 317, 318, 319, 320, 321, 322, 323, 324, 325, 326, 327, 328, 329, 330, 331, 332, 333, 334, 335, 336, 337, 338, 339, 340, 341, 342, 343, 344, 345, 346, 347, 348, 349, 350, 351, 352, 353, 354, 355, 356, 357, 358, 359, 360, 361, 362, 363, 364, 365, 366, 367, 368, 369, 370, 371, 372, 373, 374, 375, 376, 377, 378, 379, 380, 381, 382, 383, 384, 385, 386, 387, 388, 389, 390, 391, 392, 393, 394, 395, 396, 397, 398, 399, 400, 401, 402, 403, 404, 405, 406, 407, 408, 409, 410, 411, 412, 413, 414, 415, 416, 417, 418, 419, 420, 421, 422, 423, 424, 425, 426, 427, 428, 429, 430, 431, 432, 433, 434, 435, 436, 437, 438, 439, 440, 441, 442, 443, 444, 445, 446, 447, 448, 449, 450, 451, 452, 453, 454, 455, 456, 457, 458, 459, 460, 461, 462, 463, 464, 465, 466, 467, 468, 469, 470, 471, 472, 473, 474, 475, 476, 477, 478, 479, 480, 481, 482, 483, 484, 485, 486, 487, 488, 489, 490, 491, 492, 493, 494, 495, 496, 497, 498, 499, 500, 501, 502, 503, 504, 505, 506, 507, 508, 509, 510, 511, 512, 513, 514, 515, 516, 517, 518, 519, 520, 521, 522, 523, 524, 525, 526, 527, 528, 529, 530, 531, 532, 533, 534, 535, 536, 537, 538, 539, 540, 541, 542, 543, 544, 545, 546, 547, 548, 549, 550, 551, 552, 553, 554, 555, 556, 557, 558, 559, 560, 561, 562, 563, 564, 565, 566, 567, 568, 569, 570, 571, 572, 573, 574, 575, 576, 577, 578, 579, 580, 581, 582, 583, 584, 585, 586, 587, 588, 589, 590, 591, 592, 593, 594, 595, 596, 597, 598, 599, 600, 601, 602, 603, 604, 605, 606, 607, 608, 609, 610, 611, 612, 613, 614, 615, 616, 617, 618, 619, 620, 621, 622, 623, 624, 625, 626, 627, 628, 629, 630, 631, 632,



[illegible]

[illegible]

[illegible]

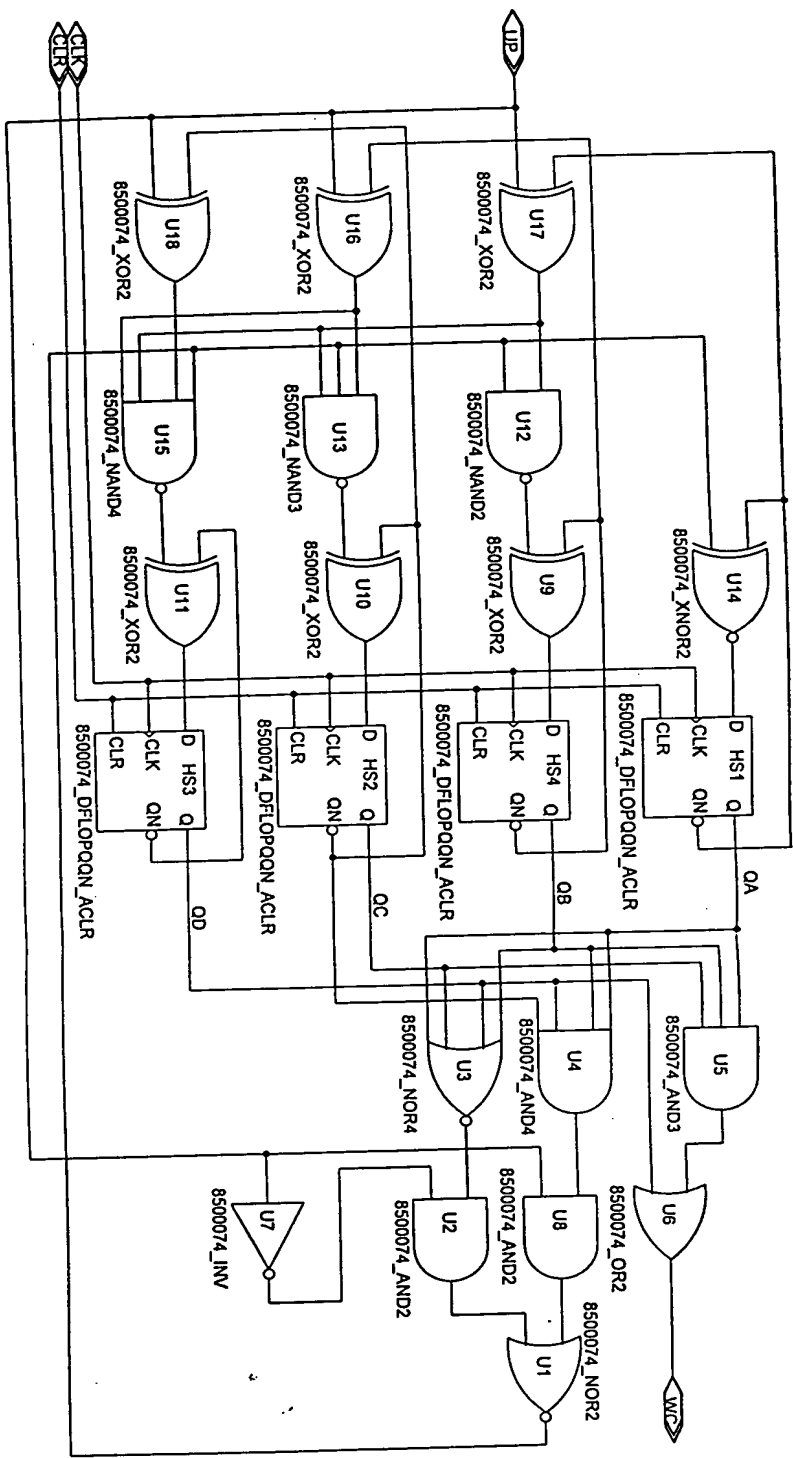


Fig. 124

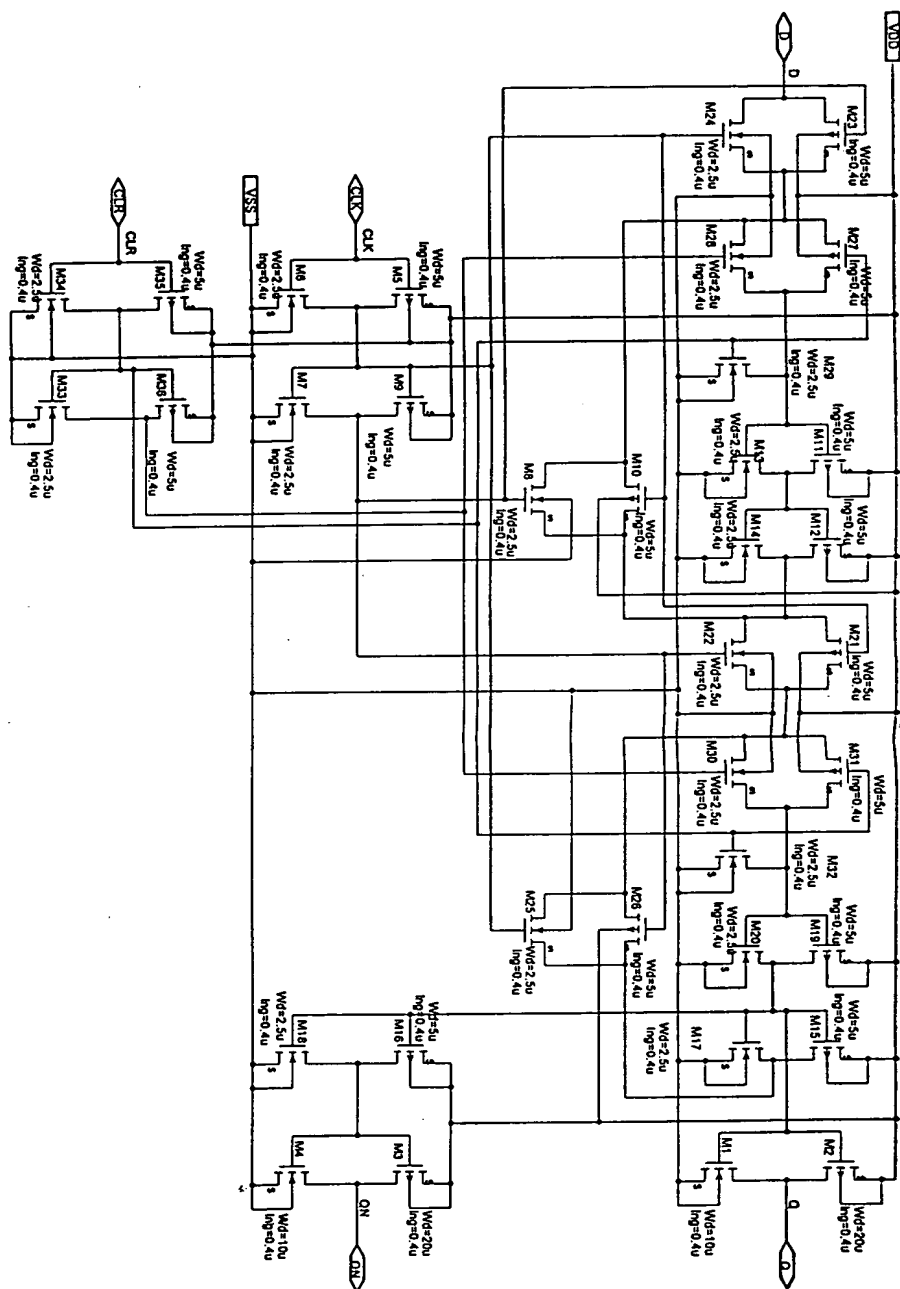


FIG. 125

FIG. 125

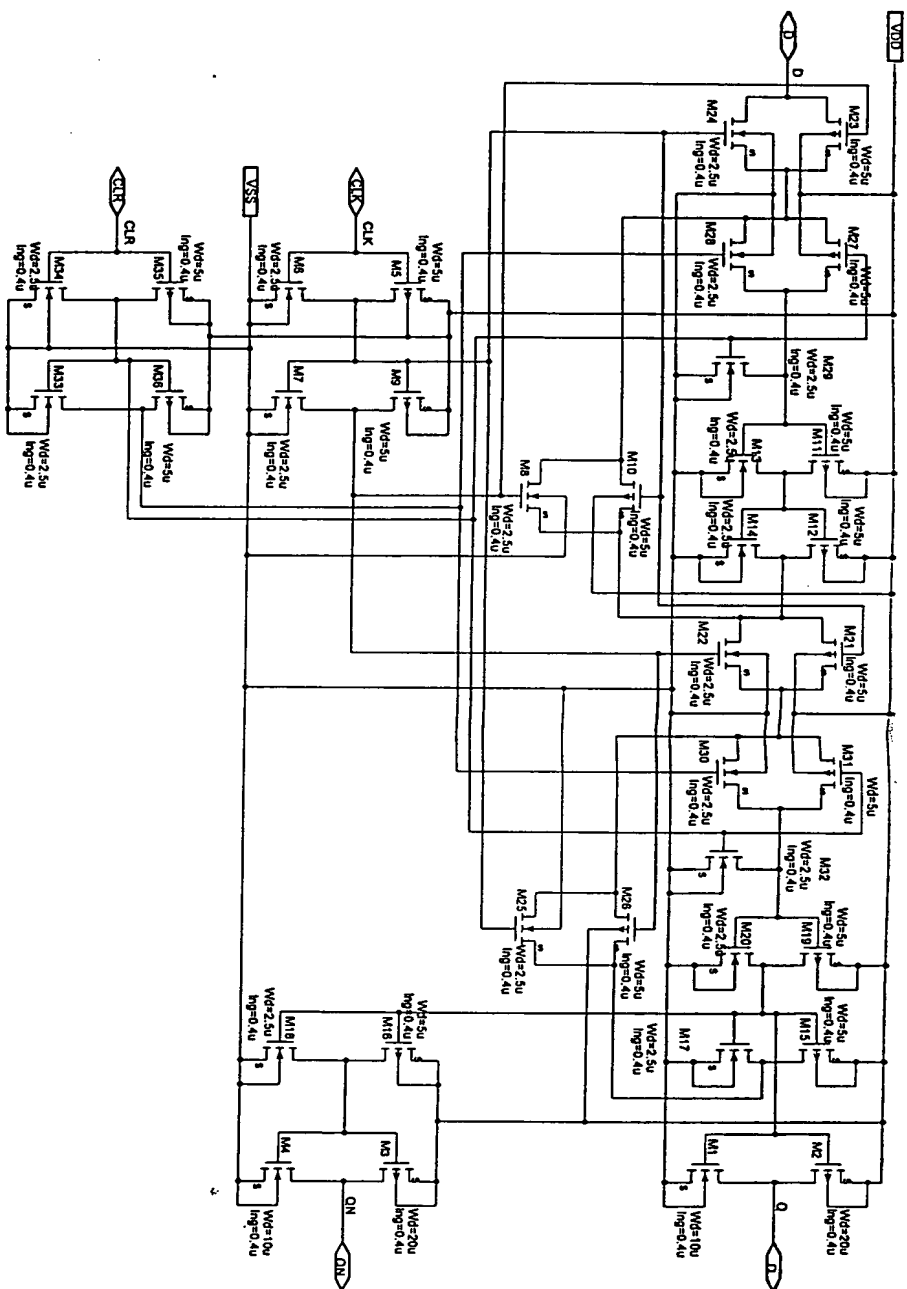


Fig 126

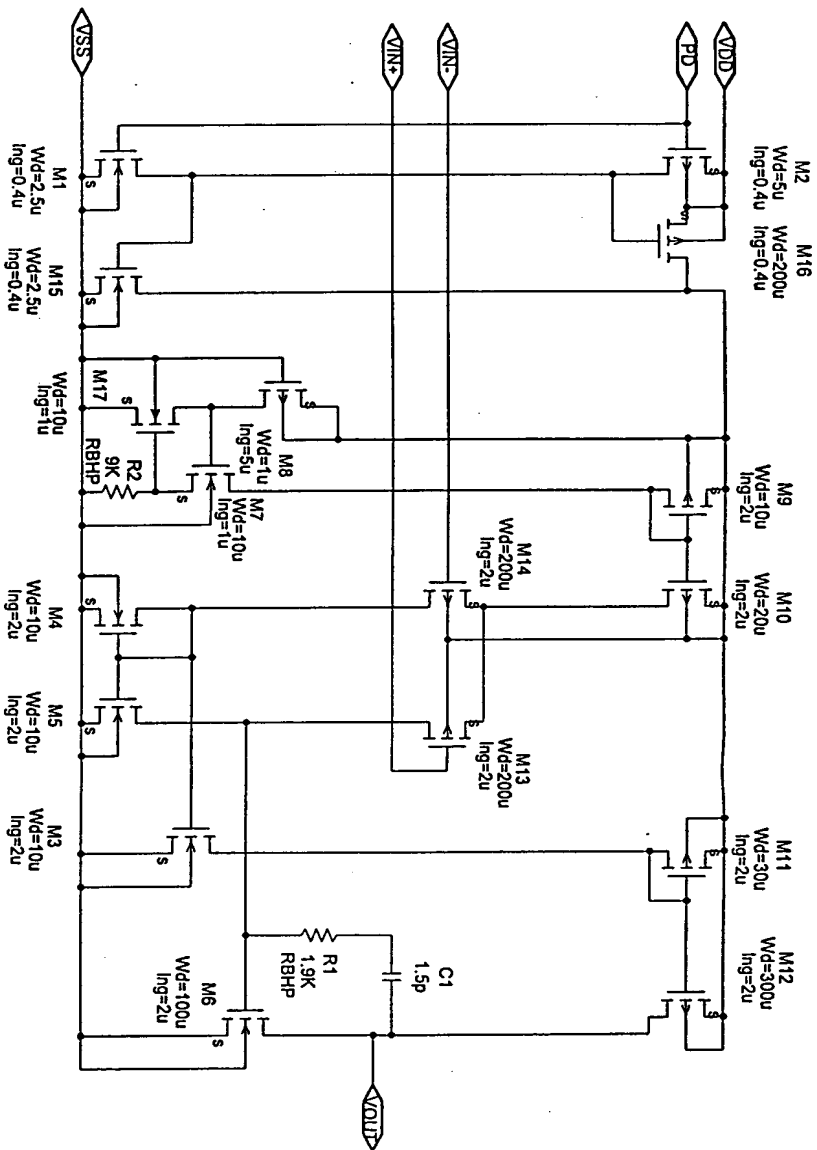


Fig. 128

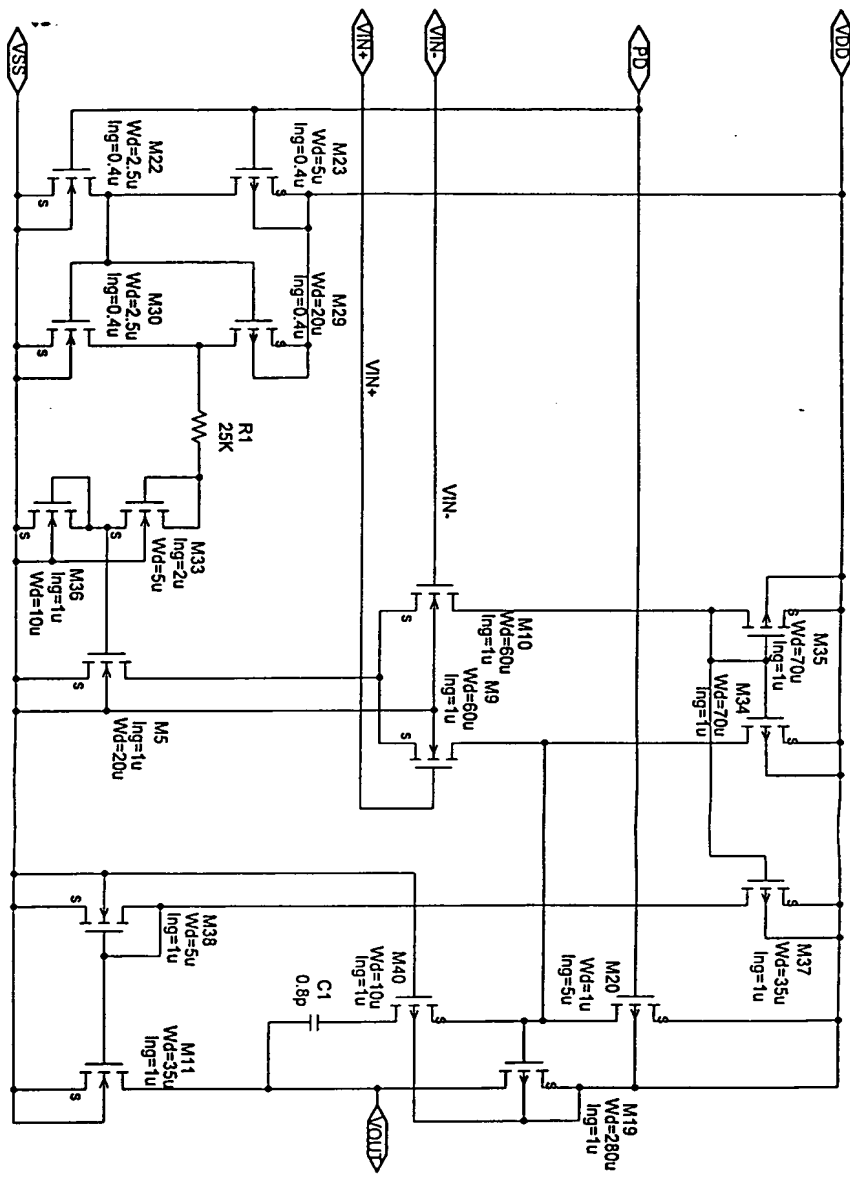
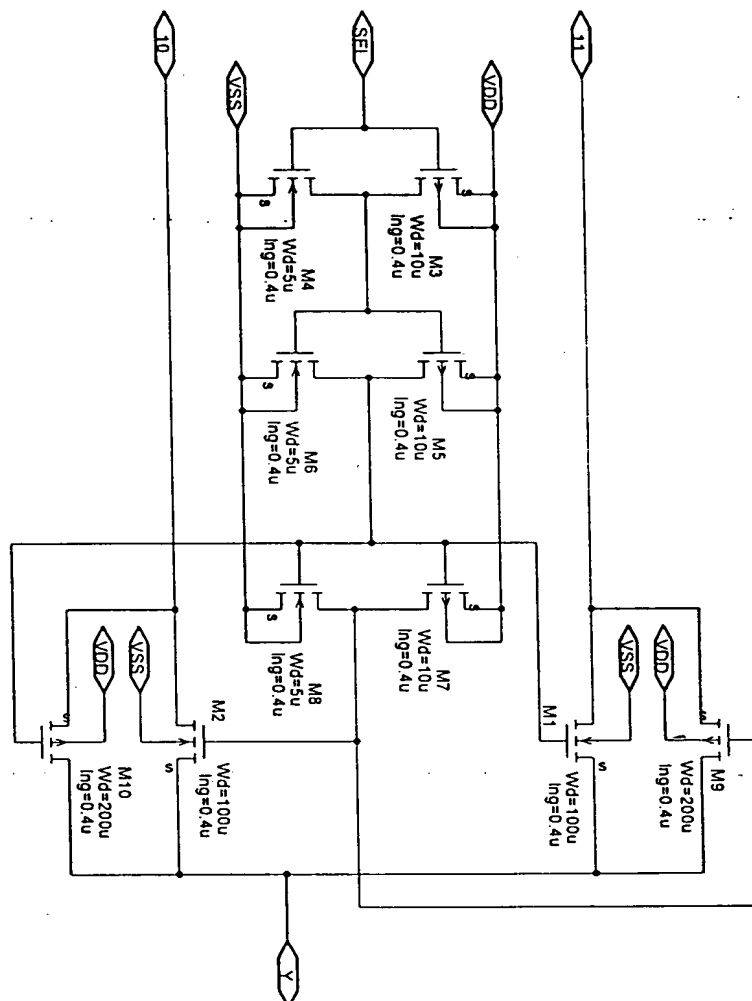


FIG. 129

FIG. 129 is a schematic diagram of a differential amplifier circuit. The circuit includes a differential pair of MOSFETs (M1, M2) with a tail current source (M3). The gates of M1 and M2 are connected to a common-mode feedback network (M4, M5) and a differential-mode feedback network (M6, M7). The output nodes are connected to a load network (M8, M9) and a differential-mode feedback network (M10, M11). The circuit is biased by a current source (M12) and a resistor (R1). The output is taken from the differential-mode feedback network (M10, M11).



Fe 130

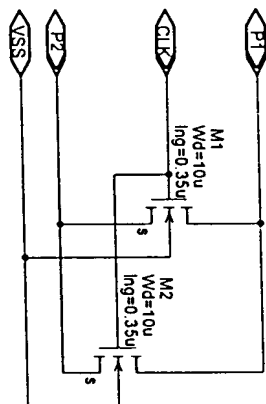


Fig. 131

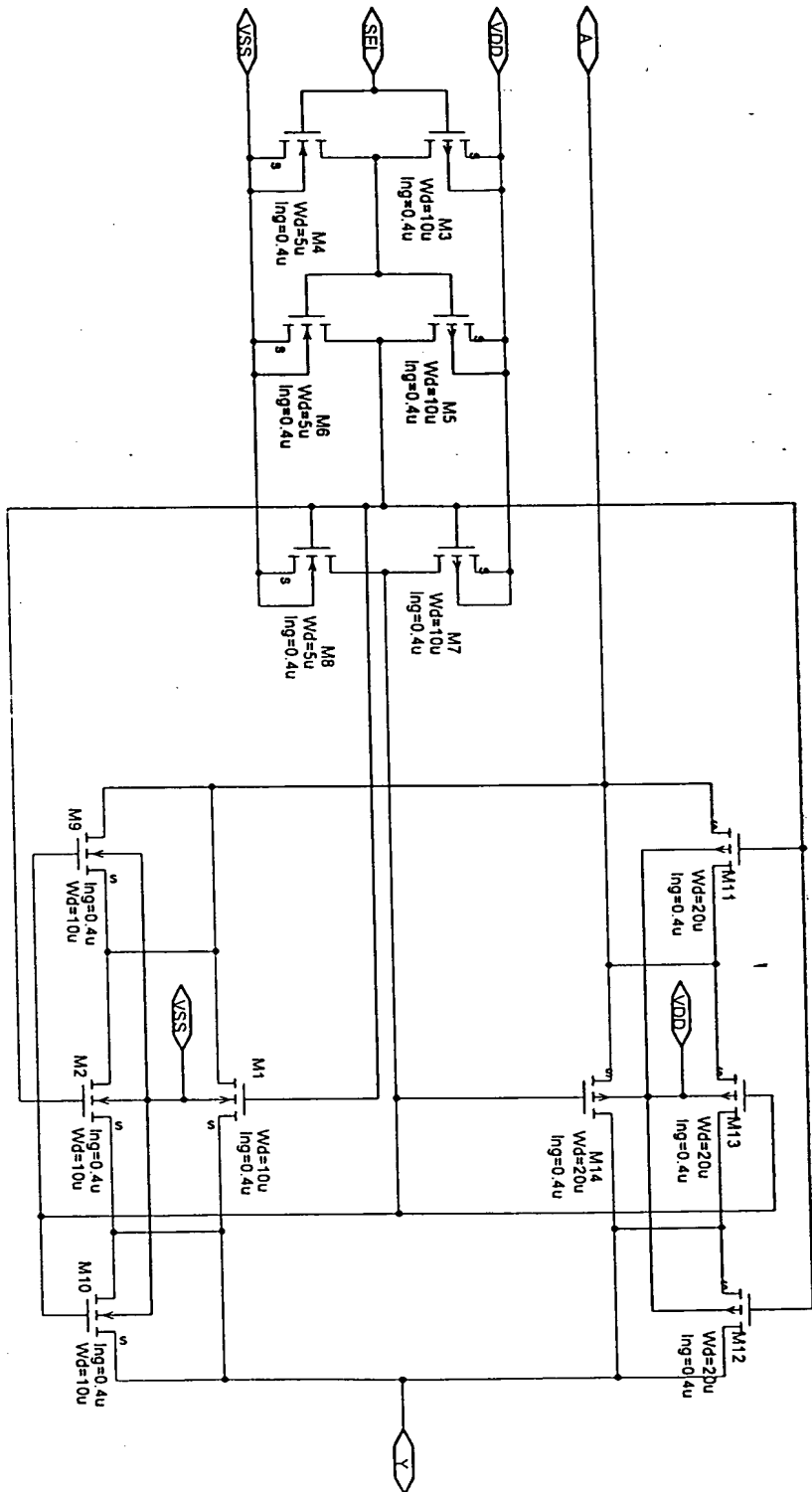


Fig. 132



1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100

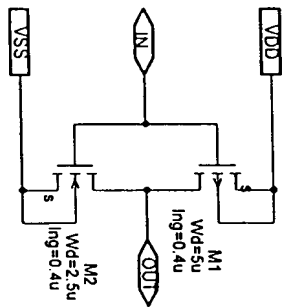
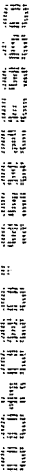


FIG. 138

FIG. 138 is a schematic diagram of a CMOS inverter circuit. The input (IN) is connected to the gates of both NMOS transistor M1 and PMOS transistor M2. The source of M1 is connected to VSS, and the source of M2 is connected to VDD. The drains of both transistors are connected together to form the output (OUT). Transistor M1 has parameters $W/d=5u$ and $l_{ng}=0.4u$. Transistor M2 has parameters $W/d=2.5u$ and $l_{ng}=0.4u$.



F. 1. 139

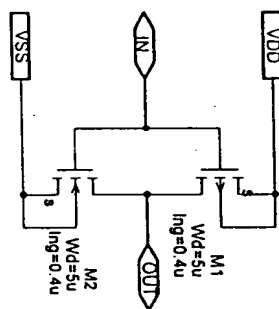
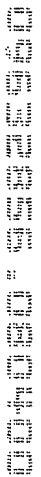
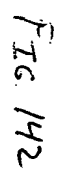


Fig. 140



f_{IG} . 141



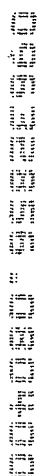
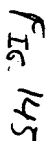


Fig 1413

[illegible]

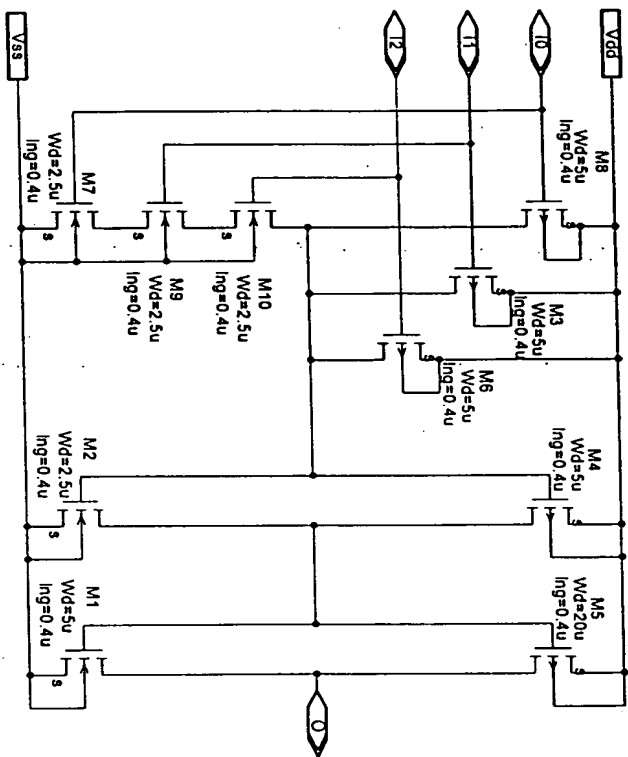
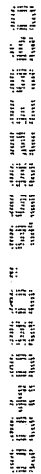


Fig. 14/6



Γ_{IG} . 147

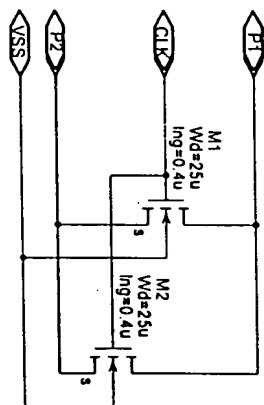


Fig. 148

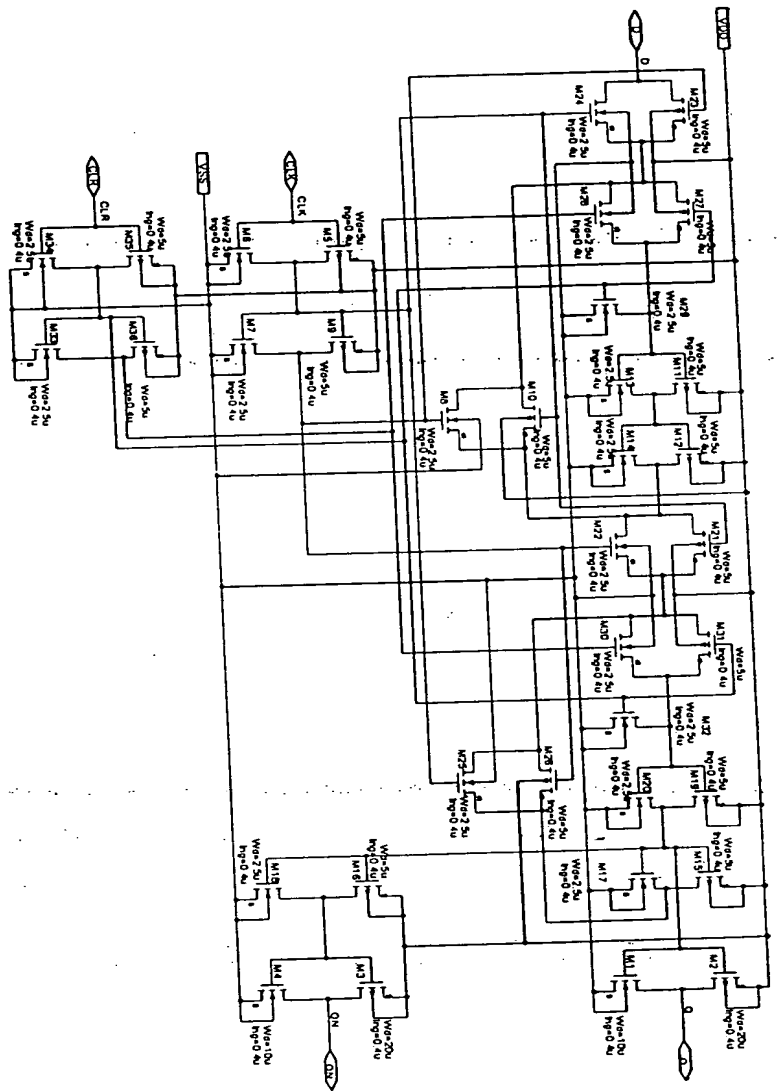


Fig. 150

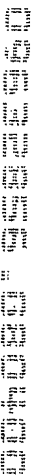


Fig. 151

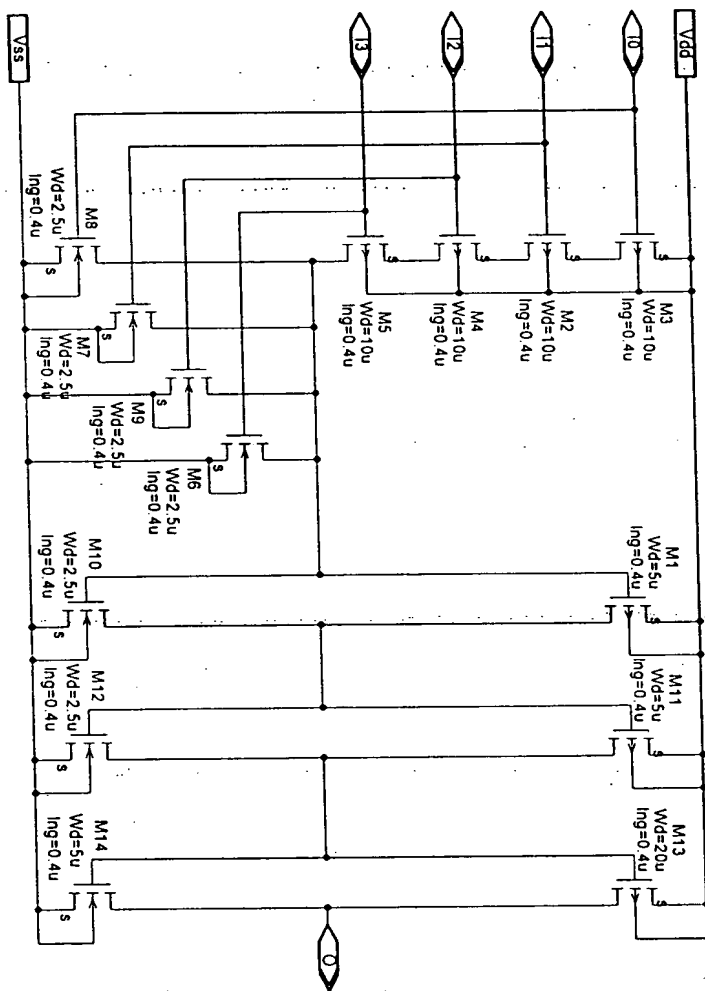
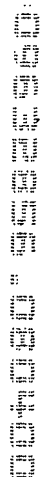


Fig. 152



File. 153

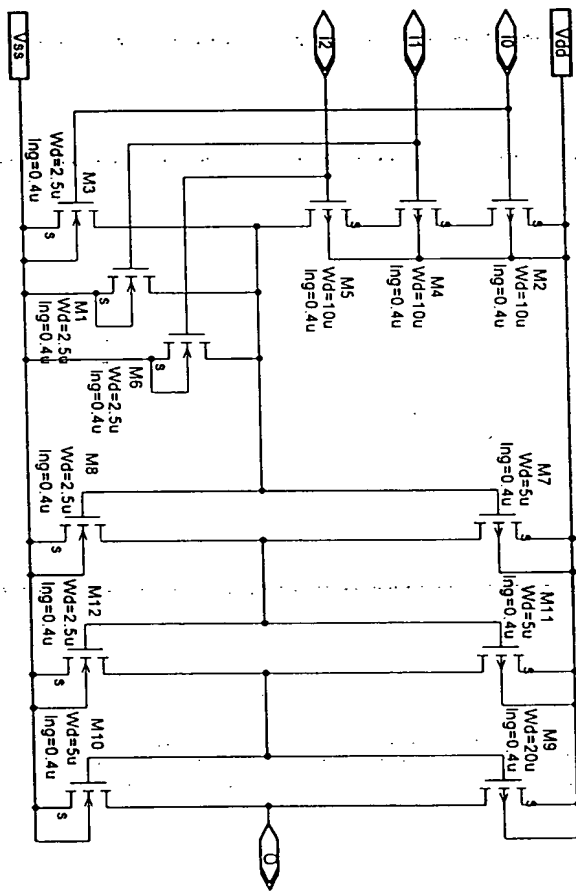
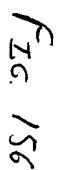


Fig 154



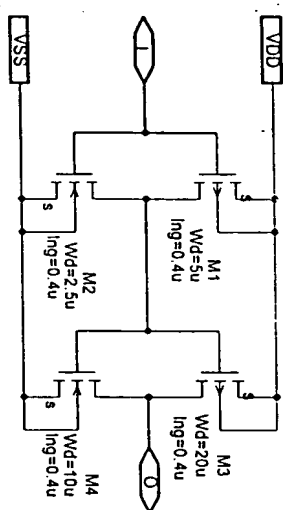


Fig. 158

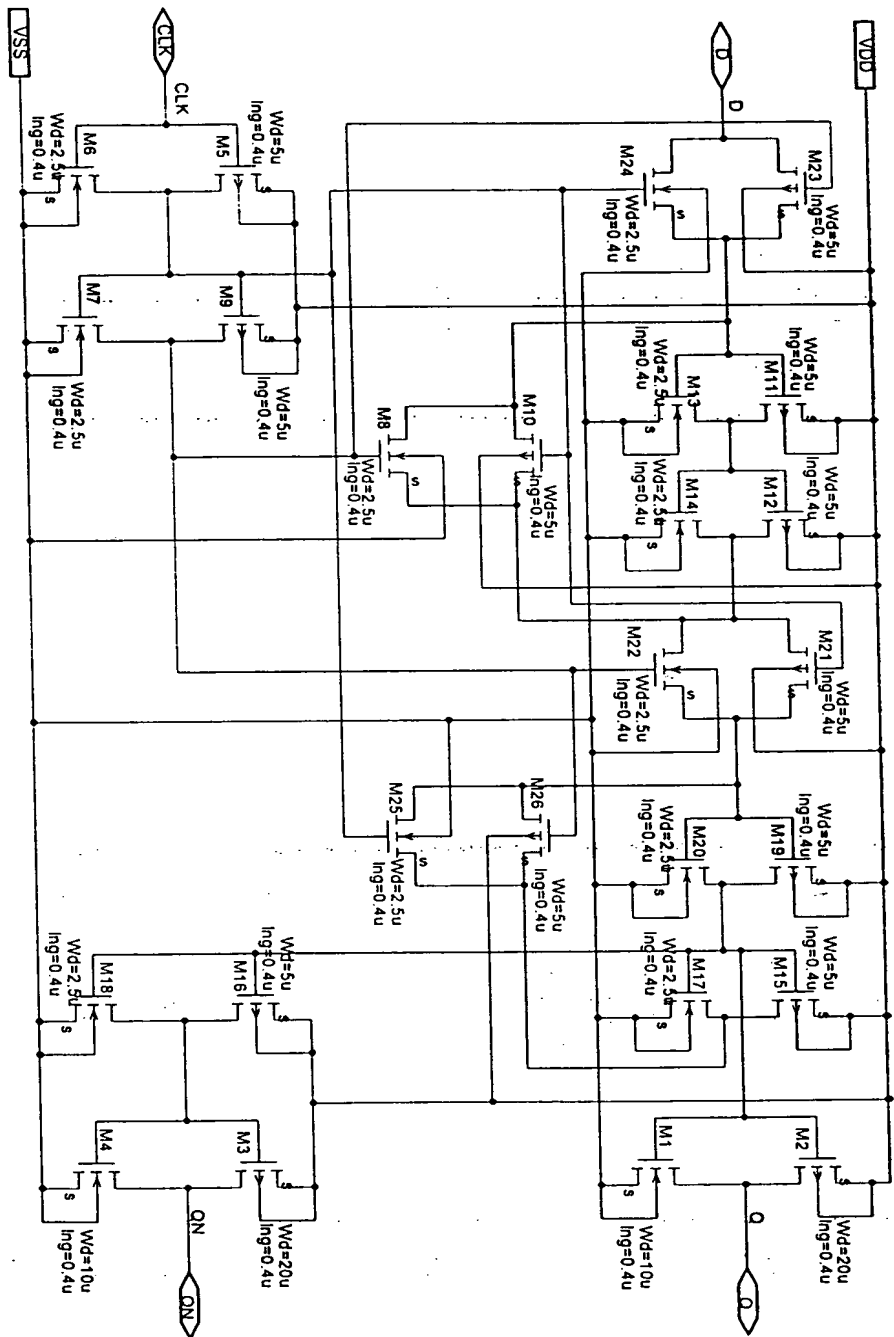


FIG 159

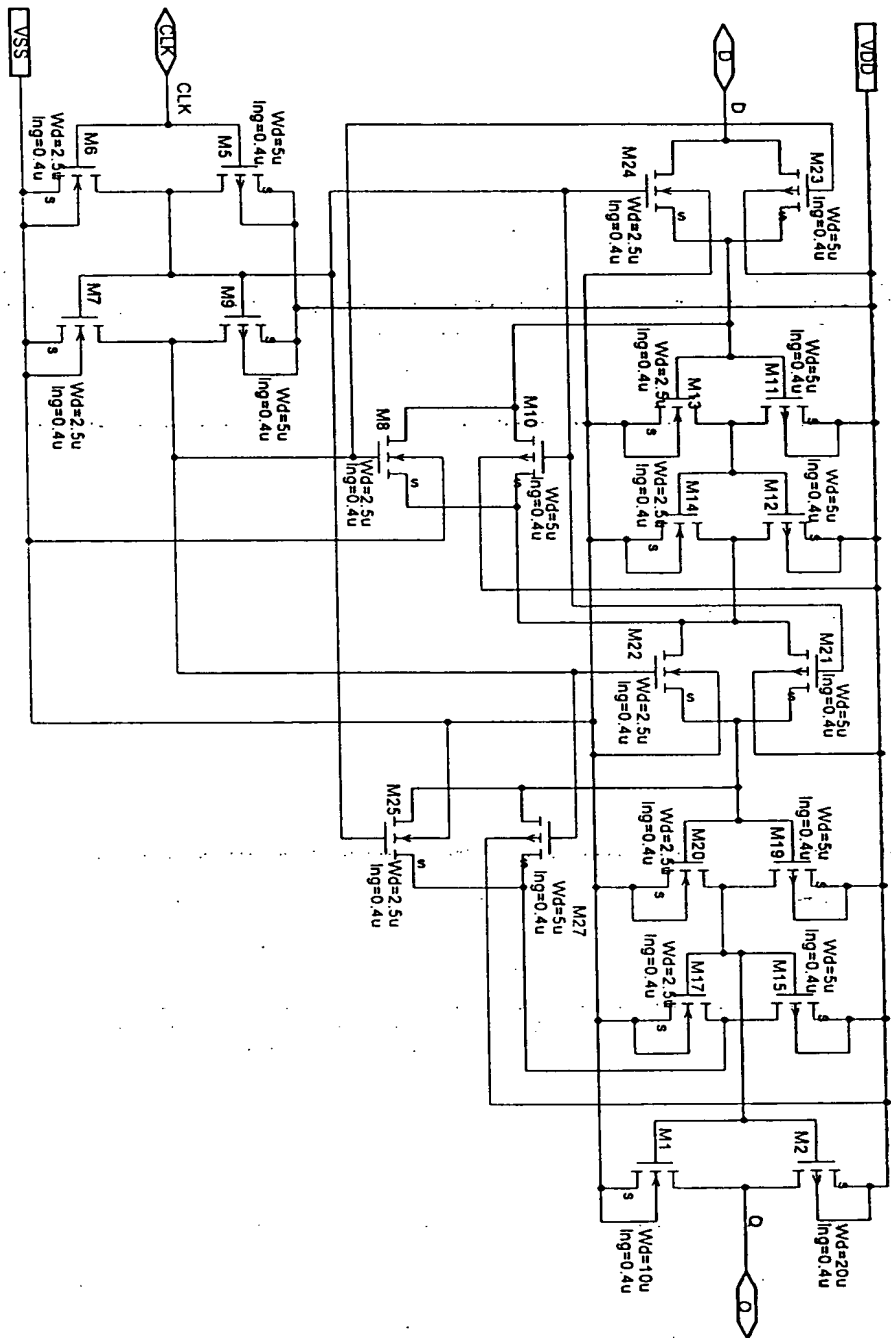
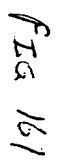


Fig. 160



1. The first group of people who are not in the majority are those who are not in the majority.